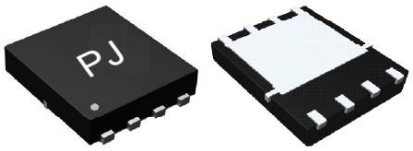
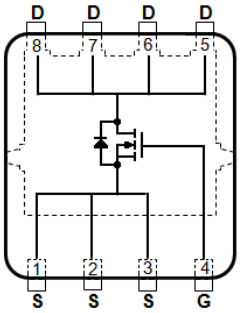
PJQ5474A

100V N-Channel Enhancement Mode MOSFET

Voltage	100 V	Current	18A

DFN5060-8L





Features

- $R_{DS(ON)}$, V_{GS}@10V, ID@18A<50mΩ
- $R_{DS(ON)}$, V_{GS}@4.5V, ID@15A<55mΩ
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: DFN5060-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0028 ounces, 0.08 grams
- Marking: Q5474A

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current		I_D	18	A
Pulsed Drain Current		I_{DM}	36	A
Single Pulse Avalanche Energy (Note 5)		E_{AS}	16.2	mJ
Power Dissipation	T_C=25°C	P_D	52	W
	Derate above 25°C		416	mW/°C
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient, t<10s (Note 3)		$R_{\theta JC}$	2.4	°C/W

PJQ5474A

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V,I_D=250uA	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	1.0	1.5	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V,I_D=18A	-	37	50	mΩ
		V_{GS}=4.5V,I_D=15A	-	38	55	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=80V,V_{GS}=0V	-	0.03	1.0	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	-	±10	±100	nA
Dynamic (Note 7)						
Total Gate Charge	Q_g	V_{DS}=80V, I_D=18A, V_{GS}=10V (Note 1,2)	-	61	-	nC
Gate-Source Charge	Q_{gs}		-	8.8	-	
Gate-Drain Charge	Q_{gd}		-	11	-	
Input Capacitance	Ciss	V_{DS}=15V, V_{GS}=0V, f=1.0MHZ	-	3555	-	pF
Output Capacitance	Coss		-	119	-	
Reverse Transfer Capacitance	Crss		-	56	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=50V, I_D=18A, V_{GS}=10V, R_G=3.3Ω (Note 1,2)	-	16	-	ns
Turn-On Rise Time	tr		-	50	-	
Turn-Off Delay Time	$td_{(off)}$		-	64	-	
Turn-Off Fall Time	tf		-	18	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	18	A
Diode Forward Voltage	V_{SD}	I_S=1.0A, V_{GS}=0V	-	0.7	1.2	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
5. The test condition is L=0.1mH, I_{AS}=18A, V_{DD}=25V, V_{GS}=10V
6. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 $inch^2$ with 2oz.square pad of copper.
7. Guaranteed by design, not subject to production testing.

PJQ5474A

TYPICAL CHARACTERISTIC CURVES

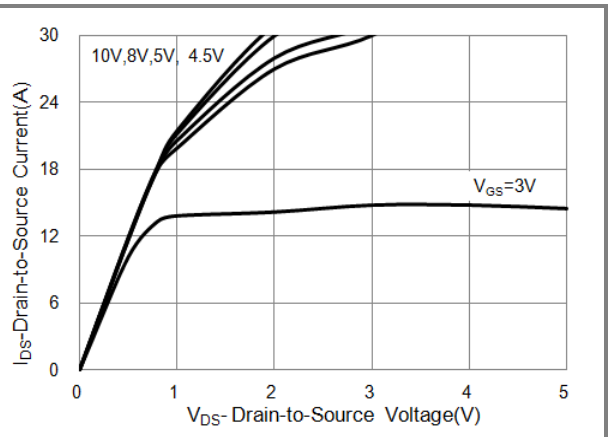


Fig.1 On-Region Characteristics

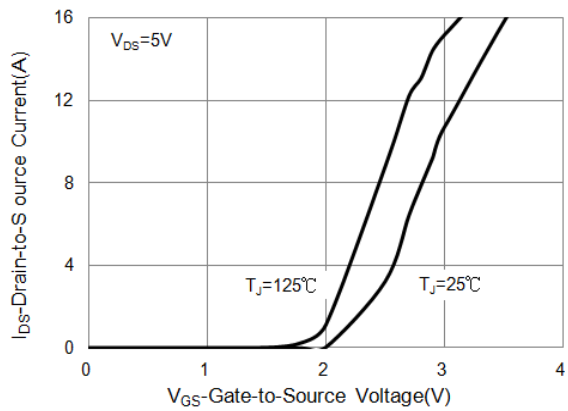


Fig.2 Transfer Characteristics

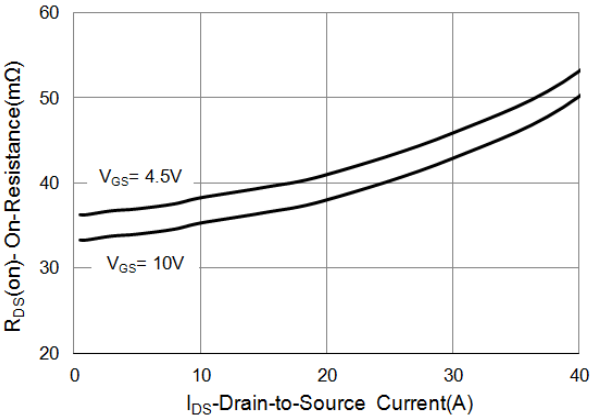


Fig.3 On-Resistance vs. Drain Current

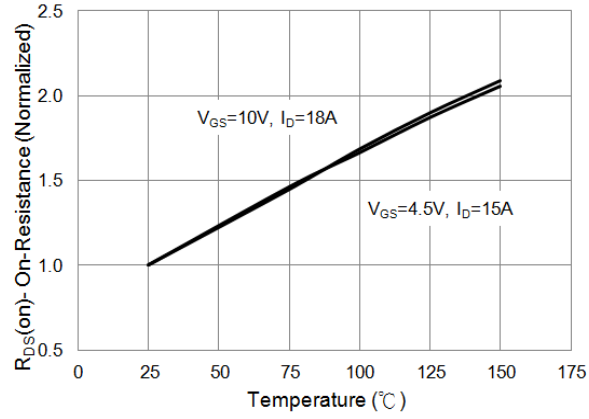


Fig.4 On-Resistance vs. Junction temperature

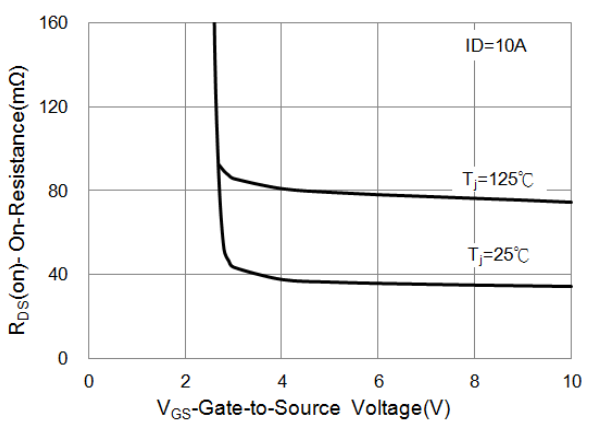


Fig.5 On-Resistance Variation with VGS.

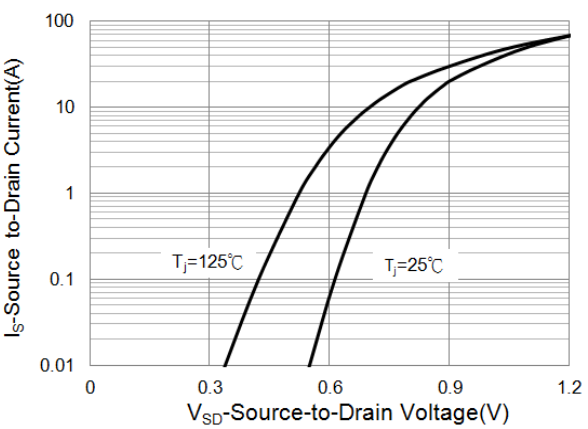


Fig.6 Body Diode Characteristics

PJQ5474A

TYPICAL CHARACTERISTIC CURVES

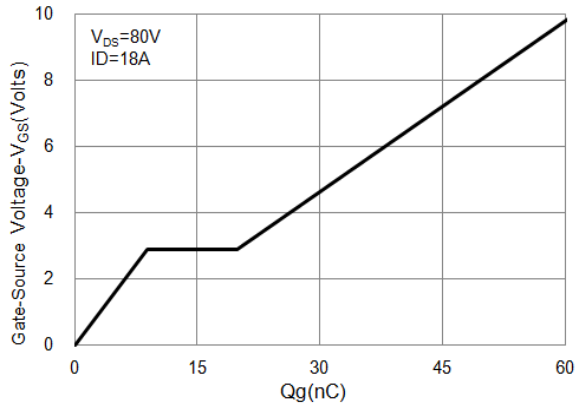


Fig.7 Gate-Charge Characteristics

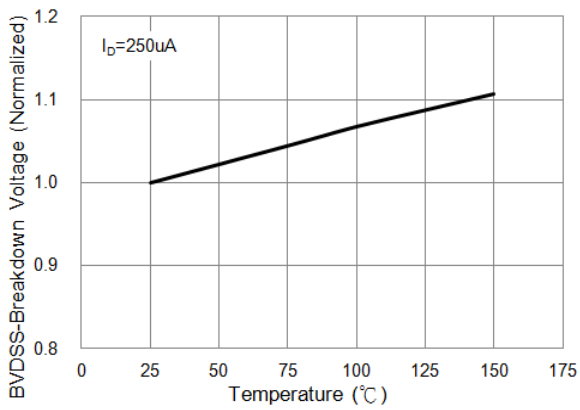


Fig.8 Breakdown Voltage Variation vs. Temperature

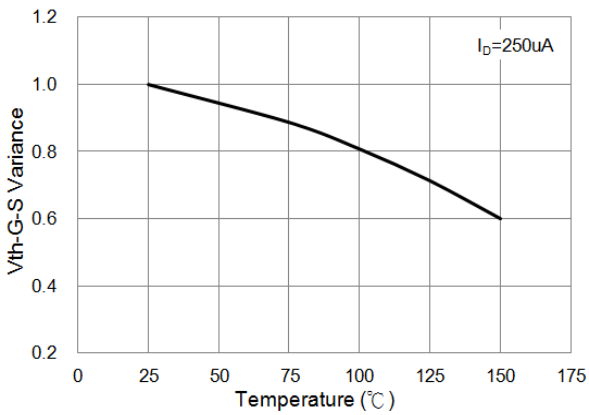


Fig.9 Threshold Voltage Variation with Temperature.

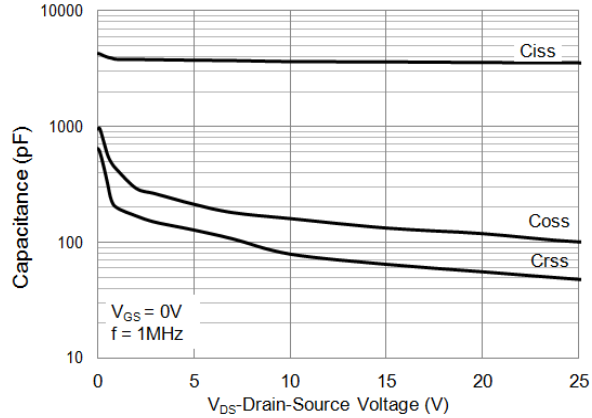


Fig.10 Capacitance vs. Drain-Source Voltage.

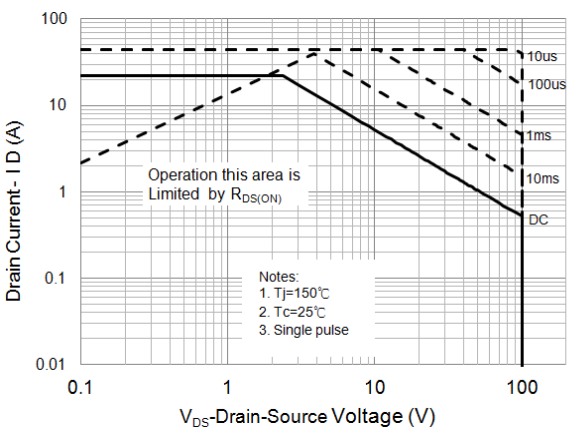


Fig.11 Maximum Safe Operating Area

PJQ5474A

TYPICAL CHARACTERISTIC CURVES

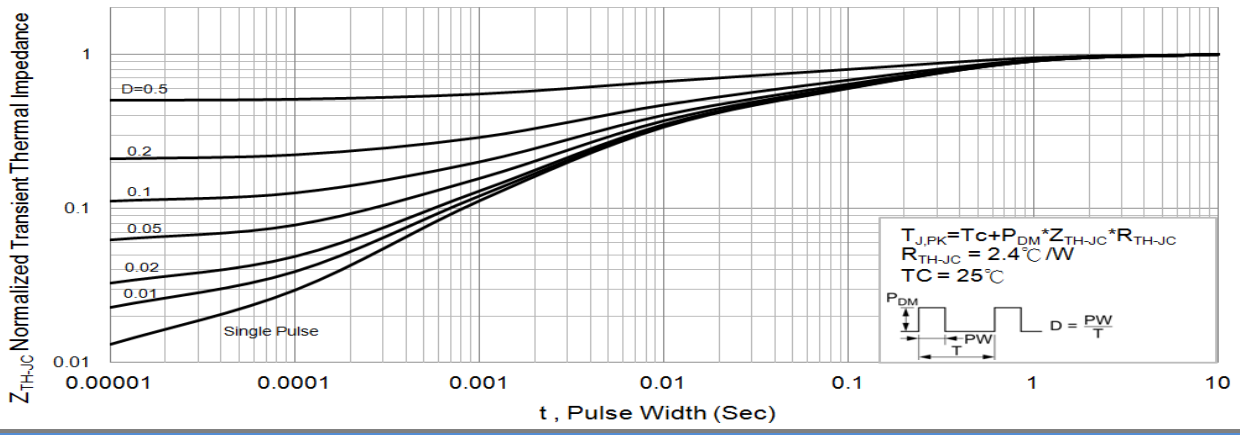


Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

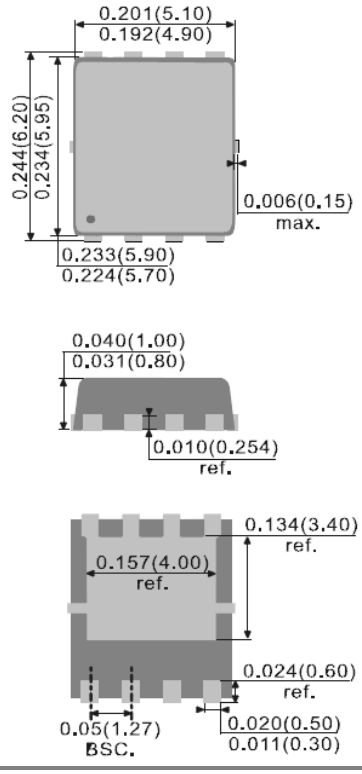
PJQ5474A

PART NO PACKING CODE VERSION

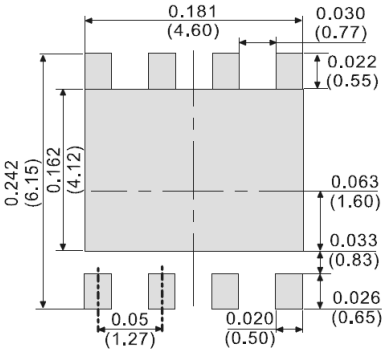
Part No Packing Code	Package Type	Packing type	Marking	Version
PJQ5474A_R2_00001	DFN5060-8L	3000pcs / 13” reel	Q5474A	Halogen free

Packaging Information & Mounting Pad Layout

DFN5060-8L Dimension — Unit: mm



DFN5060-8L PAD LATOUT — Unit: mm



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