



30V P-Channel Enhancement Mode MOSFET

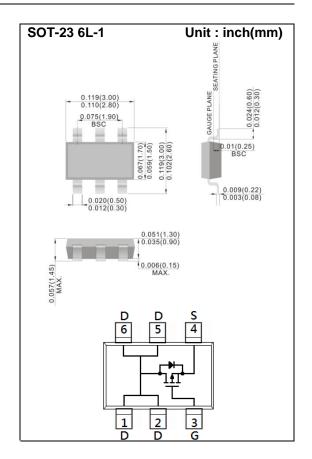
Voltage -30 V Current -4.6A

Features

- RDS(ON) , VGS@-10V, ID@-4.6A<71mΩ
- RDS(ON), VGS@-4.5V, ID@-3.3A<81m Ω
- RDS(ON) , VGS@-2.5V, ID@-1.8A<110mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: SOT-23 6L-1 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: S01



Maximum Ratings and Thermal Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	<u>+</u> 12	V
Continuous Drain Current		I _D	-4.6	Α
Pulsed Drain Current		I _{DM}	-18.4	А
Power Dissipation	T _a =25°C	P _D	2	W
	Derate above 25°C		16	mW/°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal resistance				
- Junction to Ambient (Note 3)		$R_{ hetaJA}$	62.5	°C/W





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D =-250uA	-30	-	ı	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-0.5	-0.96	-1.3	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-4.6A	-	60	71	mΩ
		V _{GS} =-4.5V, I _D =-3.3A	-	67	81	
		V _{GS} =-2.5V, I _D =-1.8A	-	84	110	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Q_g	V _{DS} =-15V, I _D =-4.6A, V _{GS} =-10V ^(Note 1,2)	-	15.5	ı	nC
Gate-Source Charge	Q_gs		-	1.5	1	
Gate-Drain Charge	Q_{gd}		-	2.2	ı	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V,	-	637	1	pF
Output Capacitance	Coss		-	50	1	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	35	1	
Switching						
Turn-On Delay Time	td _(on)	\/ A5\/ A0A	-	3	ı	ns
Turn-On Rise Time	tr	V _{DD} =-15V, I _D =-4.6A,	-	43	-	
Turn-Off Delay Time	td _(off)	V_{GS} =-10V, R_G =6 Ω (Note 1,2)	-	224	-	
Turn-Off Fall Time	tf		-	101	ı	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	-2.0	А
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _{GS} =0V		-0.75	-1.2	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





TYPICAL CHARACTERISTIC CURVES

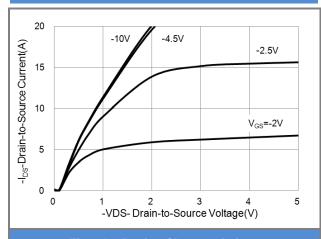


Fig.1 On-Region Characteristics

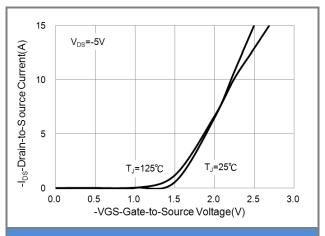


Fig.2 Transfer Characteristics

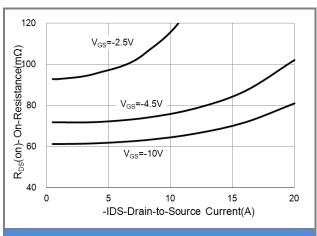


Fig.3 On-Resistance vs. Drain Current

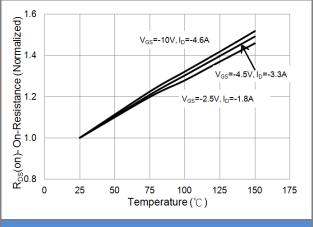


Fig.4 On-Resistance vs. Junction temperature

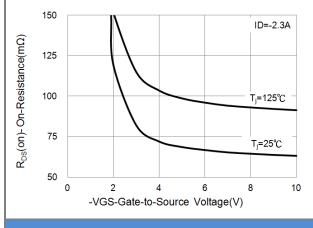
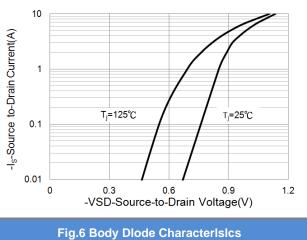


Fig.5 On-Resistance Variation with VGS.







TYPICAL CHARACTERISTIC CURVES

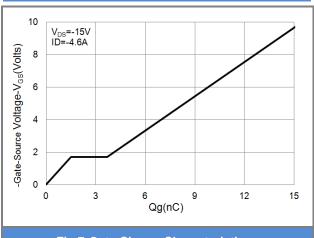


Fig.7 Gate-Charge Characteristics

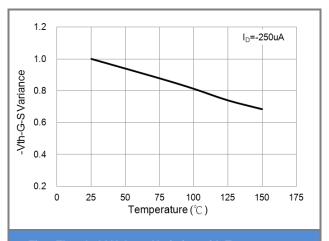


Fig.8 Threshold Voltage Variation with Temperature.

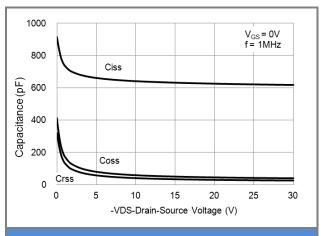


Fig.9 Capacitance vs. Drain-Source Voltage.

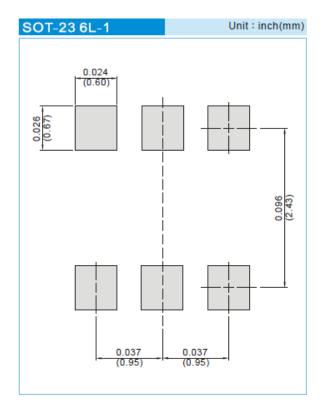




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJS6401_S1_00001	SOT-23 6L-1	3K pcs / 7" reel	S01	Halogen free

MOUNTING PAD LAYOUT







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