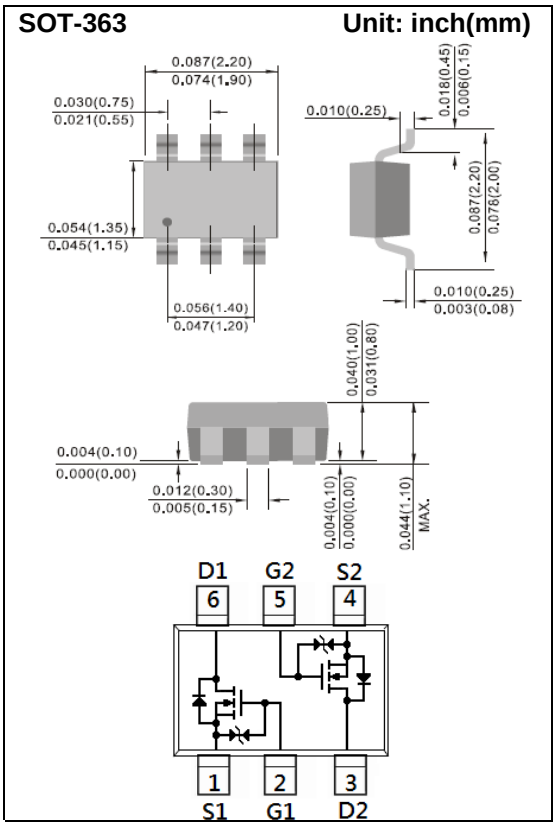
PJT7802

20V N-Channel Enhancement Mode MOSFET – ESD Protected

Voltage	20 V	Current	0.5A



Features

- RDS(ON) , VGS@4.5V, ID@0.5A<0.4Ω
- RDS(ON) , VGS@2.5V, ID@0.3A<0.7Ω
- RDS(ON) , VGS@1.8V, ID@0.1A<1.2Ω(typ.)
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.(Halogen Free)

Mechanical Data

- Case : SOT-363 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0002 ounces, 0.006 grams
- Marking : T02

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current		I_D	0.5	A
Pulsed Drain Current (Note 4)		I_{DM}	2.0	A
Power Dissipation	T_a=25°C	P_D	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient (Note 3)		$R_{\theta JA}$	357	°C/W

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250uA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	0.4	0.63	1.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=4.5V,I_D=0.5A	-	0.32	0.4	Ω
		V_{GS}=2.5V,I_D=0.3A	-	0.6	0.7	
		V_{GS}=1.8V,I_D=0.1A	-	1.2	-	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=16V, V_{GS}=0V	-	0.02	1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±10V, V_{DS}=0V	-	±2	±10	uA
Dynamic (Note 5)						
Total Gate Charge	Q_g	V_{DS}=10V, I_D=0.5A, V_{GS}=4.5V (Note 1,2)	-	0.9	-	nC
Gate-Source Charge	Q_{gs}		-	0.3	-	
Gate-Drain Charge	Q_{gd}		-	0.1	-	
Input Capacitance	Ciss	V_{DS}=10V, V_{GS}=0V, f=1.0MHZ	-	39	-	pF
Output Capacitance	Coss		-	18	-	
Reverse Transfer Capacitance	Crss		-	9	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=10V, I_D=0.5A, V_{GS}=4.5V, R_G=6Ω (Note 1,2)	-	3	-	ns
Turn-On Rise Time	tr			22	-	
Turn-Off Delay Time	$td_{(off)}$			7	-	
Turn-Off Fall Time	tf		-	19	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	0.4	A
Diode Forward Voltage	V_{SD}	I_S=0.5A, V_{GS}=0V		0.91	1.3	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited
5. Guaranteed by design, not subject to production testing

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TYPICAL CHARACTERISTIC CURVES

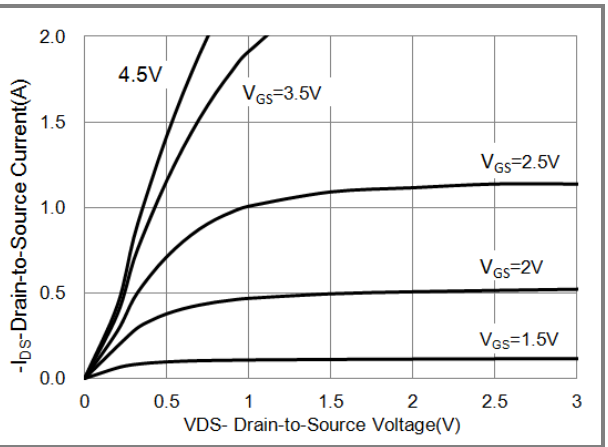

Fig.1 On-Region Characteristics

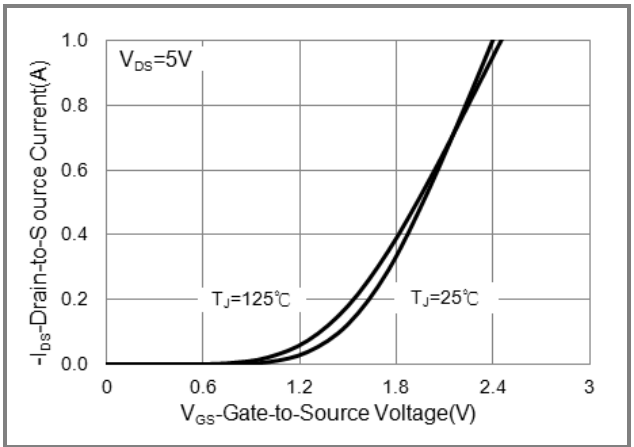

Fig.2 Transfer Characteristics

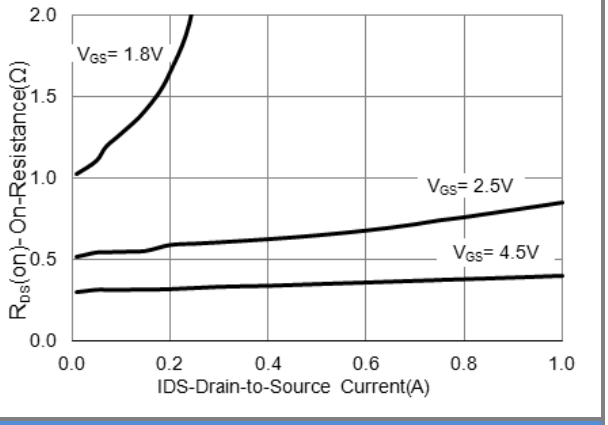

Fig.3 On-Resistance vs. Drain Current

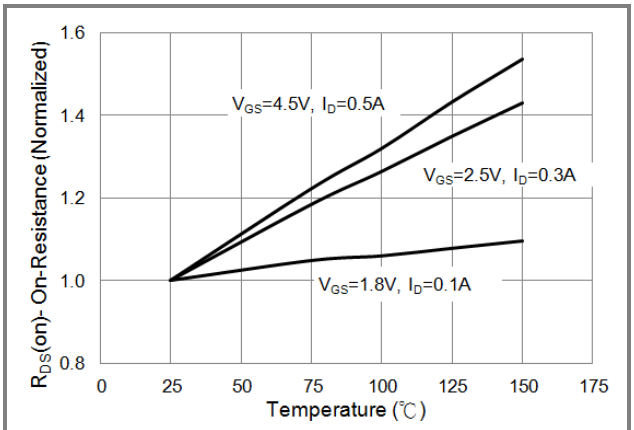

Fig.4 On-Resistance vs. Junction temperature

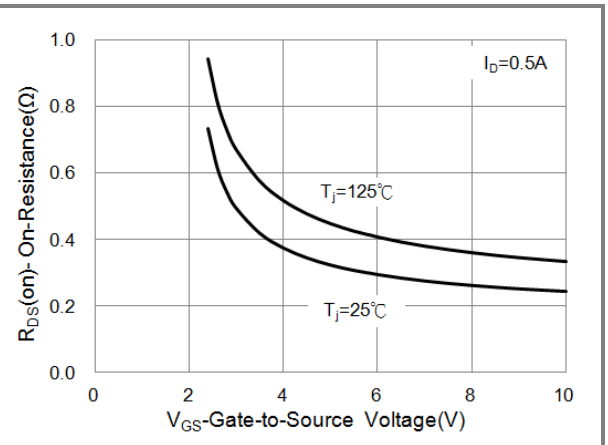

Fig.5 On-Resistance Variation with VGS.

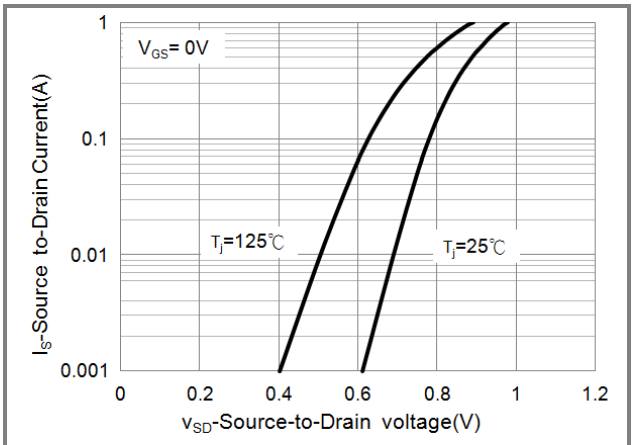

Fig.6 Body Diode Characteristics

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TYPICAL CHARACTERISTIC CURVES

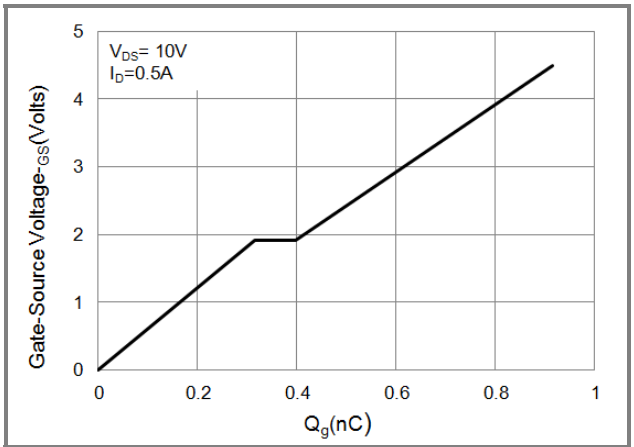

Fig.7 Gate-Charge Characteristics

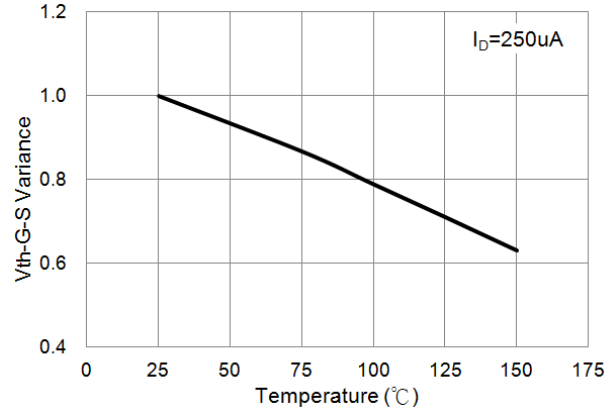

Fig.8 Threshold Voltage Variation with Temperature.

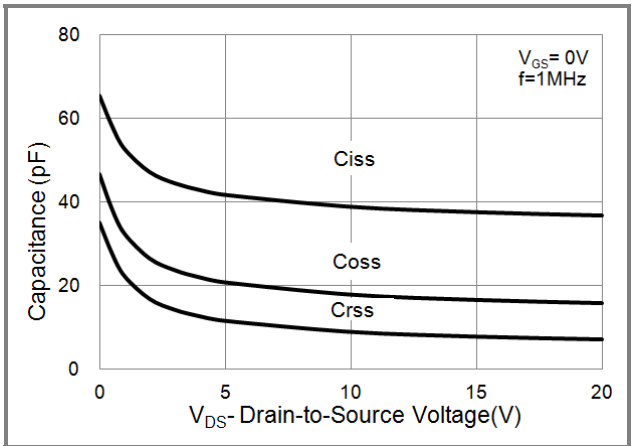

Fig.9 Threshold Voltage Variation with Temperature.

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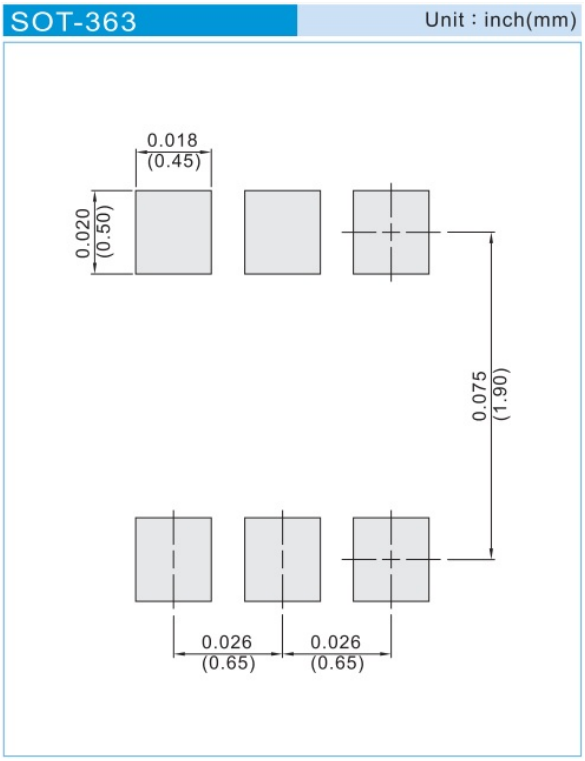
PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJT7802_R1_00001	SOT-363	3K pcs / 7” reel	T02	Halogen free
PJT7802_R2_00001	SOT-363	10K pcs / 13” reel	T02	Halogen free

MOUNTING PAD LAYOUT



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