



60V P-Channel Enhancement Mode MOSFET

Voltage

-60 V

Current

-250mA

Features

- RDS(ON), VGS@-10V, ID@-500mA<4Ω
- RDS(ON), VGS@-4.5V, ID@-200mA<6Ω
- RDS(ON), VGS@-2.5V, ID@-50mA<13Ω
- Advanced Trench Process Technology
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

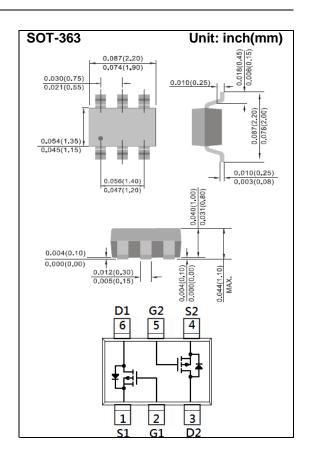
Mechanical Data

Case: SOT-363 Package

Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0002 ounces, 0.006 grams

Marking: T39



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	-60	V
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V
Continuous Drain Current		I _D	-250	mA
Pulsed Drain Current		I _{DM}	-1000	mA
Power Dissipation	T _A =25°C	P _D	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		T_{J}, T_{STG}	-55~150	°C
Typical Thermal resistance				
- Junction to Ambient (Note 3)		$R_{\theta JA}$	357	°C/W





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-60	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1.0	-1.5	-2.5	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V,I _D =-500mA	-	2.4	4	Ω	
		V _{GS} =-4.5V,I _D =-200mA	-	2.65	6		
		V _{GS} =-2.5V,I _D =-50mA	-	4.5	13		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-48V,V _{GS} =0V	-	-	-1	uA	
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA	
Dynamic (Note 4)							
Total Gate Charge	Q_{g}	V _{DS} =-25V, I _D =-100mA, V _{GS} =-4.5V	-	1.1	-	nC	
Gate-Source Charge	Q_gs		-	0.3	-		
Gate-Drain Charge	Q_gd		-	0.2	-		
Input Capacitance	Ciss	V _{DS} =-25V, V _{GS} =0V, f=1.0MHZ	-	51	-	pF	
Output Capacitance	Coss		-	15	-		
Reverse Transfer Capacitance	Crss	I=I.UIVIMZ	-	2.2	-		
Turn-On Delay Time	td _(on)		-	4.8	-		
Turn-On Rise Time	tr	$\begin{array}{c} V_{DD}\text{=-}25\text{V}, \ I_{D}\text{=-}100\text{mA}, \\ V_{GS}\text{=-}10\text{V}, \\ R_{G}\text{=-}6\Omega \end{array}$	-	19	-		
Turn-Off Delay Time	td _(off)		-	52	-		
Turn-Off Fall Time	tf		-	32	-		
Drain-Source Diode							
Maximum Continuous Drain-Source					050	mA	
Diode Forward Current	I _S		-	-	-250		
Diode Forward Voltage	V_{SD}	I _S =-500mA, V _{GS} =0V	-	-0.95	-1.3	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
- 4. Guaranteed by design, not subject to production testing





TYPICAL CHARACTERISTIC CURVES

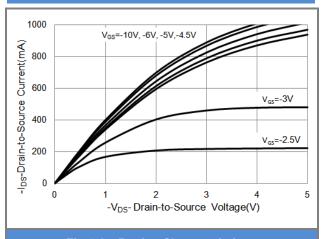


Fig.1 On-Region Characteristics

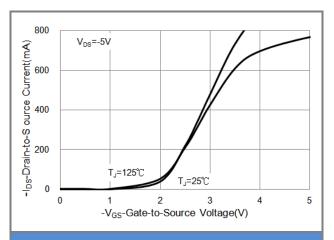


Fig.2 Transfer Characteristics

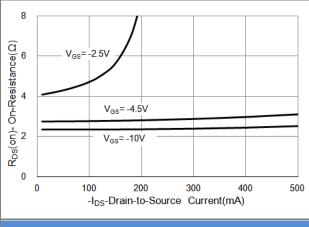


Fig.3 On-Resistance vs. Drain Current

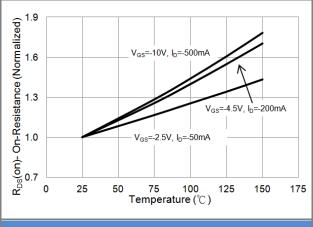


Fig.4 On-Resistance vs. Junction temperature

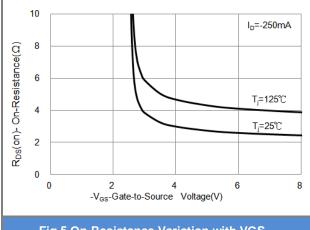
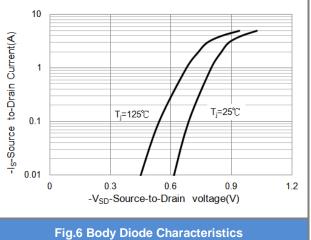


Fig.5 On-Resistance Variation with VGS.







TYPICAL CHARACTERISTIC CURVES

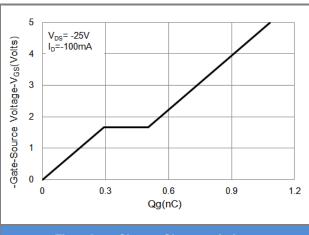


Fig.7 Gate-Charge Characteristics

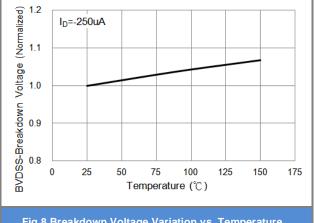


Fig.8 Breakdown Voltage Variation vs. Temperature

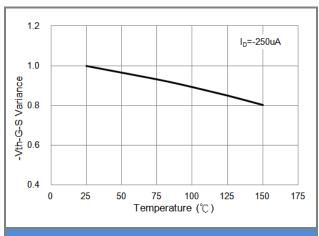


Fig.9 Threshold Voltage Variation with Temperature.

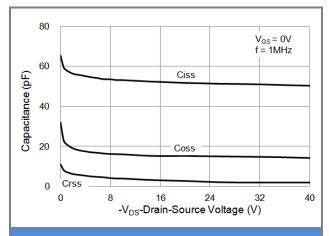


Fig.10 Capacitance vs. Drain-Source Voltage.

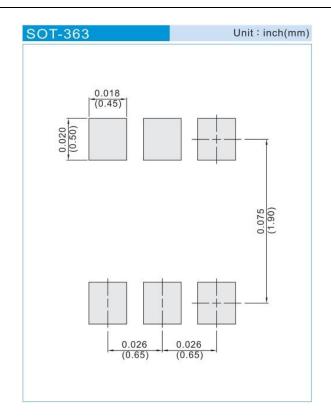




PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing type	Marking	Version
PJT7839_R1_00001	SOT-363	3K pcs / 7" reel	T39	Halogen free
PJT7839 _R2_00001	SOT-363	10K pcs / 13" reel	T39	Halogen free

MOUNTING PAD LAYOUT







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