

# PJW4P06A

## 60V P-Channel Enhancement Mode MOSFET

**Voltage**

**-60 V**

**Current**

**-4.0 A**

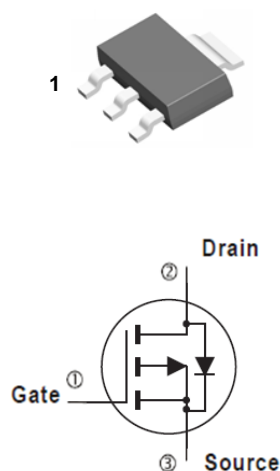
### Features

- $R_{DS(ON)}$ ,  $V_{GS}@-10V, I_D@-4.0A < 110m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@-4.5V, I_D@-2.0A < 130m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.  
(Halogen Free)

### Mechanical Data

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.043 ounces, 0.123 grams
- Marking: W4P06A

SOT-223



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	-60	V
Gate-Source Voltage		V <sub>GS</sub>	±20	V
Continuous Drain Current	T <sub>A</sub> =25°C	I <sub>D</sub>	-4	A
	T <sub>A</sub> =70°C		-3.2	
Pulsed Drain Current <sup>(Note 1)</sup>		I <sub>DM</sub>	-16	A
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	3.1	W
	T <sub>A</sub> =70°C		2	
Single Pulse Avalanche Energy <sup>(Note 5)</sup>		E <sub>AS</sub>	12.8	mJ
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55~150	°C
Typical Thermal resistance		R <sub>θJA</sub>	40.3	°C/W
- Junction to Ambient <sup>(Note 6)</sup>				

- Limited only By Maximum Junction Temperature



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## Electrical Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1.0	-1.7	-2.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.0A	-	87	110	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.0A	-	110	130	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V	-	-	-1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Dynamic (Note 7)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-30V, I <sub>D</sub> =-4.0A, V <sub>GS</sub> =-10V (Note 1,2)	-	10	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.6	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	3	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, f=1.0MHZ	-	785	-	pF
Output Capacitance	C <sub>oss</sub>		-	175	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	112	-	
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DS</sub> =-30V, RL=30Ω V <sub>GS</sub> =-10V, R <sub>G</sub> =6.2Ω (Note 1,2)	-	8	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	15	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	43	-	
Turn-Off Fall Time	t <sub>f</sub>		-	8.4	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	-4	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	-	-0.76	-1.0	V

### NOTES :

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. Repetitive rating, pulse width limited by junction temperature T<sub>J</sub>(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> = 25°C.
5. The test condition is L=0.1mH, I<sub>AS</sub>=-16A, V<sub>DD</sub>=-25V, V<sub>GS</sub>=-10V
6. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
7. Guaranteed by design, not subject to production testing.

# PJW4P06A

## TYPICAL CHARACTERISTIC CURVES

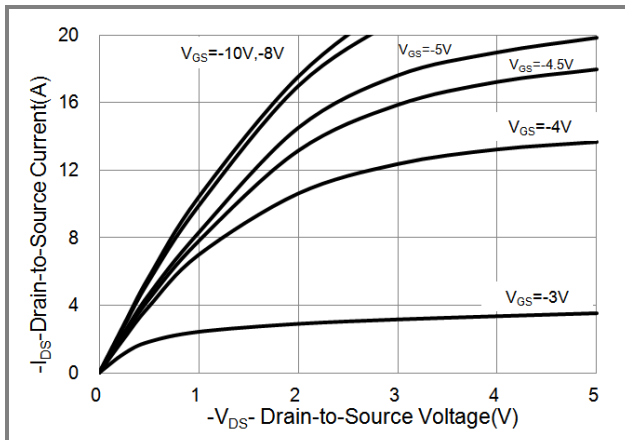


Fig.1 Output Characteristics

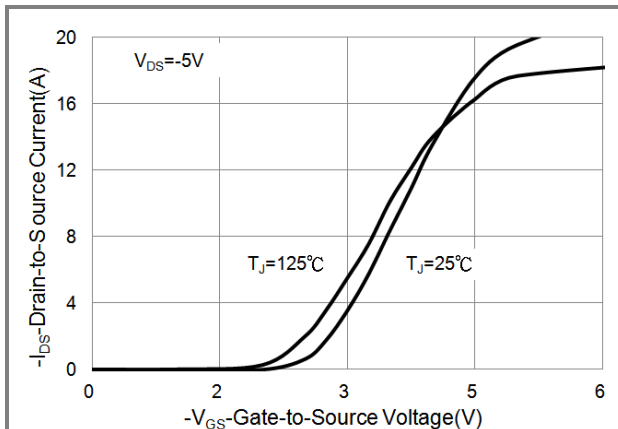


Fig.2 Transfer Characteristics

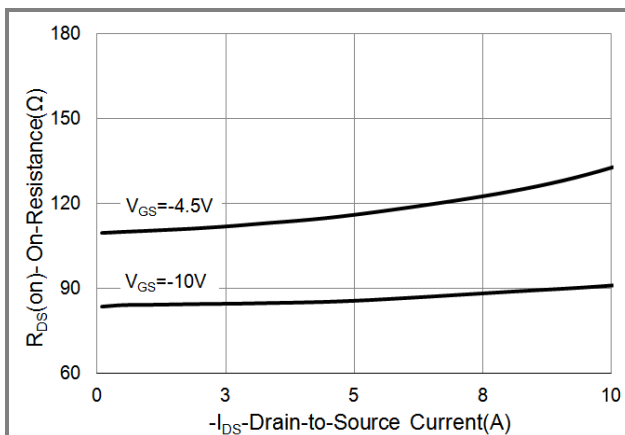


Fig.3 On-Resistance vs. Drain Current

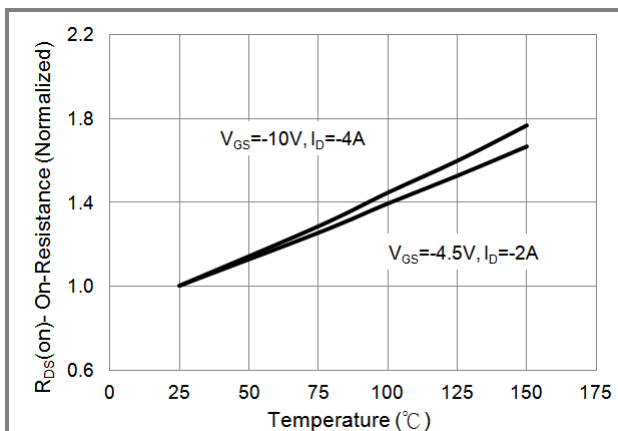


Fig.4 On-Resistance vs. Junction temperature

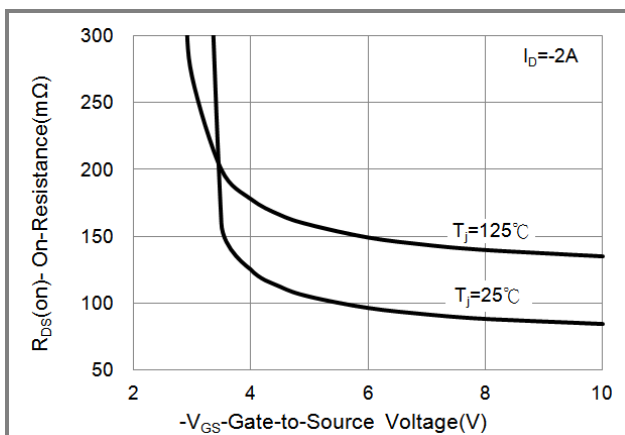


Fig.5 On-Resistance Variation with V\_GS.

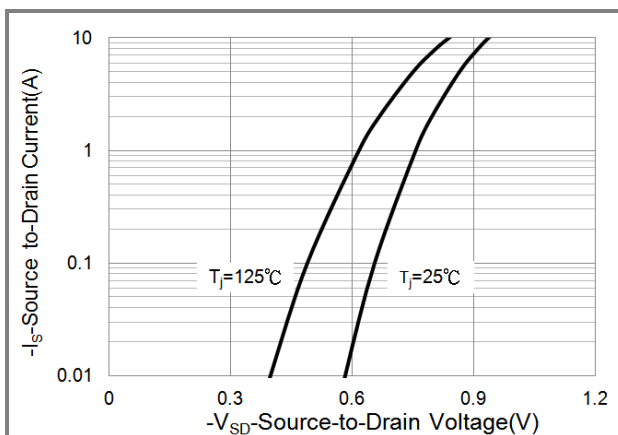
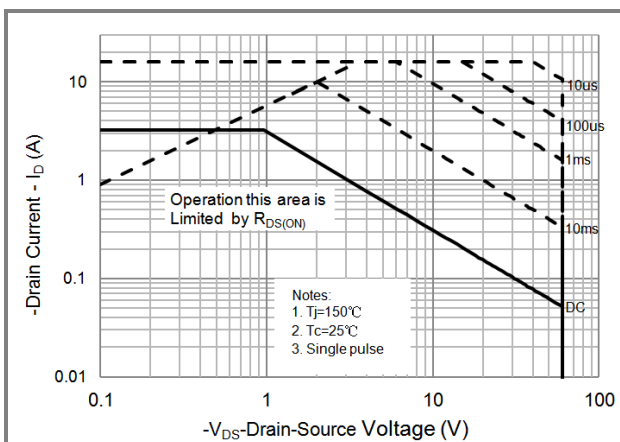
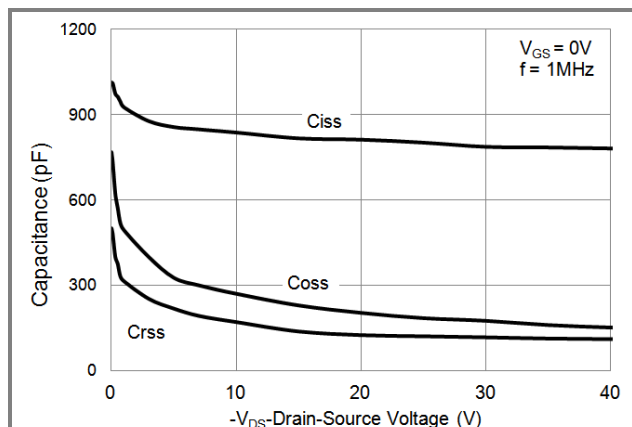
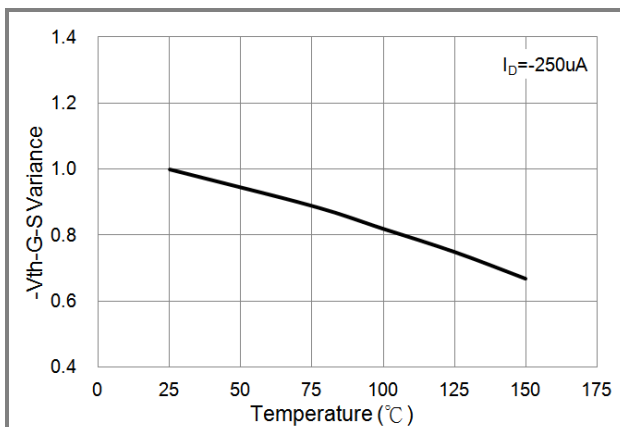
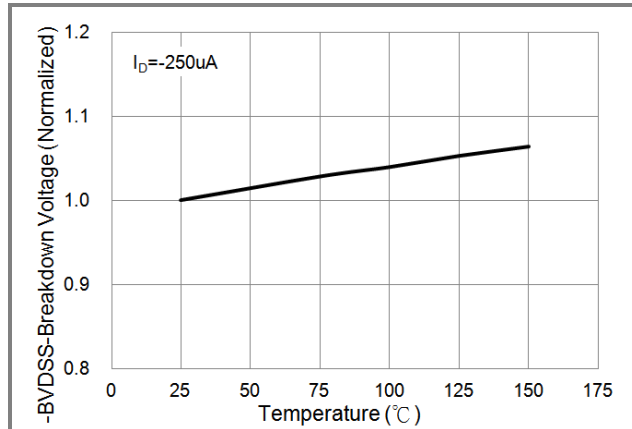
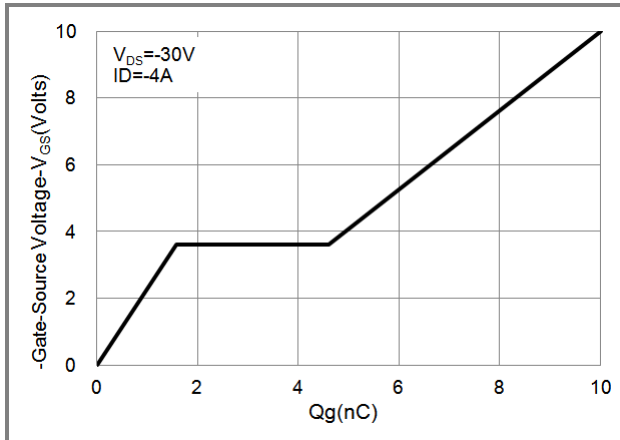


Fig.6 Source-Drain Diode Forward Voltage

## PJW4P06A

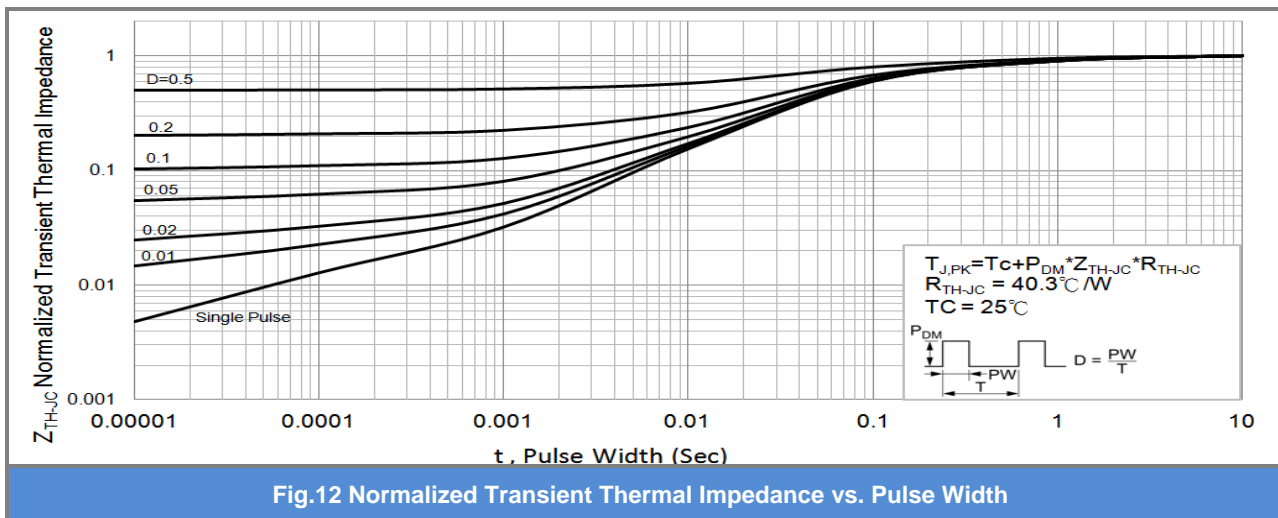
### TYPICAL CHARACTERISTIC CURVES





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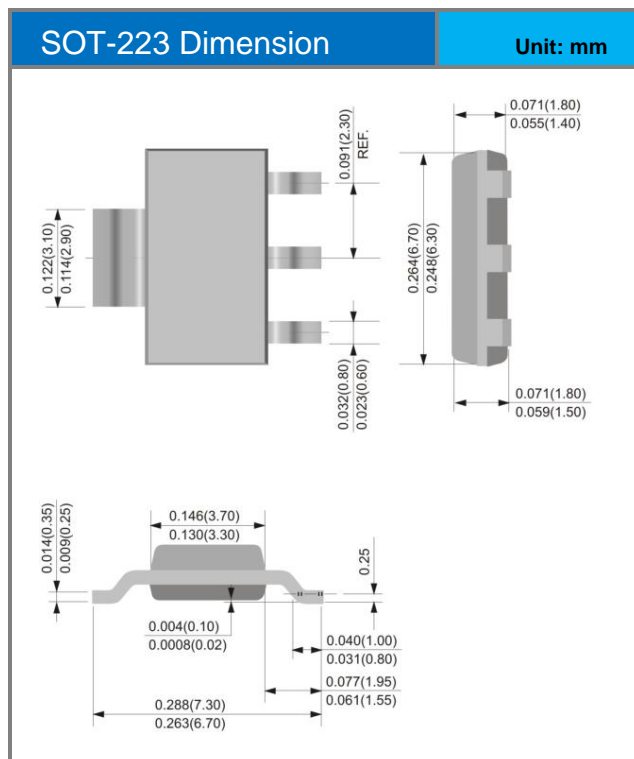
### TYPICAL CHARACTERISTIC CURVES





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## Packaging Information

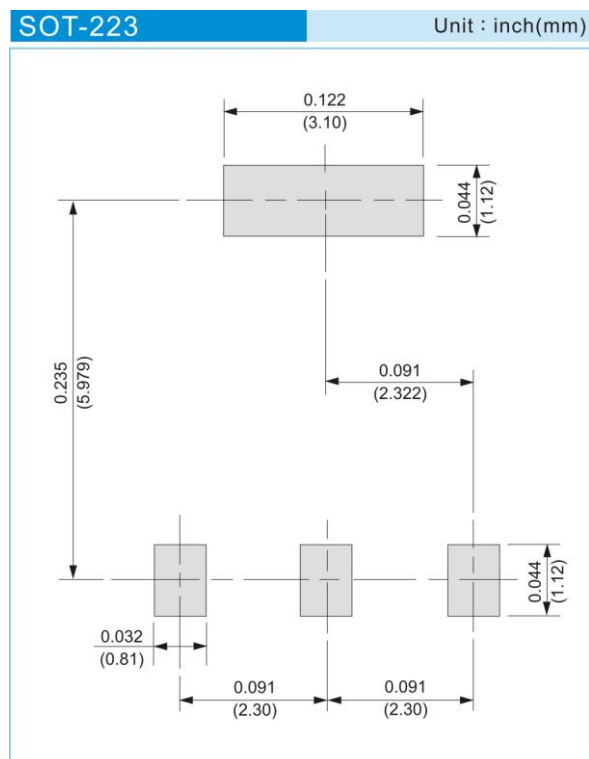


# PJW4P06A

## PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJW4P06A_R2_00001	SOT-223	2,500pcs / 13" reel	W4P06A	Halogen free

## MOUNTING PAD LAYOUT





## PJW4P06A

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