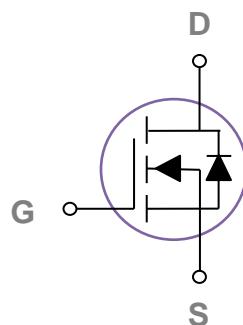


### General Description

These N-Channel enhancement mode power field effect transistors are using Super Junction technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply

### TO247 Pin Configuration



BVDSS	RDS(ON)	ID
650V	69mΩ	53A

### Features

- 53A, 650V,  $RDS(ON) = 69m\Omega$  @  $VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- High efficient switched mode power supplies
- LED Lighting
- Adapter/charger

### Absolute Maximum Ratings ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	53	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	33.5	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	212	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	344	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	8.3	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	390	W
	Power Dissipation – Derate above $25^\circ C$	3.125	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.32	$^\circ C/W$



650V N-Channel MOSFETs

PJX53N65D

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =1mA	650	---	---	V
△BV <sub>DSS</sub> /△T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	---	0.48	---	V/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DSON</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =26.5A	---	55	69	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =1mA	2	---	4	V
△V <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	-8.83	---	mV/°C

**Dynamic and switching Characteristics**

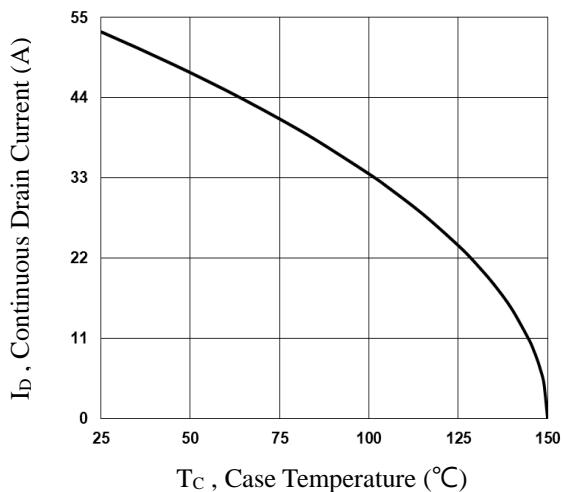
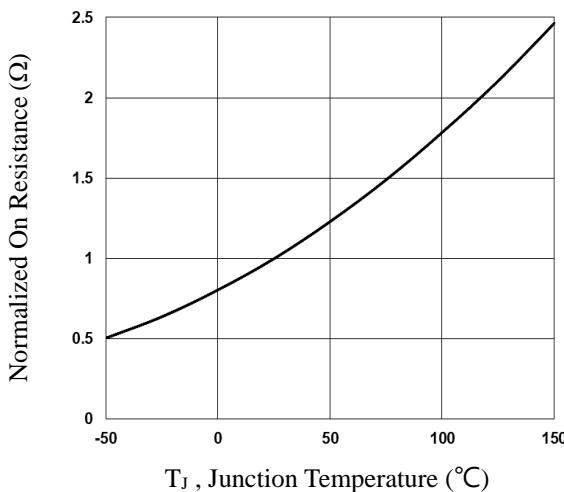
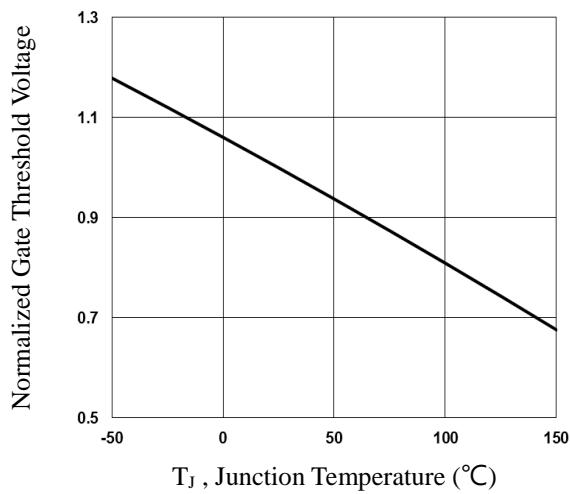
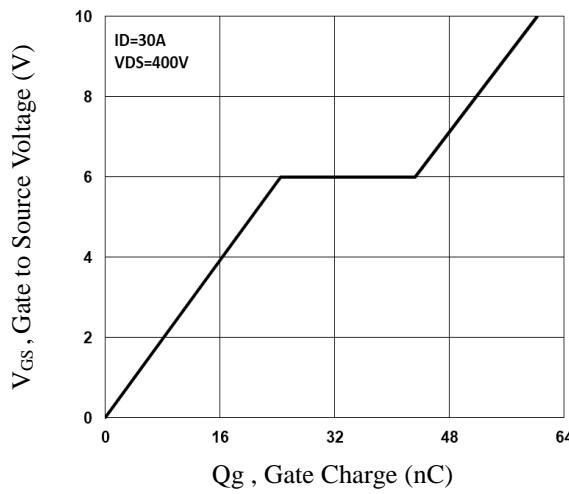
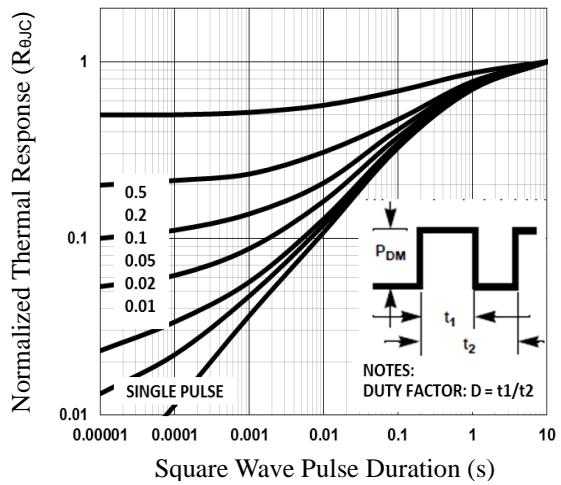
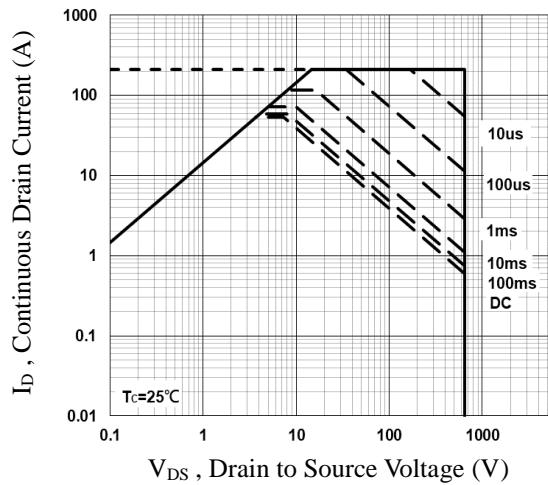
Q <sub>g</sub>	Total Gate Charge <sup>2,3</sup>	V <sub>DS</sub> =400V, V <sub>GS</sub> =10V, I <sub>D</sub> =30A	---	60.2	---	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>2,3</sup>		---	24.5	---	
Q <sub>gd</sub>	Gate-Drain Charge <sup>2,3</sup>		---	18.7	---	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2,3</sup>	V <sub>DD</sub> =400V, V <sub>GS</sub> =10V, R <sub>G</sub> =25Ω I <sub>D</sub> =30A	---	129	---	ns
T <sub>r</sub>	Rise Time <sup>2,3</sup>		---	150	---	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2,3</sup>		---	100	---	
T <sub>f</sub>	Fall Time <sup>2,3</sup>		---	77	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, F=100KHz	---	4650	---	pF
C <sub>oss</sub>	Output Capacitance		---	280	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	8.6	---	

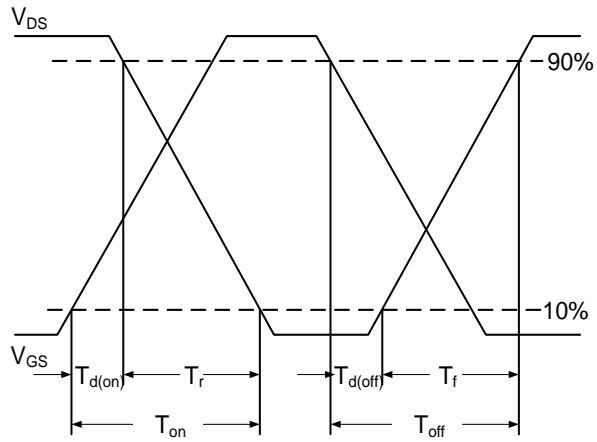
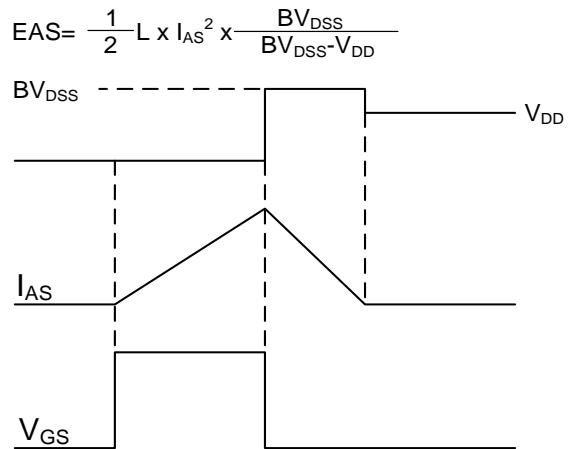
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>s</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	53	A
I <sub>SM</sub>	Pulsed Source Current		---	---	106	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>s</sub> =53A, T <sub>J</sub> =25°C	---	---	1.4	V
t <sub>rr</sub>	Reverse Recovery Time <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>s</sub> =30A, dI/dt=100A/μs, T <sub>J</sub> =25°C	---	174	---	ns
Q <sub>rr</sub>	Reverse Recovery Charge <sup>2</sup>		---	1.05	---	uC
I <sub>rrm</sub>	Peak reverse recovery current	T <sub>J</sub> =25°C	---	11.32	---	A

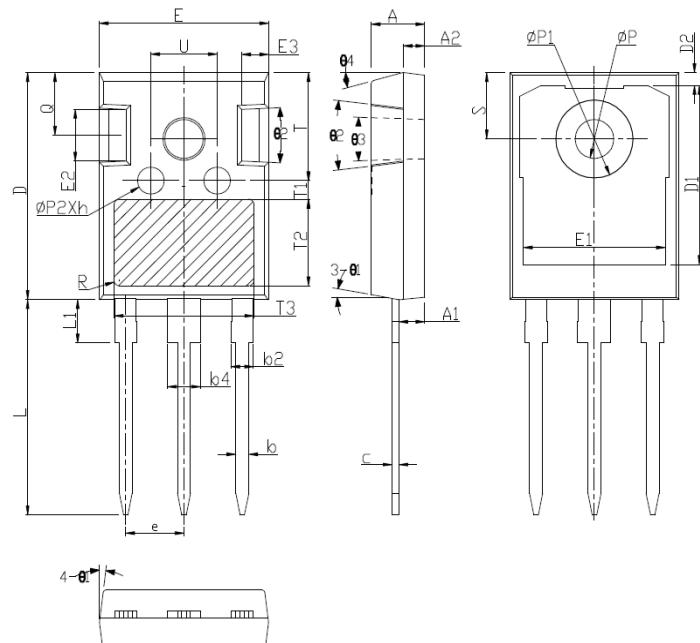
Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- V<sub>DD</sub>=100V, V<sub>GS</sub>=10V, L=10mH, I<sub>AS</sub>=8.3A., R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
- Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_c$** 

**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

## TO247 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters			Symbol	Dimensions In Millimeters		
	Min	Nom	Max		Min	Nom	Max
A	4.750	5.000	5.250	L	19.520	19.920	20.320
A1	2.160	2.410	2.660	L1	---	---	4.300
A2	1.850	2.000	2.150	ΦP	3.350	3.600	3.850
b	1.110	1.200	1.350	ΦP1	---	---	7.300
b2	1.900	2.010	2.250	ΦP2	2.250	2.500	2.750
b4	2.900	3.100	3.250	Q	5.500	5.800	6.100
c	0.510	0.610	0.750	S	6.15BSC		
D	20.600	21.000	21.400	R	0.50REF		
D1	16.150	16.550	16.950	T	9.700	---	10.300
D2	1.000	1.200	1.400	T1	1.65REF		
E	15.500	15.800	16.100	T2	8.00REF		
E1	13.000	13.300	13.600	T3	12.80REF		
E2	4.700	5.000	5.300	U	5.900	---	6.500
E3	2.250	2.500	2.750	θ1	3°	7°	10°
e	5.44BSC			θ2	2°	5°	8°
h	0.000	0.100	0.250	θ3	1°	---	2°
				θ4	10°	15°	20°