

### General Description

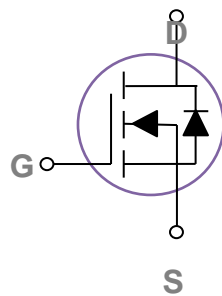
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
600V	65mΩ	60A

### Features

- 600V,60A,  $R_{DS(ON)} = 65m\Omega @ V_{GS} = 10V$
- Improved  $dv/dt$  capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### TO247 Pin Configuration



### Applications

- High efficient switched mode power supplies
- LED Lighting
- Adapter/charger

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	600	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	60	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	38	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	240	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	1800	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	10	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	312	W
	Power Dissipation – Derate above $25^\circ\text{C}$	2.5	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	40	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.4	$^\circ\text{C}/\text{W}$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	600	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =600V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	50	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	---	65	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2.5	---	4.5	V

**Dynamic and switching Characteristics**

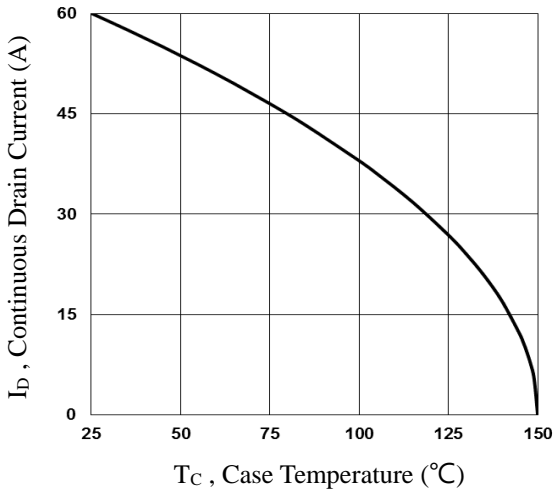
Q <sub>g</sub>	Total Gate Charge <sup>3, 4</sup>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =1.3A	---	160	---	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3, 4</sup>		---	25	---	
Q <sub>gd</sub>	Gate-Drain Charge <sup>3, 4</sup>		---	65	---	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>3, 4</sup>	V <sub>DD</sub> =30V, V <sub>GS</sub> =10V, R <sub>G</sub> =25Ω I <sub>D</sub> =0.5A	---	150	---	ns
T <sub>r</sub>	Rise Time <sup>3, 4</sup>		---	500	---	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>3, 4</sup>		---	1400	---	
T <sub>f</sub>	Fall Time <sup>3, 4</sup>		---	823	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, F=1MHz	---	4900	---	pF
C <sub>oss</sub>	Output Capacitance		---	2730	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	128	---	

**Drain-Source Diode Characteristics and Maximum Ratings**

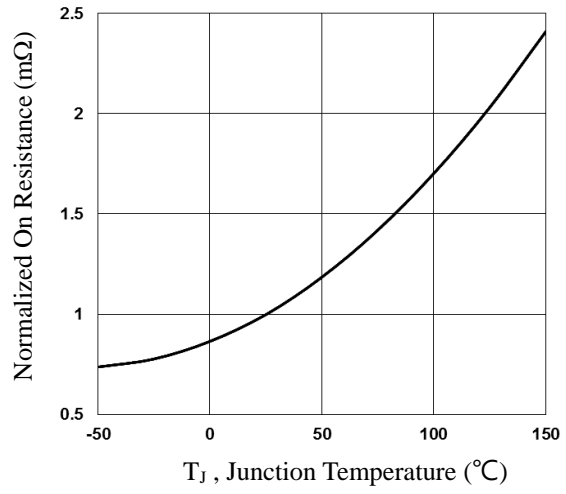
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	60	A
I <sub>SM</sub>	Pulsed Source Current		---	---	120	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =60A, T <sub>J</sub> =25°C	---	---	1.4	V
T <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>S</sub> =30A, V <sub>GS</sub> =0V, V <sub>R</sub> =200V	---	640	---	nS
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge	dI <sub>F</sub> /dt=100A/uS	---	14	---	uC

Note :

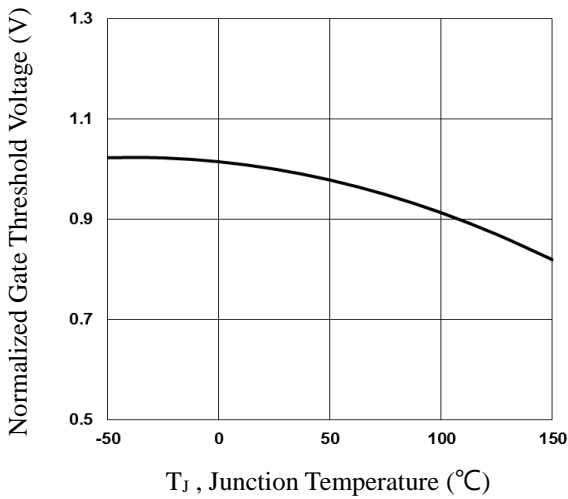
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=50V, V<sub>GS</sub>=10V, L=36mH, I<sub>AS</sub>=10A, R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.



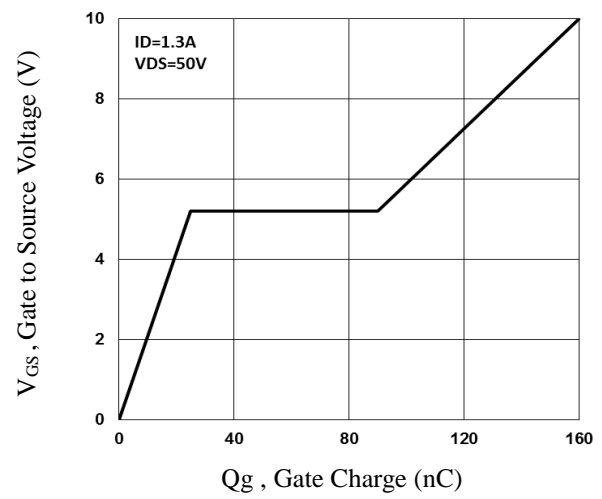
**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**



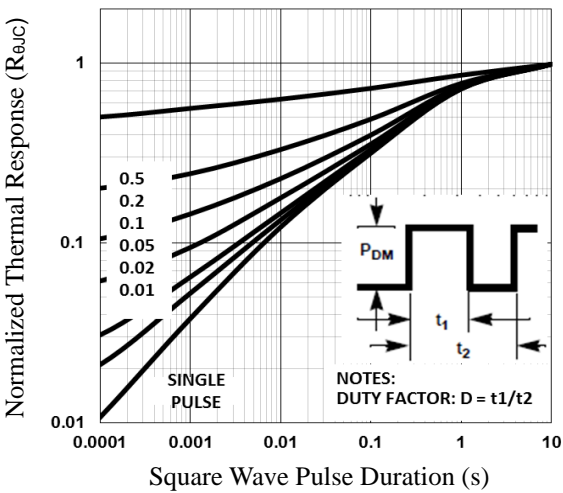
**Fig.2 Normalized RDSON vs. T<sub>j</sub>**



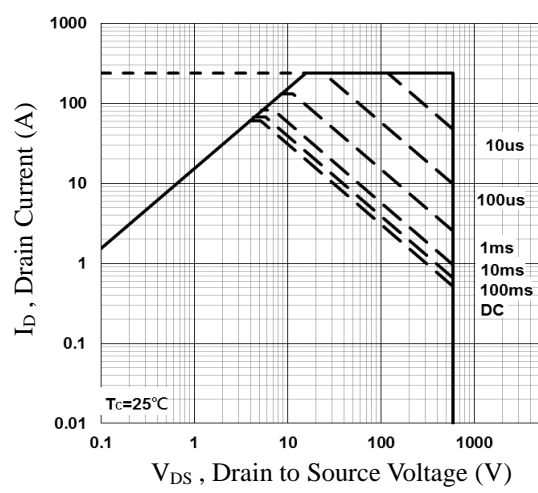
**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**



**Fig.4 Gate Charge Characteristics**



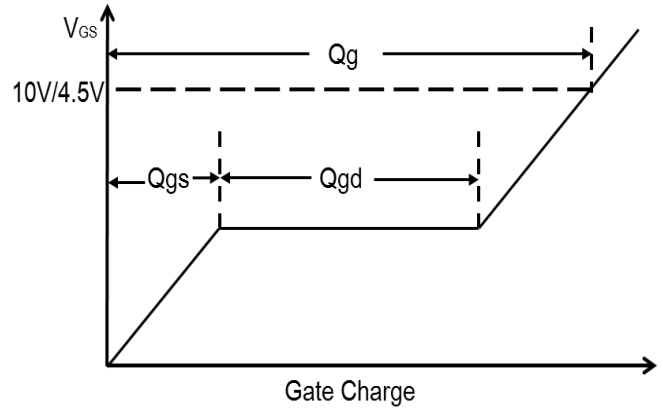
**Fig.5 Normalized Transient Impedance**



**Fig.6 Maximum Safe Operation Area**

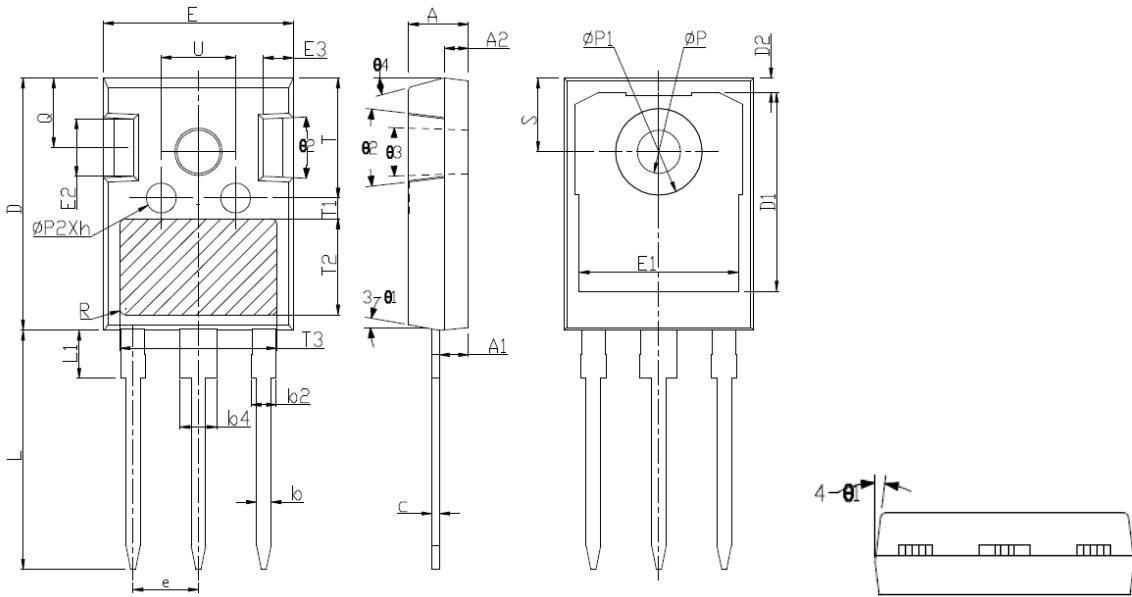


**Fig.7 Switching Time Waveform**



**Fig.8 Gate Charge Waveform**

**TO247 PACKAGE INFORMATION**



SYMBOL	mm			SYMBOL	mm		
	MIN	NOM	MAX		MIN	NOM	MAX
A	4.75	5.00	5.25	L	19.52	19.92	20.32
A1	2.16	2.41	2.66	L1	---	---	4.30
A2	1.85	2.00	2.15	ΦP	3.35	3.60	3.85
b	1.11	1.20	1.35	ΦP1	---	---	7.30
b2	1.90	2.01	2.25	ΦP2	2.25	2.50	2.75
b4	2.90	3.10	3.25	Q	5.50	5.80	6.10
c	0.51	0.61	0.75	S	6.15BSC		
D	20.60	21.00	21.40	R	0.50REF		
D1	16.15	16.55	16.95	T	9.70	---	10.30
D2	1.00	1.20	1.40	T1	1.65REF		
E	15.50	15.80	16.10	T2	8.00REF		
E1	13.00	13.30	13.60	T3	12.80REF		
E2	4.70	5.00	5.30	U	5.9	---	6.5
E3	2.25	2.50	2.75	θ1	4°	7°	10°
e	5.44BSC			θ2	2°	5°	8°
h	0.00	0.10	0.25	θ3	1°	---	2°
				θ4	10°	15°	20°