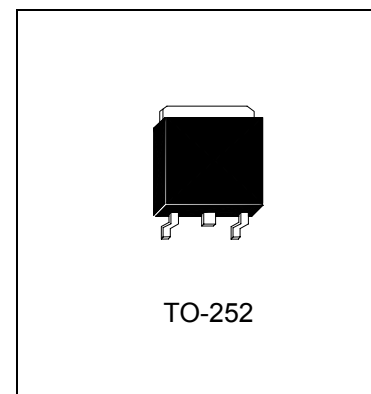


3-TERMINAL POSITIVE VOLTAGE REGULATOR

PL7824XJ3



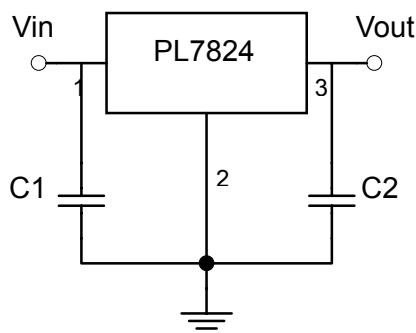
Description

The PL7824XJ3 series of three-terminal positive regulators are available in the TO-252 package. These regulators can provide local on-card regulation, eliminating the distribution problems associated with single point regulation. Each employs internal current limiting, thermal shutdown and safe operating area protection, making it essentially indestructible. If adequate heat sinking is provided, they can deliver over 1A output current. Although designed primarily as fixed voltage regulators, these devices can be used with external components to obtain adjustable voltages and currents. PL7824XJ3 is characterized for operation from 0°C to +125°C, and if operating temperature is always high, please refer to the power dissipation curve.

Absolute Maximum Ratings (Ta=25°C)

- Input Voltage 40 V
- Total Power Dissipation Internally limited
- Operating Temperature Range 0 °C to +125 °C
- Maximum Junction Temperature 125 °C
- Storage Temperature Range -55 °C to +150 °C
- Lead Temperature (Soldering 10S) 230 °C

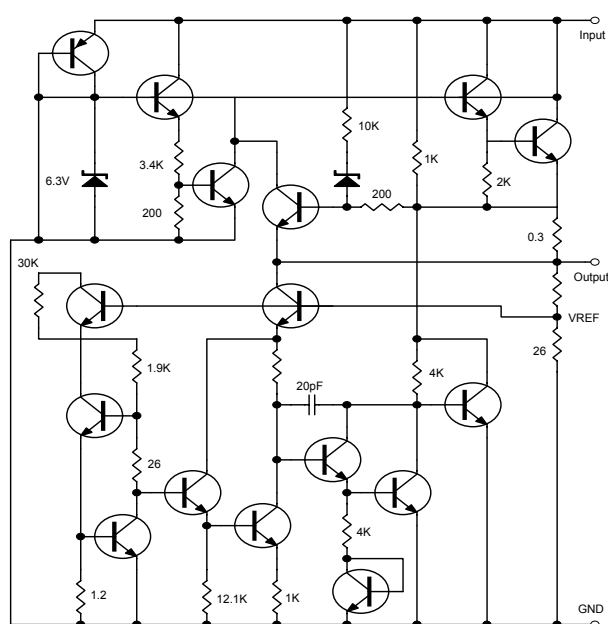
Typical Application



Note:

C1 and C2 are required if regulator is located far from power supply filter and load, or oscillation may induced on the loop.

Schematic Diagram





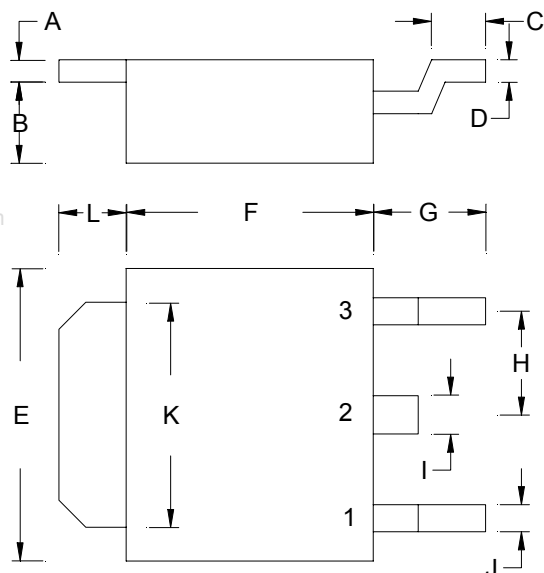
Electrical Characteristics

$V_{in}=33V$, $I_o=500mA$, $0^{\circ}C \leq T_j \leq 125^{\circ}C$ (unless otherwise noted)

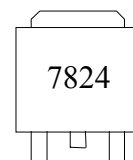
Symbol	Parameter	Conditions	PL7824A			Units
			Min	Typ	Max	
V_o	Output Voltage	$T_j=25^{\circ}C$	23.28	24	24.72	V
		$PD \leq 15W$, $5mA \leq I_o \leq 1A$	23.28	24	24.72	
ΔV_o	Line Regulation	$T_j=25^{\circ}C$, $27V \leq V_{in} \leq 38V$	-	18	240	mV
		$T_j=25^{\circ}C$, $30V \leq V_{in} \leq 36V$	-	6	240	
ΔV_o	Load Regulation	$5mA \leq I_o \leq 1.5A$	-	-	240	mV
		$250mA \leq I_o \leq 750mA$	-	-	120	
I_Q	Quiescent Current	$I_o \leq 1A$, $T_j=25^{\circ}C$	-	5.5	8	mA
ΔI_Q	Quiescent Current Change	$5mA \leq I_o \leq 1A$	-	-	0.5	mA
		$27V \leq V_{in} \leq 38V$	-	-	1.3	
V_n	Output Noise Voltage	$T_a=25^{\circ}C$, $10Hz \leq f \leq 100KHz$	-	-	200	μV
RR	Ripple Rejection	$28V \leq V_{in} \leq 38V$, $f=120Hz$	-	68	-	dB
VD	Dropout Voltage	$T_j=25^{\circ}C$, $I_o=1A$	-	2	-	V
I_{sc}	Short Circuit Current	$T_j=25^{\circ}C$	-	1.5	-	A
I_{pk}	Peak Output Current	$T_j=25^{\circ}C$	1.7	-	-	A
$\Delta V_o / \Delta T$	Average T_c of V_{out}	$0^{\circ}C \leq T_j \leq +125^{\circ}C$, $I_o=5mA$	-	-0.8	-	$mV/^{\circ}C$

Symbol	Parameter	Conditions	PL7824B			Units
			Min	Typ	Max	
V_o	Output Voltage	$T_j=25^{\circ}C$	23	24	25	V
		$PD \leq 15W$, $5mA \leq I_o \leq 1A$	23.8	24	25.2	
ΔV_o	Line Regulation	$T_j=25^{\circ}C$, $27V \leq V_{in} \leq 38V$	-	18	480	mV
		$T_j=25^{\circ}C$, $30V \leq V_{in} \leq 36V$	-	6	240	
ΔV_o	Load Regulation	$5mA \leq I_o \leq 1.5A$	-	-	480	mV
		$250mA \leq I_o \leq 750mA$	-	-	240	
I_Q	Quiescent Current	$I_o \leq 1A$, $T_j=25^{\circ}C$	-	5.5	8	mA
ΔI_Q	Quiescent Current Change	$5mA \leq I_o \leq 1A$	-	-	0.5	mA
		$27V \leq V_{in} \leq 38V$	-	-	1.3	
V_n	Output Noise Voltage	$T_a=25^{\circ}C$, $10Hz \leq f \leq 100KHz$	-	-	300	μV
RR	Ripple Rejection	$28V \leq V_{in} \leq 38V$, $f=120Hz$	62	73	-	dB
VD	Dropout Voltage	$T_j=25^{\circ}C$, $I_o=1A$	-	2.5	-	V
I_{sc}	Short Circuit Current	$T_j=25^{\circ}C$	-	1.5	-	A
I_{pk}	Peak Output Current	$T_j=25^{\circ}C$	1.7	-	-	A
$\Delta V_o / \Delta T$	Average T_c of V_{out}	$0^{\circ}C \leq T_j \leq +125^{\circ}C$, $I_o=5mA$	-	-0.8	-	$mV/^{\circ}C$

TO-252 Dimension



Marking:



Style: Pin 1.Vin 2.Ground 3.Vout

3-Lead TO-252 Plastic Surface Mount Package
 CYStek Package Code: J3

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0177	0.0217	0.45	0.55	G	0.0866	0.1102	2.20	2.80
B	0.0650	0.0768	1.65	1.95	H	-	*0.0906	-	*2.30
C	0.0354	0.0591	0.90	1.50	I	-	0.0354	-	0.90
D	0.0177	0.0236	0.45	0.60	J	-	0.0315	-	0.80
E	0.2520	0.2677	6.40	6.80	K	0.2047	0.2165	5.20	5.50
F	0.2125	0.2283	5.40	5.80	L	0.0551	0.0630	1.40	1.60

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: 42 Alloy; solder plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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