

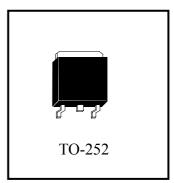
CYStech Electronics Corp.

Spec. No. : C511J3 Issued Date : 2003.03.25 Revised Date : Page No. : 1/3

Three Terminal Positive Voltage Regulators

PL78D05XJ3

These voltage regulators are monolithic integrated circuits designed as fixed voltage regulators for a wide variety of applications including local, on-card regulation. These regulators employ internal current limiting, thermal shutdown, and safe-area compensation. With adequate heatsinking they can deliver output currents in excess of 1.0A. Although designed primarily as fixed voltage regulator, these devices can be used with external components to obtain adjustable voltages and currents.



Maximum Ratings

Rating	Symbol	Value	Unit
Input Voltage	Vin	35	V
Output Current	Io	1	A
Power Dissipation	PD	15	W
Operating Junction	TJ	0 to +125	$^{\circ}$
Temperature Range			

Ordering Information

Device	Output Voltage Tolerance	Package
PL78D05AJ3	3%	TO-252
PL78D05BJ3	5%	TO-252



CYStech Electronics Corp.

Spec. No. : C511J3 Issued Date : 2003.03.25 Revised Date :

Page No.: 2/3

Electrical Characteristics ViN=10V,Iout=500mA,TJ=25°C,CiN=0.33μF,Cout=0.1μF, Unless otherwise specified

Characteristics	Symbol	Test Condition	Min	Тур	Max	Unit
Output Voltage						
PL78D05A	Vo		4.85	5.0	5.15	V
PL78D05B			4.75	5.0	5.25	
Output Voltage						
PL78D05A	Vo	$5.0\text{mA} \leq \text{Iout} \leq 1.0\text{A}, \text{Pd} \leq 15\text{W}$	4.85	5.0	5.15	V
PL78D05B			4.75	5.0	5.25	
Line Regulation	ΔVo	7V≤V _{IN} ≤25V	-	-	50	mV
		8V≤V _{IN} ≤25V	-	-	25	
Load Regulation	ΔVo	5.0mA≤Iouт≤1.5A	-	-	100	mV
		250mA≤Iouт≤750mA	-	-	50	
Quiescent Current	IQ	Iout≤1.0A	-	ı	8	mA
Quiescent Current Change	$\Delta \mathrm{Iq}$	5.0mA≤Iouт≤1.5A	-	-	0.5	mA
		7V≤V _{IN} ≤25V	-	-	1.3	
Dropout Voltage	VD	IOUT=1.0A	_	2	_	V
Peak Output Current	IPK		1.7	-	_	A

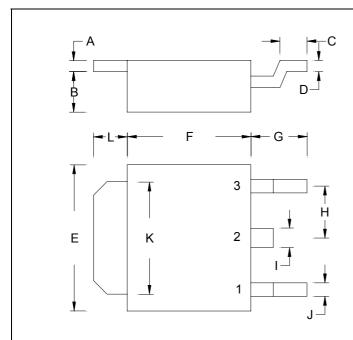


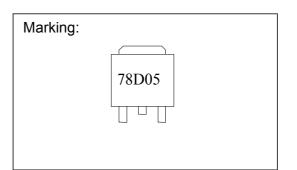
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Spec. No. : C511J3 Issued Date : 2003.03.25 Revised Date : . .

Page No.: 3/3

TO-252 Dimension





Style: Pin 1.Vin 2.Ground 3.Vout

3-Lead TO-252 Plastic Surface Mount Package CYStek Package Code: J3

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.	ואווט	Min.	Max.	Min.	Max.
Α	0.0177	0.0217	0.45	0.55	G	0.0866	0.1102	2.20	2.80
В	0.0650	0.0768	1.65	1.95	Н	-	*0.0906	-	*2.30
С	0.0354	0.0591	0.90	1.50	I	-	0.0354	-	0.90
D	0.0177	0.0236	0.45	0.60	J	-	0.0315	-	0.80
Е	0.2520	0.2677	6.40	6.80	K	0.2047	0.2165	5.20	5.50
F	0.2125	0.2283	5.40	5.80	L	0.0551	0.0630	1.40	1.60

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: 42 Alloy; solder plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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