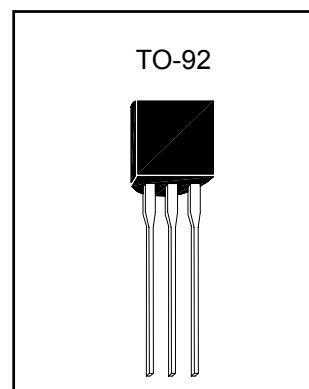


Three Terminal Low Current Positive Voltage Regulators

PL78L08XA3

Description

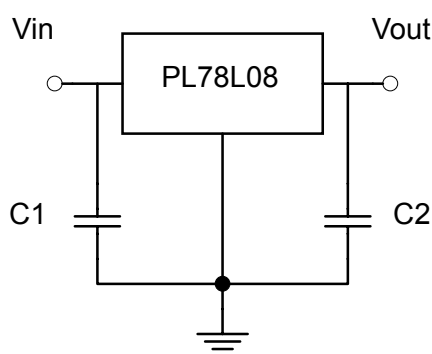
These regulators employ internal current-limiting and thermal-shutdown, making them essentially indestructible. They can deliver up to 100mA output current, if the case temperature can keep in $T_c=25^{\circ}\text{C}$. They are intended as fixed voltage regulators in a wide range of applications including local (on-card) regulation for elimination of noise and distribution problems associated with single-point regulation. In addition, they can be used with power pass elements to make high-current voltage regulators. The PL78L08 used as a Zener diode/resistor combination replacement, offers an effective output impedance improvement of typically two orders of magnitude, along with lower quiescent current and lower noise.



Absolute Maximum Ratings ($T_a=25^{\circ}\text{C}$)

- Input Voltage..... 35 V
- Total Power Dissipation..... Internally limited
- Operating Temperature Range..... 0°C to $+125^{\circ}\text{C}$
- Maximum Junction Temperature..... 125°C
- Storage Temperature Range..... -55°C to $+150^{\circ}\text{C}$
- Lead Temperature (Soldering 10S)..... 260°C

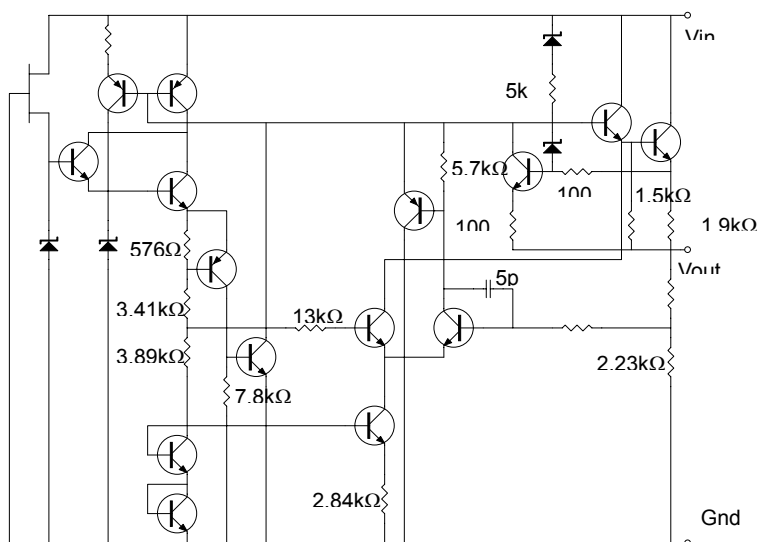
Typical Application



Note:

C1 and C2 are required if regulator is located far from power supply filter and load, or oscillation may induced on the loop.

Schematic Diagram





Ordering Information

Device	Output Voltage Tolerance
PL78L08AA3	3%
PL78L08BA3	5%

Electrical Characteristics

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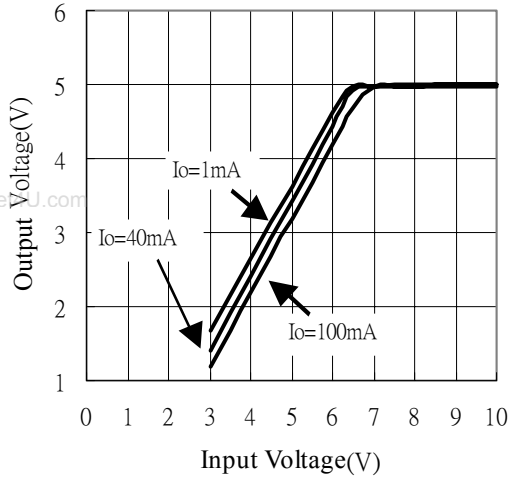
Vin=14V, Iout=40mA, 0°C≤Tj≤125°C, Cin=0.33uF, Cout=0.1uF (unless otherwise noted)

Symbol	Parameter	Conditions	PL78L08A			Units
			Min	Typ	Max	
Vo	Output Voltage	Tj=25°C	7.76	8	8.24	V
		1mA≤Io≤70mA	7.76	-	8.24	
		1mA≤Io≤40mA and 10.5V≤Vin≤23V	7.76	-	8.24	
ΔVo	Line Regulation	Tj=25°C, 10.5V≤Vin≤23V	-	80	175	mV
		Tj=25°C, 11V≤Vin≤23V	-	70	125	
ΔVo	Load Regulation	Tj=25°C, 1mA≤Io≤40mA	-	8	40	mV
		Tj=25°C, 1mA≤Io≤100mA	-	15	80	
IQ	Quiescent Current	Tj=25°C	-	2	5.5	mA
ΔIQ	Quiescent Current Change	1mA≤Io≤40mA	-	-	0.1	mA
		11V≤Vin≤23V	-	-	1.5	
Vn	Output Noise Voltage	Ta=25°C, 10Hz≤f≤10KHz	-	60	-	uV
ΔVin / ΔVout	Ripple Rejection	11V≤Vin≤21V, f=120Hz	39	45	-	dB
Isc	Peak Output / Short Circuit Current	Tj=25°C	-	140	-	mA
VD	Dropout Voltage	Tj=25°C	-	1.7	-	V

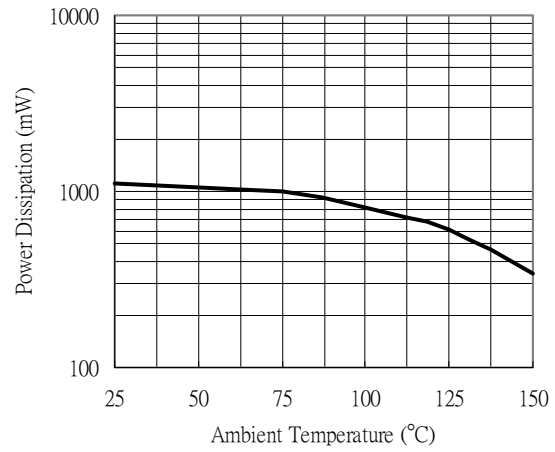
Symbol	Parameter	Conditions	PL78L08B			Units
			Min	Typ	Max	
Vo	Output Voltage	Tj=25°C	7.7	8	8.3	V
		1mA≤Io≤70mA	7.6	-	8.4	
		1mA≤Io≤40mA and 10.5V≤Vin≤23V	7.6	-	8.4	
ΔVo	Line Regulation	Tj=25°C, 10.5V≤Vin≤23V	-	80	175	mV
		Tj=25°C, 11V≤Vin≤23V	-	70	125	
ΔVo	Load Regulation	Tj=25°C, 1mA≤Io≤40mA	-	8	40	mV
		Tj=25°C, 1mA≤Io≤100mA	-	15	80	
IQ	Quiescent Current	Tj=25°C	-	2	5.5	mA
ΔIQ	Quiescent Current Change	1mA≤Io≤40mA	-	-	0.1	mA
		11V≤Vin≤23V	-	-	1.5	
Vn	Output Noise Voltage	Ta=25°C, 10Hz≤f≤10KHz	-	60	-	uV
ΔVin / ΔVout	Ripple Rejection	11V≤Vin≤21V, f=120Hz	39	45	-	dB
Isc	Peak Output / Short Circuit Current	Tj=25°C	-	140	-	mA
VD	Dropout Voltage	Tj=25°C	-	1.7	-	V

Characteristic Curves

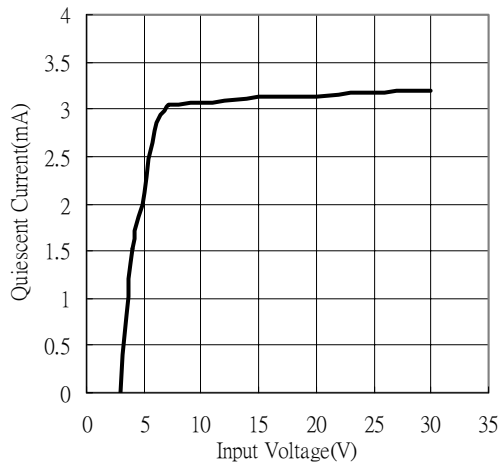
Dropout Characteristics



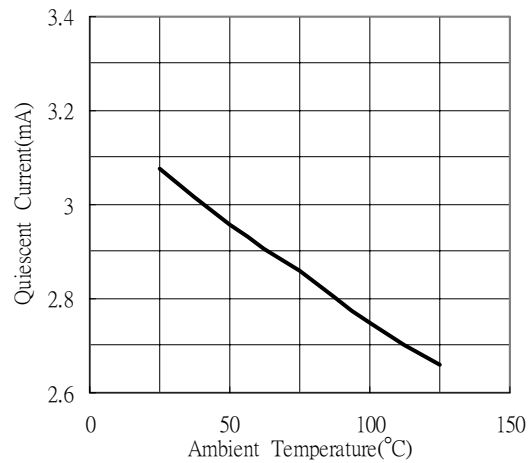
Maximum Average Power Dissipation



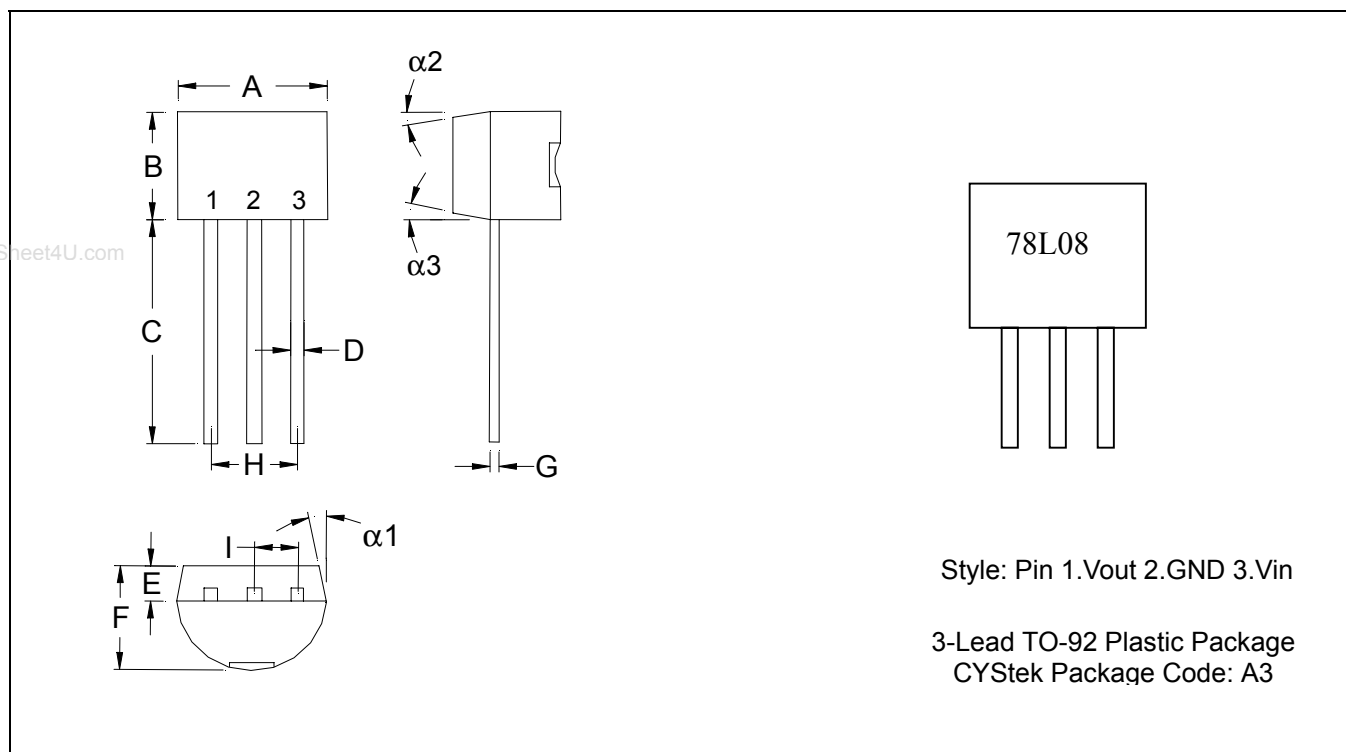
Quiescent Current



Quiescent Current



TO-92 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1704	0.1902	4.33	4.83	G	0.0142	0.0220	0.36	0.56
B	0.1704	0.1902	4.33	4.83	H	-	*0.1000	-	*2.54
C	0.5000	-	12.70	-	I	-	*0.0500	-	*1.27
D	0.0142	0.0220	0.36	0.56	$\alpha 1$	-	*5°	-	*5°
E	-	*0.0500	-	*1.27	$\alpha 2$	-	*2°	-	*2°
F	0.1323	0.1480	3.36	3.76	$\alpha 3$	-	*2°	-	*2°

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: 42 Alloy ; solder plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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