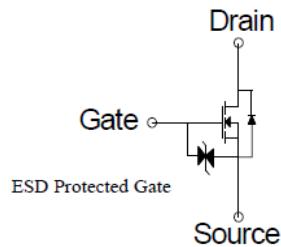
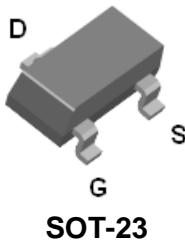


# PM550BA

## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
250V	3Ω @ $V_{GS} = 10V$	0.4A



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	250	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current $T_A = 25^\circ C$	$I_D$	0.4	A
		0.3	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	1.6	
Power Dissipation $T_A = 25^\circ C$	$P_D$	0.75	W
		0.5	
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		166	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.

## PM550BA

### N-Channel Enhancement Mode MOSFET

#### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

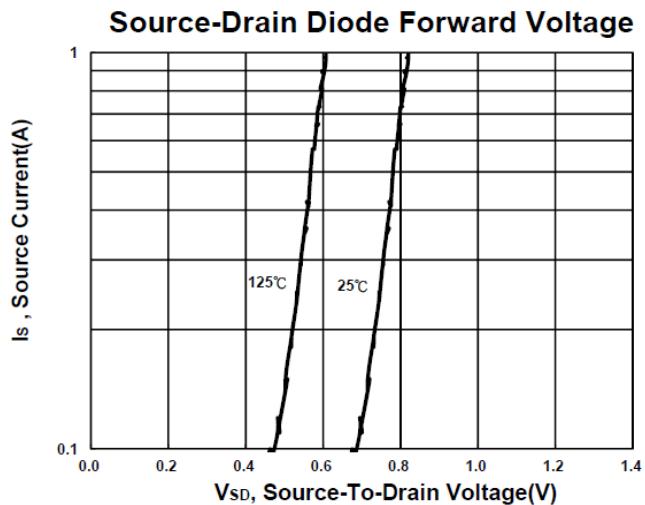
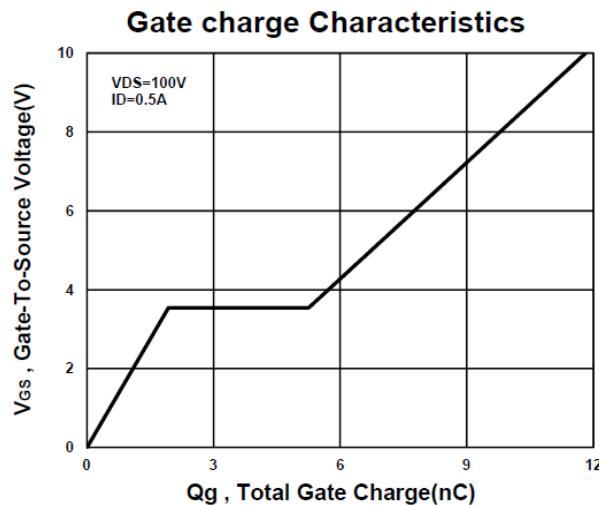
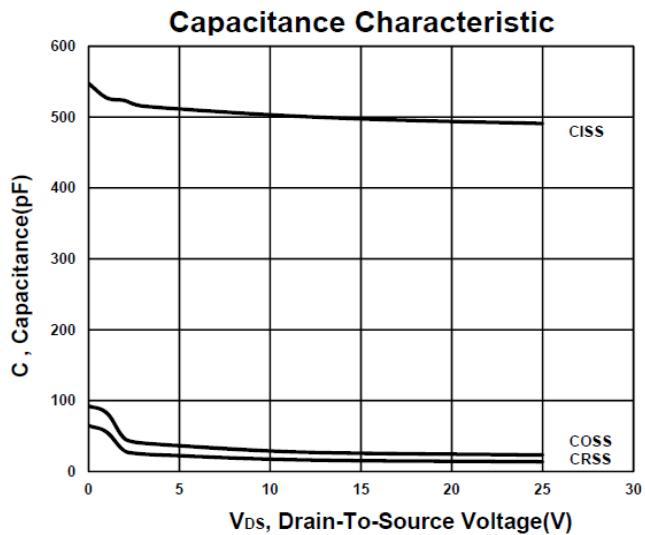
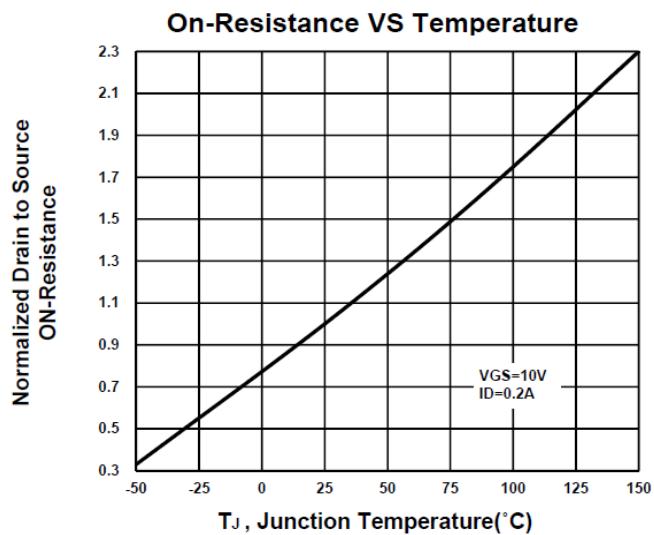
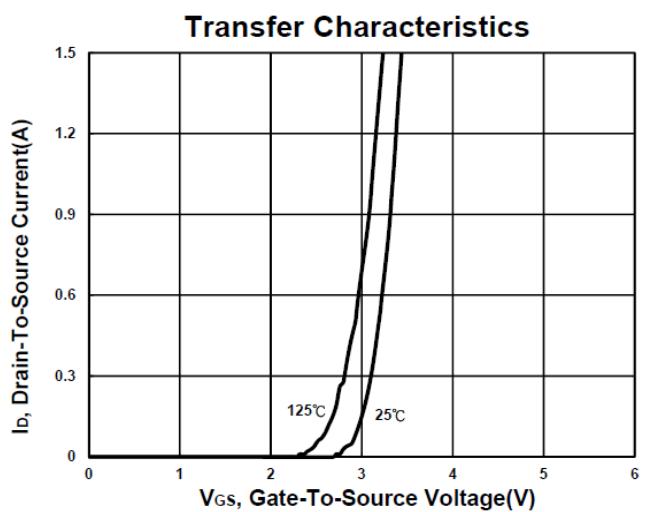
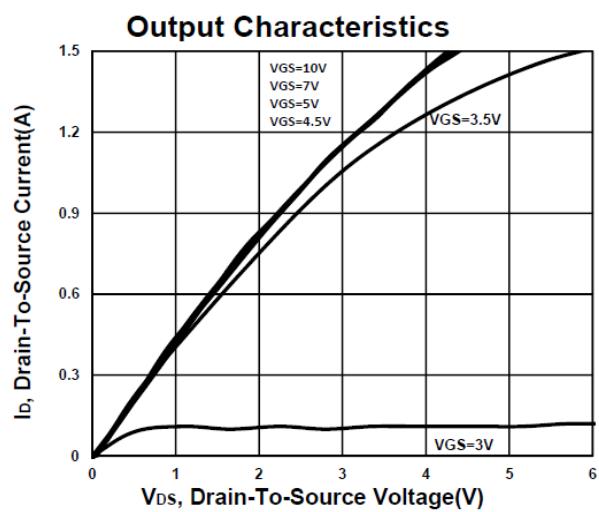
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	250			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.9	3	
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 16\text{V}$			$\pm 30$	uA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 200\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
		$V_{\text{DS}} = 200\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 5\text{V}, I_D = 0.1\text{A}$		2.3	3.2	$\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 0.2\text{A}$		2.2	3	
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 5\text{V}, I_D = 0.2\text{A}$		2		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		489		pF
Output Capacitance	$C_{\text{oss}}$			24		
Reverse Transfer Capacitance	$C_{\text{rss}}$			14		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 0.5\text{A}$		14		nC
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$			2		
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			5		
Turn-On Delay Time <sup>2</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = 125\text{V}$ $I_D \approx 0.1\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		16		nS
Rise Time <sup>2</sup>	$t_r$			80		
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d}(\text{off})}$			24		
Fall Time <sup>2</sup>	$t_f$			40		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( <math>T_J = 25^\circ\text{C}</math> )</b>						
Continuous Current	$I_S$				0.4	A
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$	$I_F = 0.2\text{A}, V_{\text{GS}} = 0\text{V}$			1.3	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_F = 1\text{A}, dI_F/dt = 100 \text{ A}/\mu\text{s}$ $V_{\text{GS}} = 0\text{V}$		80		nS
Reverse Recovery Charge	$Q_{\text{rr}}$			102		nC

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

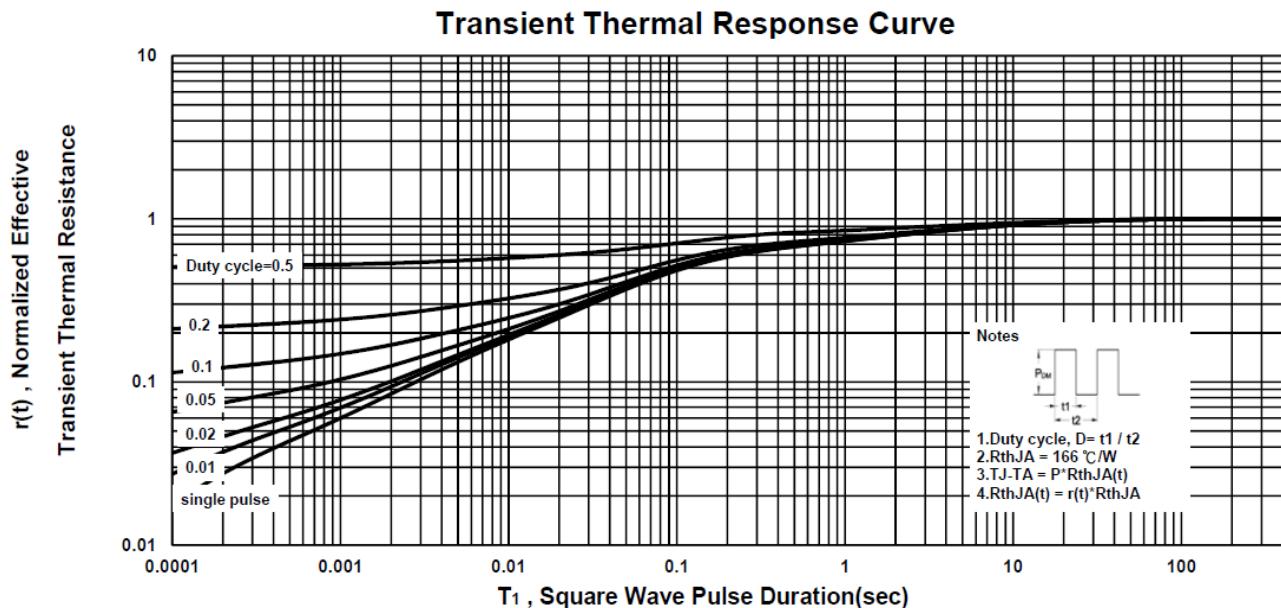
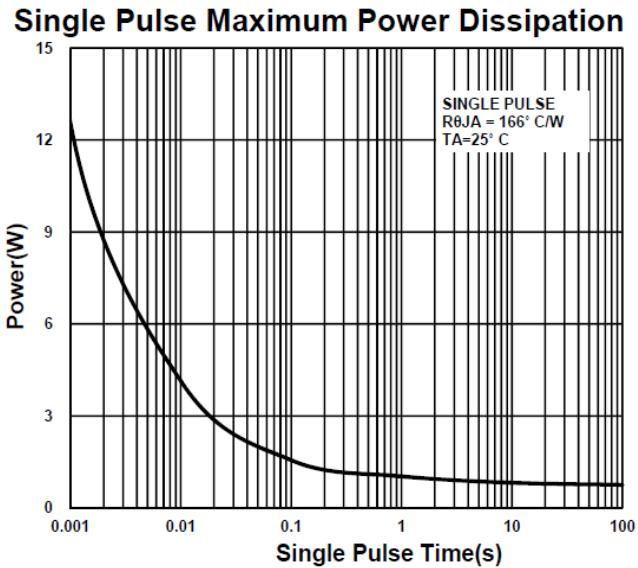
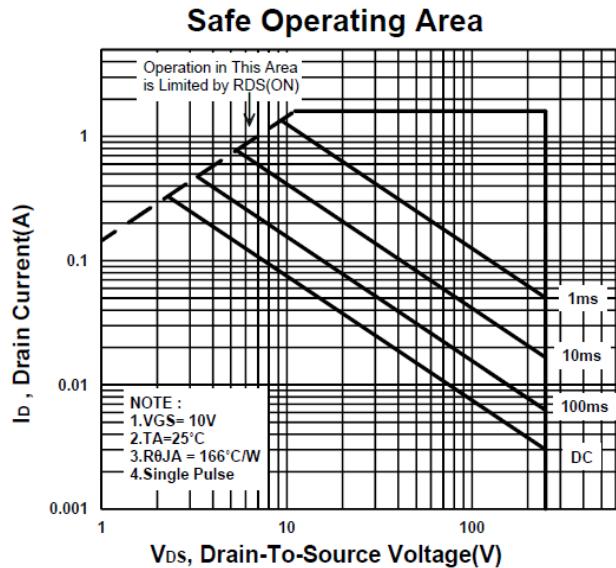
# PM550BA

## N-Channel Enhancement Mode MOSFET



## PM550BA

### N-Channel Enhancement Mode MOSFET



# PM550BA

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### SOT-23 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A		1.05		H	0.1		0.2
B	2.4		3	I	0.3		0.6
C	1.4		1.73				
D	2.7		3.1				
E	1		1.31				
F	0		0.15				
G	0.3		0.5				

