

30 V, 3 A low VF Schottky barrier rectifier 15 July 2024

1. General description

Planar Low V_F Schottky barrier rectifier encapsulated in a CFP15B (SOT1289B) power and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Very low forward voltage
- High power capability due to clip-bond technology ٠
- Small and thin SMD plastic package
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- High efficiency DC-to-DC conversion •
- Low voltage rectification
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- **OR-ing**

4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 173 °C		-	-	3	A	
V _R	reverse voltage	T _j = 25 °C		-	-	30	V	
V _F	forward voltage	I _F = 3 A; pulsed; T _j = 25 °C	[1]	-	400	450	mV	
I _R	reverse current	V_R = 30 V; pulsed; T _j = 25 °C	[1]	-	45	150	μA	

[1] Very short pulse, in order to maintain a stable junction temperature.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A	anode		
2	A	anode		
3	К	cathode		
			CFP15B (SOT1289B)	

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6. Ordering information

Table 3. Ordering information						
Type number Package						
	Name	Description	Version			
PMEG030V030EPE-Q		plastic, thermal enhanced ultra thin SMD package; 3 leads; 2.13 mm pitch; 5.8 x 4.3 x 0.95 mm body	<u>SOT1289B</u>			

7. Marking

Table 4. Marking codes					
Type number	Marking code				
PMEG030V030EPE-Q	030V U03E				

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	30	V
l _F	forward current	δ = 1; T _{sp} ≤ 170 °C		-	4.2	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 173 °C		-	3	A
I _{FSM}	non-repetitive peak forward current	half sine-wave pulse; t _p = 8.3 ms; T _{j(init)} = 25 °C		-	120	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

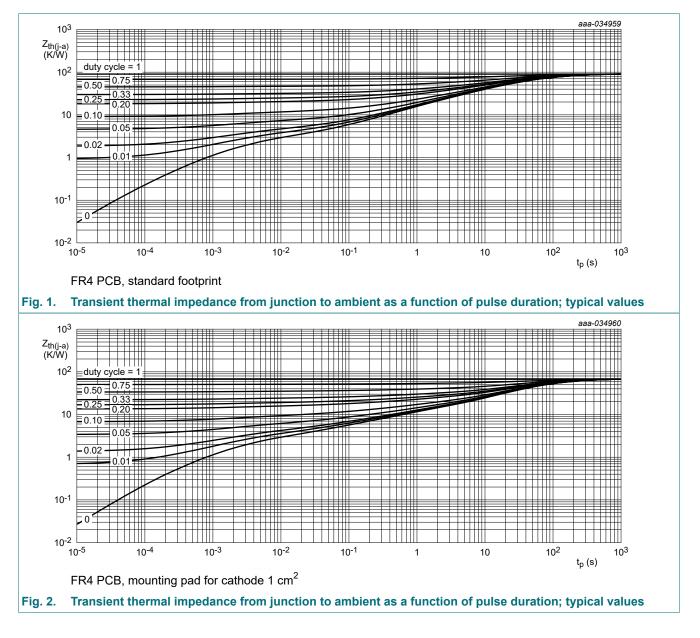
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from in free air	[1] [2]	-	-	90	K/W	
	junction to ambient		[1] [3]	-	-	70	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	3	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

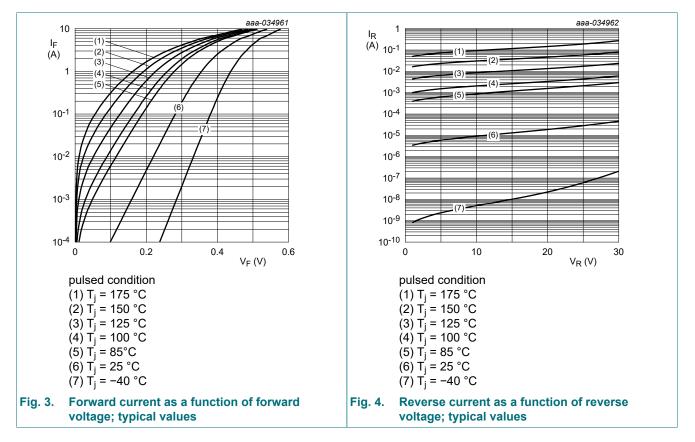
[4] Soldering point of cathode tab.



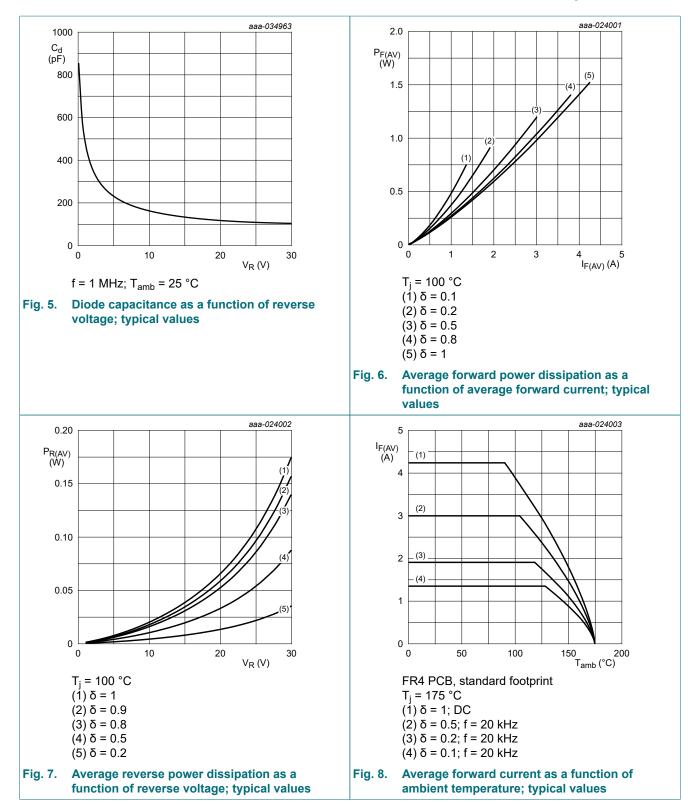
10. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	$I_R = 3 \text{ mA}; \text{ pulsed}; T_j = 25 \text{ °C}$	[1]	30	-	-	V
V _F	forward voltage	I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	350	400	mV
		I _F = 3 A; pulsed; T _j = 25 °C	[1]	-	400	450	mV
		I _F = 3 A; pulsed; T _j = -40 °C	[1]	-	470	550	mV
		I _F = 3 A; pulsed; T _j = 125 °C	[1]	-	300	370	mV
I _R	reverse current	V _R = 30 V; pulsed; T _j = 25 °C	[1]	-	45	150	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	470	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	160	-	pF
t _{rr}	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$		-	16	-	ns
	reverse recovery time ramp recovery	$dI_F/dt = 100 \text{ A}/\mu \text{s}; I_F = 3 \text{ A}; V_R = 30 \text{ V};$ $T_j = 25 ^\circ\text{C}$		-	12	-	ns
V _{FRM}	peak forward recovery voltage	$I_F = 0.5 \text{ A}; \text{ d}_F/\text{d}t = 20 \text{ A}/\mu\text{s}; T_j = 25 \text{ °C}$		-	340	-	mV

[1] Very short pulse, in order to maintain a stable junction temperature.



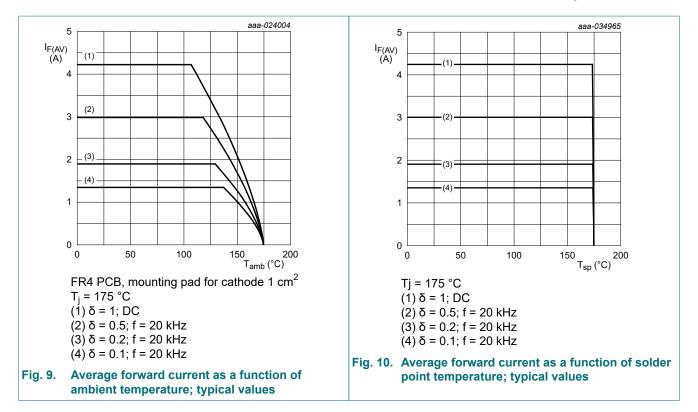
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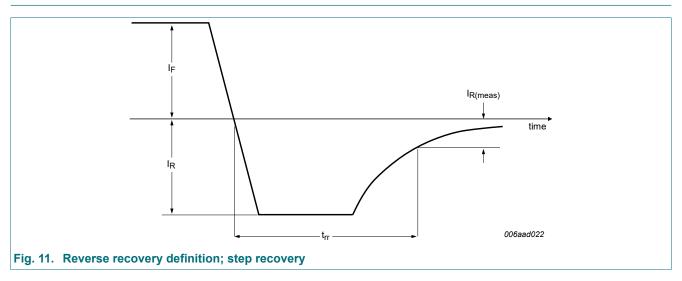
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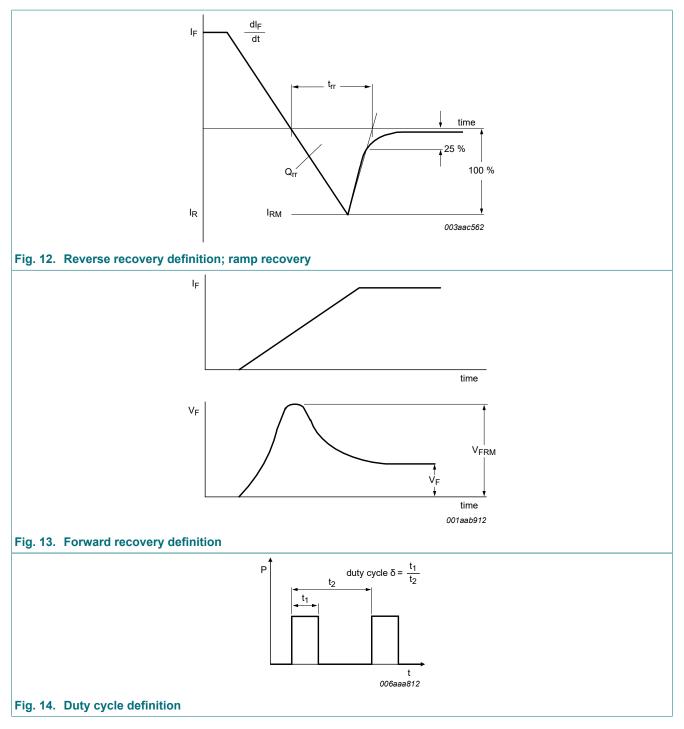
11. Test information



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The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)}=I_M \times \delta$ with I_M defined as peak current

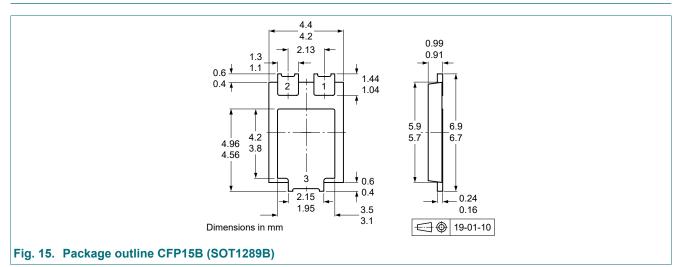
 $I_{RMS}=I_{F(AV)}$ at DC, and $I_{RMS}=I_M \times \sqrt{\delta}$

with $\mathsf{I}_{\mathsf{RMS}}$ defined as RMS current.

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

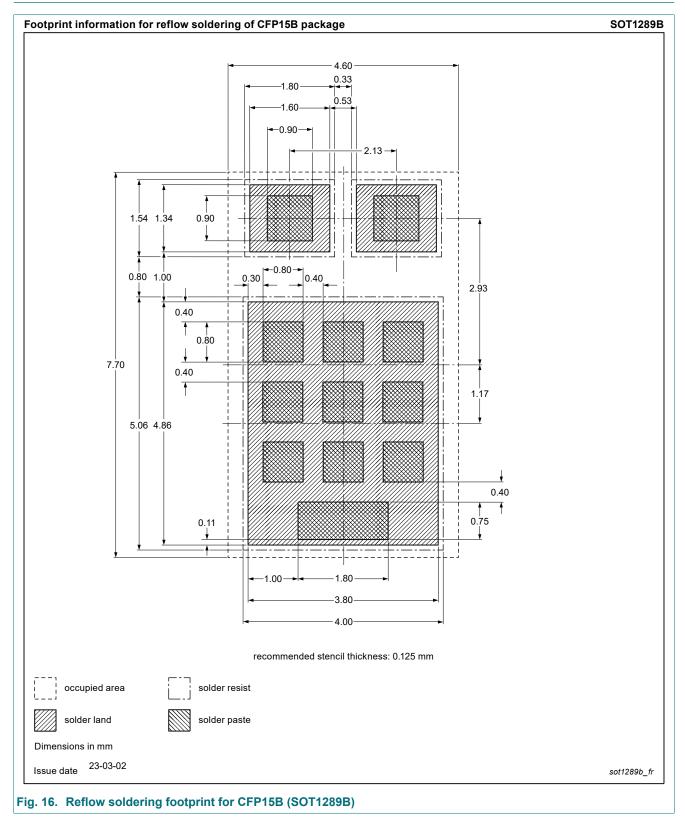
12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG030V030EPE-Q v.2	20240715	Product data sheet	-	PMEG030V030EPE-Q v.1			
Modifications:	Reflow solder	Reflow soldering footprint: Stencil design for solder paste printing changed.					
PMEG030V030EPE-Q v.1	20220715	Product data sheet	-	-			

PMEG030V030EPE-Q

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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