

# PMEG060V030EPD

60 V, 3 A low VF MEGA Schottky barrier rectifier
12 August 2016 Produ

**Product data sheet** 

## 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a CFP15 (SOT1289) power and flat lead Surface-Mounted Device (SMD) plastic package.

#### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 3 A
- Reverse voltage: V<sub>R</sub> ≤ 60 V
- · Extremely low forward voltage
- High power capability due to clip-bonding technology and heat sink
- Small and thin SMD power plastic package, typical height 0.78 mm
- AEC-Q101 qualified

## 3. Applications

- · Low voltage rectification
- High efficiency DC-to-DC conversion
- · Switch mode power supply
- · Freewheeling application
- Reverse polarity protection
- · Low power consumption application

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5 ; f = 20 kHz; T <sub>sp</sub> ≤ 165 °C		-	-	3	Α
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	-	60	V
V <sub>F</sub>	forward voltage	$I_F$ = 3 A; $t_p$ ≤ 300 μs; δ ≤ 0.02 ; $T_j$ = 25 °C		-	460	530	mV
I <sub>R</sub>	reverse current	$V_R = 10 \text{ V}; T_j = 25 ^{\circ}\text{C}; \text{ pulsed}$	[1]	-	5	30	μΑ
		V <sub>R</sub> = 60 V; T <sub>j</sub> = 25 °C; pulsed	[1]	-	75	200	μΑ

[1] Very short test pulse to prevent junction self heating



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Α	anode		⊬ <mark>F</mark> M ⊢A
2	А	anode		A aaa-009063
3	K	cathode	2	add 000000
			CFP15 (SOT1289)	

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
PMEG060V030EPD	CFP15	plastic, thermal enhanced ultra thin SMD package; 3 leads; body: 5.8 x 4.3 x 0.78 mm	SOT1289		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG060V030EPD	060V U03E

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## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	60	V
I <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 163 °C; δ = 1		-	4.2	Α
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5 ; f = 20 kHz; $T_{sp} \le$ 165 °C		-	3	А
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; $t_p$ = 8 ms; $T_{j(init)}$ = 25 °C		-	120	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient		[1][2]	-	-	90	K/W
			[1][3]	_	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[4]	-	-	3	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Soldering point of cathode tab.

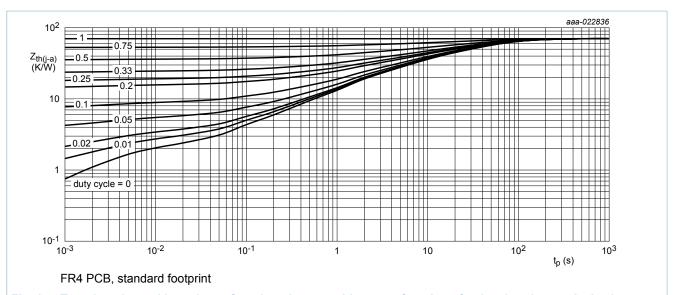


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

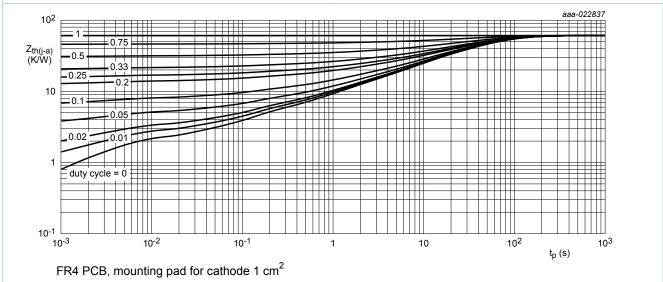


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	$I_R = 3 \text{ mA}$ ; $T_j = 25 ^{\circ}\text{C}$ ; pulsed	[1]	60	-	-	V
V <sub>F</sub>	forward voltage	$I_F = 0.1 \text{ A}; t_p \le 300  \mu\text{s}; \delta \le 0.02 ;$ $T_j = 25 ^{\circ}\text{C}$		-	285	330	mV
		$I_F$ = 1 A; $t_p$ ≤ 300 μs; δ ≤ 0.02 ; $T_j$ = 25 °C		-	375	440	mV
		$I_F = 1.5 \text{ A}; t_p \le 300  \mu\text{s}; \delta \le 0.02 ;$ $T_j = 25 ^{\circ}\text{C}$		-	400	470	mV
		$I_F$ = 2 A; $t_p$ ≤ 300 μs; δ ≤ 0.02 ; $T_j$ = 25 °C		-	420	500	mV
		$I_F$ = 3 A; $t_p$ ≤ 300 μs; δ ≤ 0.02 ; $T_j$ = 25 °C		-	460	530	mV
		$I_F$ = 3 A; $t_p \le 300 \ \mu s$ ; $\overline{o} \le 0.02$ ; $T_j$ = -40 °C		-	505	-	mV
		$I_F = 3 \text{ A}; t_p \le 300  \mu\text{s}; \delta \le 0.02 ;$ $T_j = 125 ^{\circ}\text{C}$		-	400	-	mV
I <sub>R</sub>	reverse current	$V_R = 5 \text{ V}; T_j = 25 ^{\circ}\text{C}; \text{ pulsed}; \text{ pulsed}$	[1]	-	4	15	μA
		V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C; pulsed	[1]	-	5	30	μA
		$V_R = 60 \text{ V}; T_j = 25 ^{\circ}\text{C}; \text{ pulsed}$	[1]	-	75	200	μΑ
		V <sub>R</sub> = 60 V; T <sub>j</sub> = 125 °C; pulsed	[1]	-	34	-	mA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	350	-	pF
		V <sub>R</sub> = 4 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	195	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	120	-	pF
t <sub>rr</sub>	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 ^{\circ}\text{C}$		-	12	-	ns
	reverse recovery time ramp recovery	$dI_F/dt = 200 \text{ A/}\mu\text{s}; T_j = 25 \text{ °C}; I_F = 6 \text{ A};$ $V_R = 26 \text{ V}$		-	11	-	ns

<sup>[1]</sup> Very short test pulse to prevent junction self heating

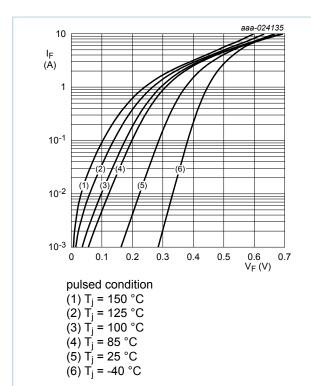


Fig. 3. Forward current as a function of forward voltage; typical values

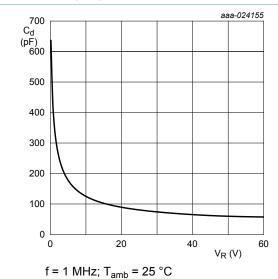


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

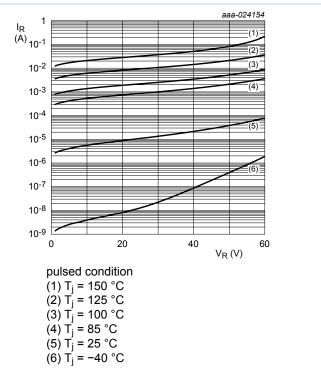
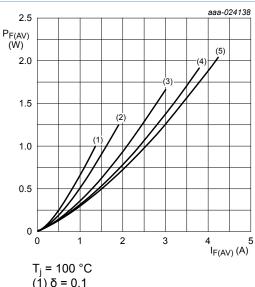


Fig. 4. Reverse current as a function of reverse voltage; typical values



 $T_j = 100^{\circ} \text{ C}$   $(1) \delta = 0.1$   $(2) \delta = 0.2$   $(3) \delta = 0.5$   $(4) \delta = 0.8$  $(5) \delta = 1$ 

Fig. 6. Average forward power dissipation as a function of average forward current; typical values

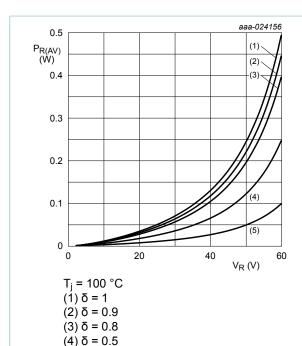
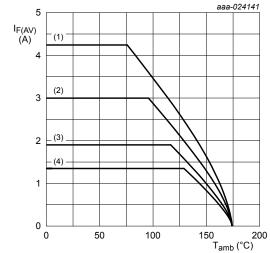


Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, mounting pad for cathode 1  $\mathrm{cm}^2$ 

 $T_j = 175 \,{}^{\circ}\text{C}$ 

 $(5) \delta = 0.2$ 

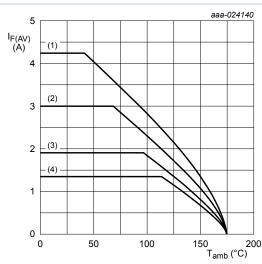
 $(1) \delta = 1; DC$ 

(2)  $\delta$  = 0.5; f = 20 kHz

(3)  $\delta$  = 0.2; f = 20 kHz

(4)  $\delta$  = 0.1; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values



FR4 PCB, standard footprint

T<sub>i</sub> = 175 °C

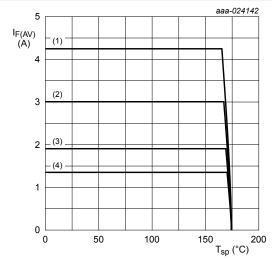
 $(1) \delta = 1; DC$ 

(2)  $\delta$  = 0.5; f = 20 kHz

(3)  $\delta$  = 0.2; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 8. Average forward current as a function of ambient temperature; typical values



 $T_i = 175 \,{}^{\circ}\text{C}$ 

 $(1) \delta = 1; DC$ 

(2)  $\delta$  = 0.5; f = 20 kHz

(3)  $\delta$  = 0.2; f = 20 kHz

(4)  $\delta$  = 0.1; f = 20 kHz

Fig. 10. Average forward current as a function of solder point temperature; typical values

### 11. Test information

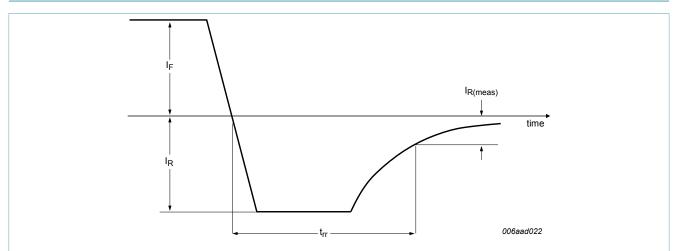


Fig. 11. Reverse recovery definition; step recovery

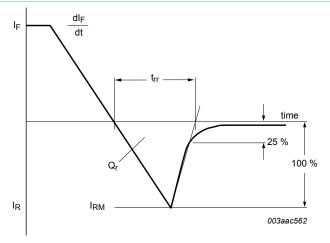


Fig. 12. Reverse recovery definition; ramp recovery

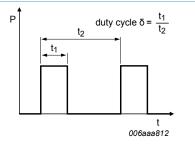


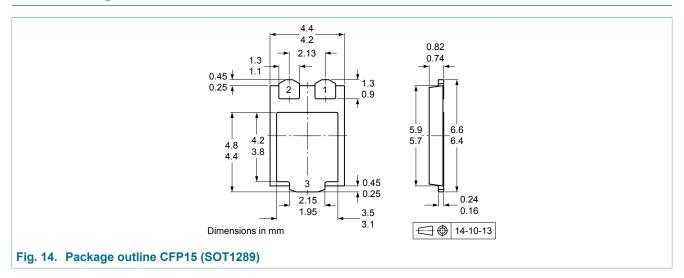
Fig. 13. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_{M} \times \delta$  with  $I_{M}$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_{M} \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

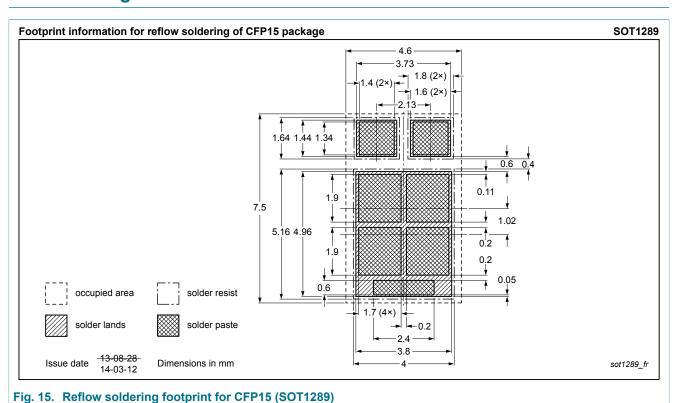
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

# 12. Package outline



## 13. Soldering



# 14. Revision history

## Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG060V030EPD v.1	20160812	Product data sheet	-	-

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# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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