

100 V, 2 A low leakage current Schottky barrier rectifier29 November 2017Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 2 A
- Reverse voltage: V_R ≤ 100 V
- Low forward voltage: V_F = 710 mV
- High power capability due to clip-bonding technology
- Extremely low leakage current
- High temperature T_j ≤ 175 °C
- Small and flat lead SMD plastic package
- AEC-Q101 qualified
- Capable for reflow and wave soldering

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- · Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Table 1. Quic	k reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5 ; f = 20 kHz; $T_{sp} \leq ~160~^\circ\text{C};$ square wave	-	-	2	A
V _R	reverse voltage	T _j = 25 °C	-	-	100	V
V _F	forward voltage	I_F = 2 A; $t_p \leq ~300~\mu s; \delta \leq ~0.02$; T_j = 25 $^\circ C$	-	710	770	mV
I _R	reverse current	V_{R} = 100 V; t_{p} \leq 300 μ s; δ \leq 0.02 ; T_{j} = 25 $^{\circ}\text{C}$	-	70	300	nA

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5. Pinning information

Table 2. Pinning information						
Pin	Symbol	Description	Simplified outline	Graphic symbol		
1	К	cathode[1]		1 🕂 2		
2	A	anode	CFP3 (SOD123W)	sym001		

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMEG10020AELR	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG10020AELR	К9

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _R	reverse voltage	T _j = 25 °C		-	100	V
I _F	forward current	T _{sp} = 155 °C; δ = 1		-	2.83	А
I _{F(AV)}	average forward current	δ = 0.5 $~;$ f = 20 kHz; $T_{amb} \leq ~90 ~^\circ\text{C};$ square wave	[1]	-	2	A
		δ = 0.5 $~;$ f = 20 kHz; $T_{sp} \leq ~160 ~^\circ\text{C};$ square wave		-	2	A
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25		-	30	A
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	50	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	680	mW
			[3]	-	1150	mW
			[1]	-	2140	mW
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics Conditions Symbol Parameter Unit Min Тур Max thermal resistance in free air 220 K/W [1] [2] -R_{th(j-a)} from junction to 130 [1] [3] K/W -_ ambient [1] [4] -70 K/W _ thermal resistance K/W [5] 18 R_{th(j-sp)} _ from junction to solder point

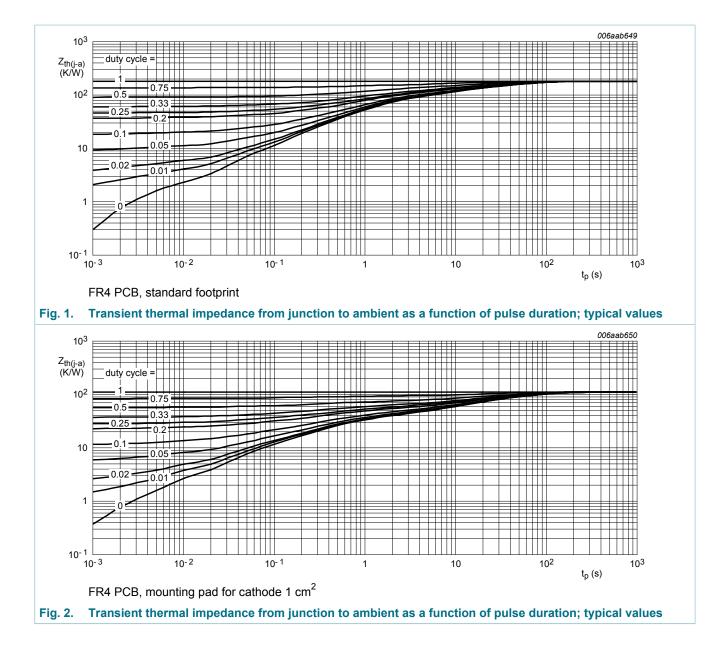
[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

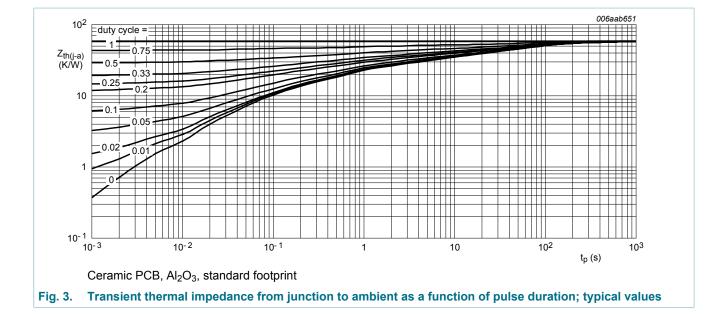
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

 $\label{eq:product} [4] \quad \text{Device mounted on a ceramic PCB, } Al_2O_3, \, \text{standard footprint.}$

[5] Soldering point of cathode tab.



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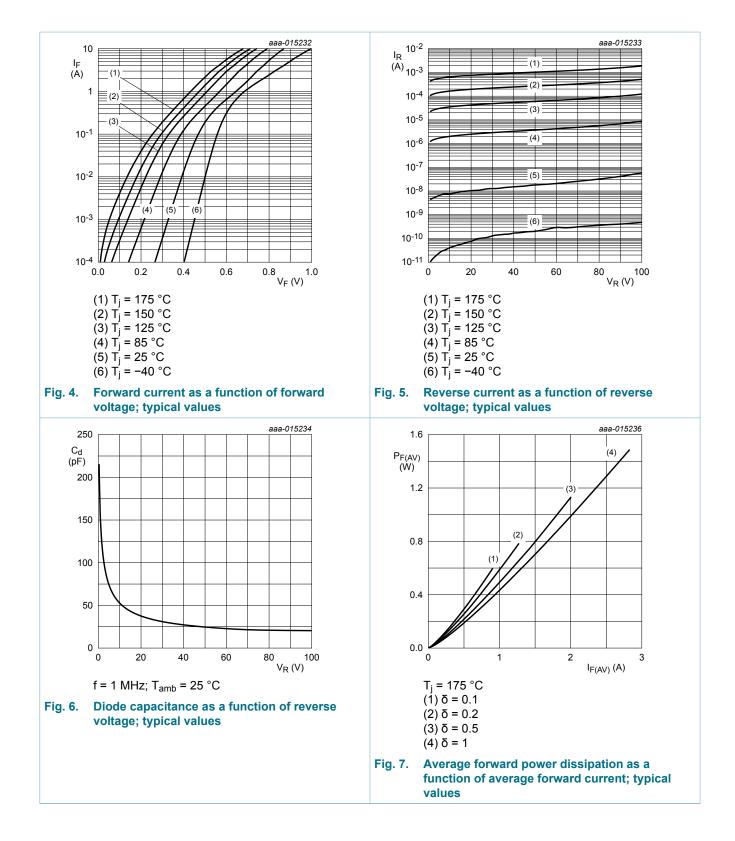
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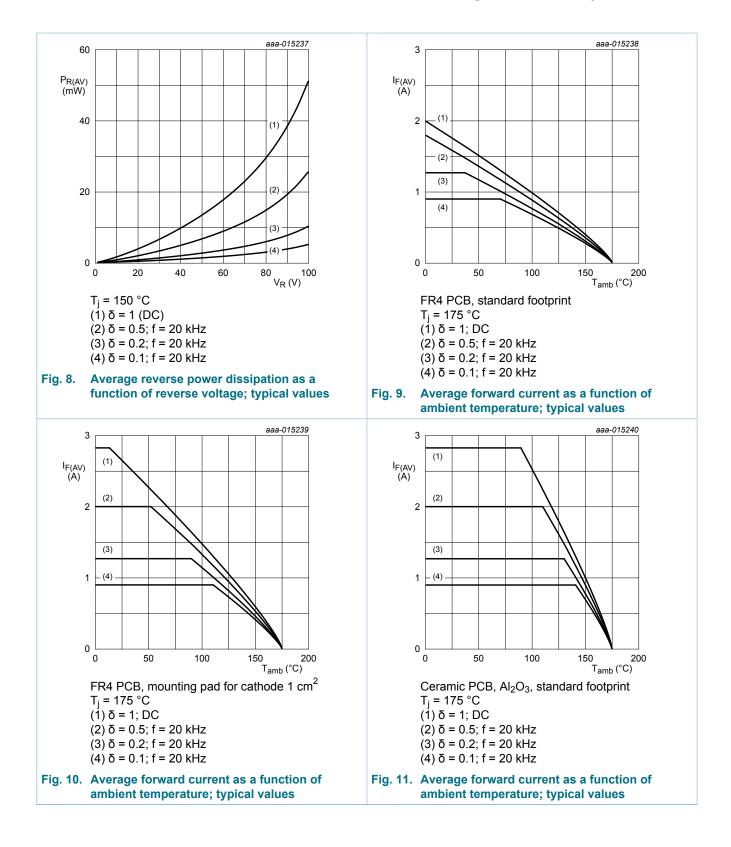
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I_{R} = 1 mA; t_{p} = 300 µs; δ = 0.02 $\ ;$ T_{j} = 25 °C	100	-	-	V
V _F	forward voltage	$I_F = 0.1 \text{ A}; t_p \le 300 \text{ μs}; \delta \le 0.02 ;$ $T_j = 25 \text{ °C}$	-	470	520	mV
		I_{F} = 0.5 A; t_{p} ≤ 300 μs; δ ≤ 0.02 ; T_{j} = 25 °C	-	580	630	mV
		$\begin{array}{l} I_{F} = 0.7 \; A; t_{p} \leq \; 300 \; \mu s; \delta \leq \; 0.02 \; \; ; \\ T_{j} = 25 \; ^{\circ} C \end{array}$	-	610	670	mV
		$\begin{array}{l} I_{\text{F}} = 1 \text{ A}; t_{p} \leq \ 300 \ \mu\text{s}; \delta \leq \ 0.02 \ ; \\ T_{j} = 25 \ ^{\circ}\text{C} \end{array}$	-	650	710	mV
		$\begin{array}{l} I_{F} = 1.6 \; A; t_{p} \leq \; 300 \; \mu s; \delta \leq \; 0.02 \; \; ; \\ T_{j} = 25 \; ^{\circ} C \end{array}$	-	690	750	mV
		I_{F} = 2 A; $t_{\text{p}} \leq 300 \ \mu\text{s}; \ \delta \leq 0.02 \ ; \\ T_{\text{j}} = 25 \ ^{\circ}\text{C}$	-	710	770	mV
		$ \begin{array}{l} I_F = 2 \; A; t_p \leq \; 300 \; \mu s; \delta \leq \; 0.02 \; \; ; \\ T_j = 125 \; ^\circ C \end{array} $	-	575	650	mV
I _R	reverse current	V_{R} = 10 V; $t_{p} \leq ~300~\mu s; ~\delta \leq ~0.02~; T_{j} = 25~^{\circ}C$	-	10	-	nA
		V_R = 60 V; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T _j = 25 °C	-	25	-	nA
		V_R = 100 V; $t_p \le 300 \ \mu s; \delta \le 0.02$; T _j = 25 °C	-	70	300	nA
		V_R = 100 V; $t_p \le 300 \ \mu s; \delta \le 0.02$; T _j = 125 °C	-	120	1000	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	135	-	pF
		V _R = 4 V; f = 1 MHz; T _j = 25 °C	-	80	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	50	-	pF
trr	reverse recovery time	$ I_F = 0.5 \text{ A}; \ I_R = 0.5 \text{ A}; \ I_{R(meas)} = 0.1 \text{ A}; \\ T_j = 25 \ ^{\circ}\text{C} $	-	5	-	ns
V _{FRM}	peak forward recovery voltage	$I_F = 0.5 \text{ A}; \text{ d}I_F/\text{d}t = 20 \text{ A}/\mu\text{s}; T_j = 25 ^\circ\text{C}$	-	630	-	mV

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PMEG10020AELR

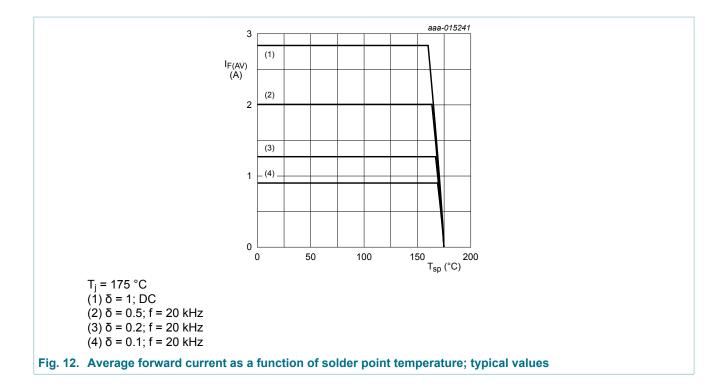




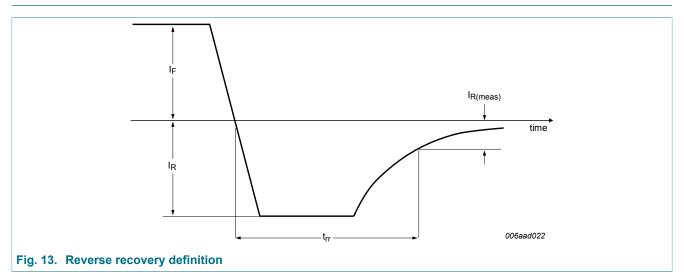
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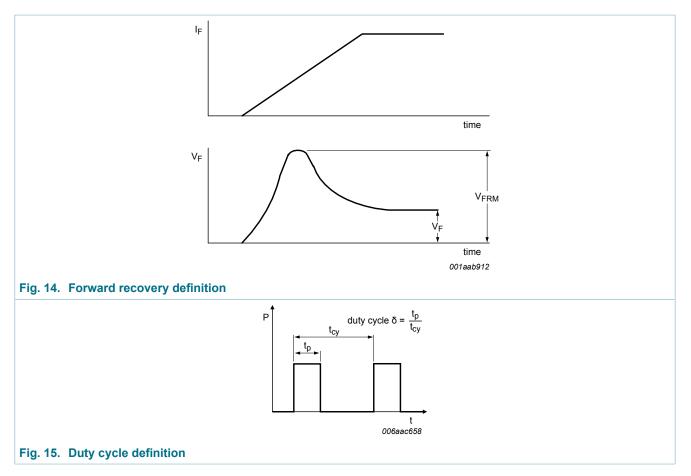
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11. Test information



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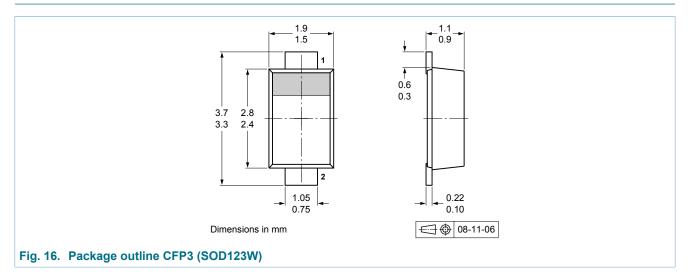
The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

Quality information

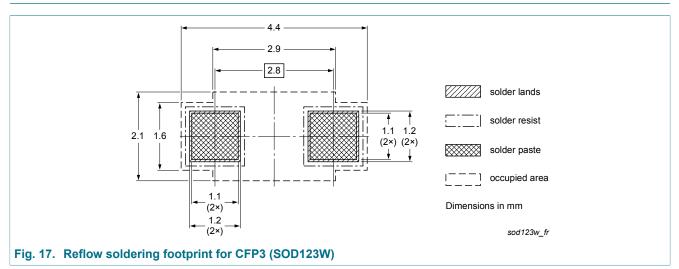
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline



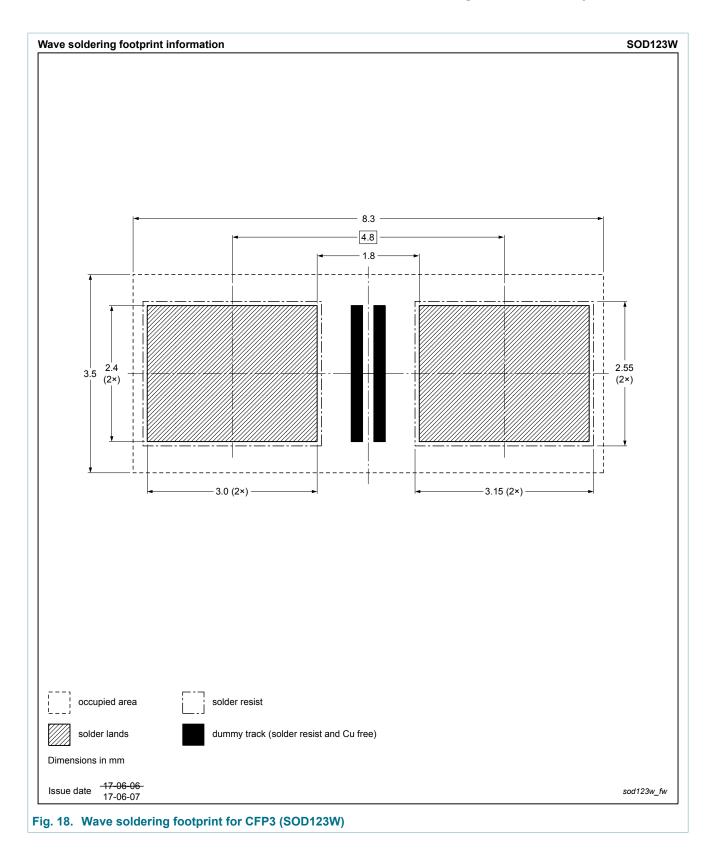
13. Soldering



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PMEG10020AELR



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14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG10020AELR v.4	20171129	PMEG10020AELR v.3					
Modifications:	 Features and benefits: Capable for reflow and wave soldering added Soldering: Wave soldering footprint added 						
PMEG10020AELR v.3	20150507	Product data sheet	-	PMEG10020AELR v.2			
PMEG10020AELR v.2	20150219	Product data sheet	-	PMEG10020AELR v.1			
PMEG10020AELR v.1	20141119	Preliminary data sheet	-	-			

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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