1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High power capability due to clip-bonding technology
- Extremely low leakage current
- High temperature T_i ≤ 175 °C
- Small and flat lead SMD plastic package

3. Applications

- · Low voltage rectification
- · High efficiency DC-to-DC conversion
- · Switch mode power supply
- · Reverse polarity protection
- · Low power consumption applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} \leq 161 °C		-	-	3	Α
V _R	reverse voltage	T _j = 25 °C		-	-	100	V
V _F	forward voltage	I _F = 3 A; pulsed; T _j = 25 °C	[1]	-	760	850	mV
I _R	reverse current	V _R = 100 V; pulsed; T _j = 25 °C	[1]	-	70	300	nA

^[1] Very short pulse, in order to maintain a stable junction temperature.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		IZ □
2	A	anode	1	K J€ A sym001

[1] The marking bar indicates the cathode.



6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG10030CELP		plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG10030CELP	GL

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage	T _j = 25 °C		-	100	V
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} \leq 161 °C		-	3	А
I _{FSM}	non-repetitive peak forward current	t_p = 8.3 ms; half-sine wave; $T_{j(init)}$ = 25 °C		-	50	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	750	mW
			[2]	-	1.25	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

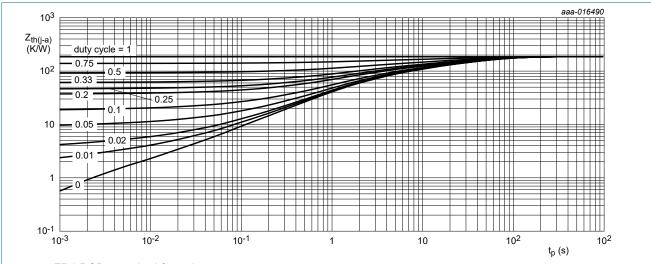
^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

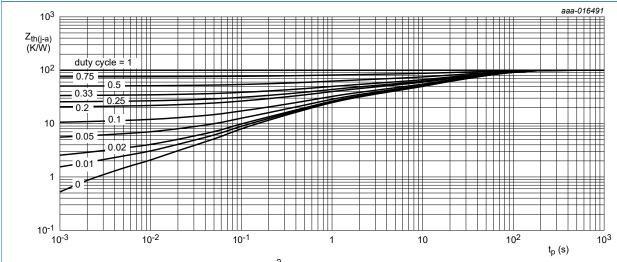
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1] [2]	-	-	200	K/W
	junction to ambient		[1] [3]	-	-	120	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[4]	-	-	12	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Soldering point of cathode tab.



FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

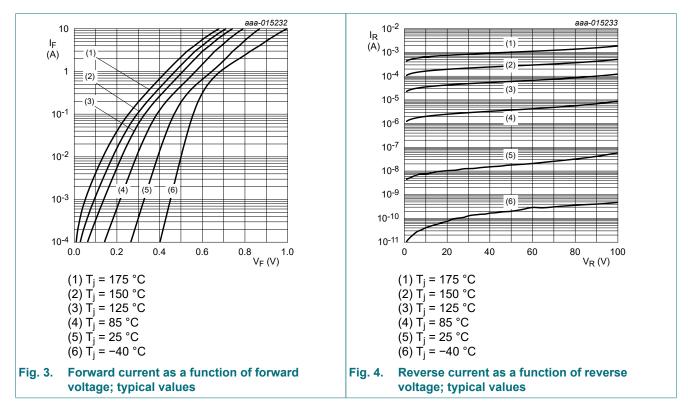
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	$I_R = 1$ mA; pulsed; $T_j = 25$ °C	[1]	100	-	-	V
V _F	forward voltage	I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	650	710	mV
		I _F = 3 A; pulsed; T _j = 25 °C	[1]	-	760	850	mV
		I _F = 3 A; pulsed; T _j = -40 °C	[1]	-	830	-	mV
		I _F = 3 A; pulsed; T _j = 125 °C	[1]	-	620	-	mV
I _R	reverse current	V _R = 60 V; pulsed; T _j = 25 °C	[1]	-	25	-	nA
		V _R = 100 V; pulsed; T _j = 25 °C	[1]	-	70	300	nA
		V _R = 100 V; pulsed; T _j = 125 °C	[1]	-	120	1000	μA
C _d	diode capacitance	$V_R = 1 \text{ V; } f = 1 \text{ MHz; } T_j = 25 ^{\circ}\text{C}$		-	135	-	pF
		V _R = 4 V; f = 1 MHz; T _j = 25 °C		-	80	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	50	-	pF
t _{rr}	reverse recovery time ramp recovery	$dI_F/dt = 200 \text{ A/}\mu\text{s}; I_F = 6 \text{ A}; V_R = 26 \text{ V};$ $T_j = 25 \text{ °C}$		-	13.6	-	ns
I _{RM}	peak reverse recovery current	$dI_F/dt = 200 \text{ A/s}; I_F = 6 \text{ A}; V_R = 26 \text{ V};$ $T_j = 25 \text{ °C}$		-	1.4	-	A
Q _{rr}	reverse recovery charge			-	10.6	-	nC

[1] Very short pulse, in order to maintain a stable junction temperature.



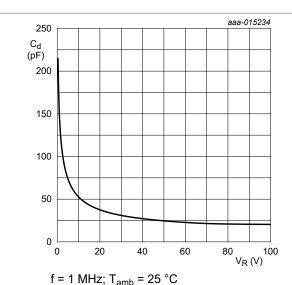
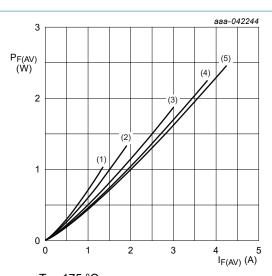
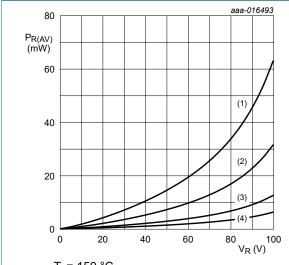


Fig. 5. Diode capacitance as a function of reverse voltage; typical values



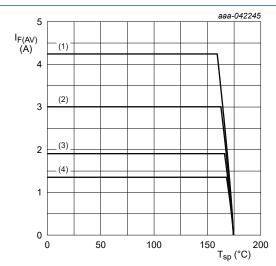
 $T_j = 175 \,^{\circ}\text{C}$ $(1) \, \delta = 0.1$ $(2) \, \delta = 0.2$ $(3) \, \delta = 0.5$ $(4) \, \delta = 0.8$ $(5) \, \delta = 1 \, (DC)$

Fig. 6. Average forward power dissipation as a function of average forward current; typical values



 $T_j = 150 \,^{\circ}\text{C}$ $(1) \, \delta = 1 \, (DC)$ $(2) \, \delta = 0.5; \, f = 20 \, \text{kHz}$ $(3) \, \delta = 0.2; \, f = 20 \, \text{kHz}$ $(4) \, \delta = 0.1; \, f = 20 \, \text{kHz}$

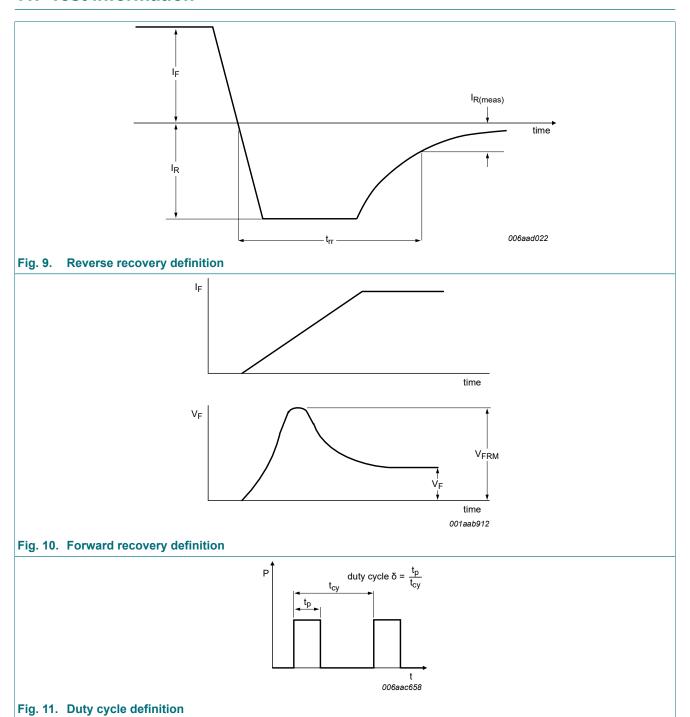
Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values



 $T_j = 175 \,^{\circ}\text{C}$ (1) $\delta = 1 \,(\text{DC})$ (2) $\delta = 0.5$; $f = 20 \,\text{kHz}$ (3) $\delta = 0.2$; $f = 20 \,\text{kHz}$ (4) $\delta = 0.1$; $f = 20 \,\text{kHz}$

Fig. 8. Average forward current as a function of solder point temperature; typical values

11. Test information



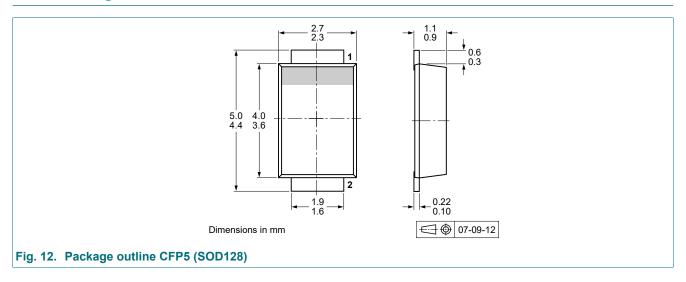
The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current,

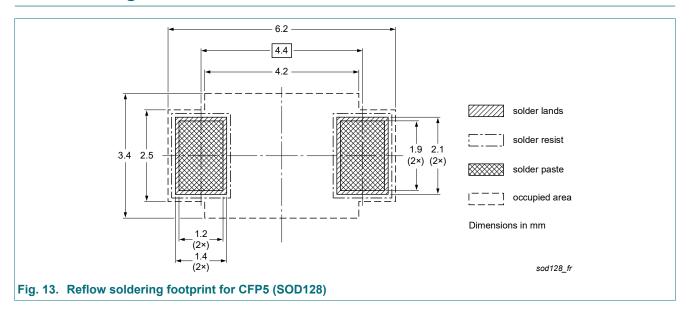
 $I_{RMS} = I_{F(AV)}$ at DC,

 $I_{RMS} = I_{M} \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

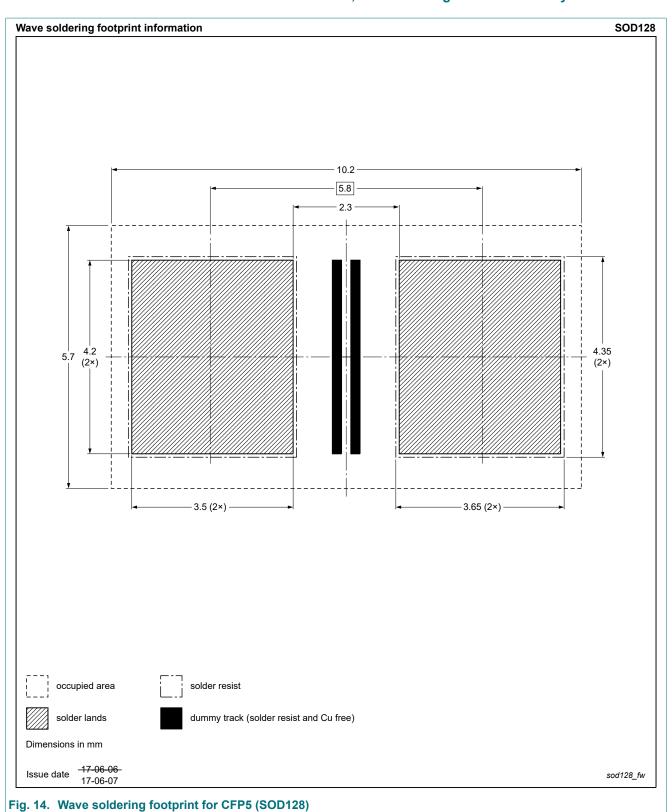
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG10030CELP v.1	20250310	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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