

# PMEG100V080ELPD

100 V, 8 A low leakage current Schottky barrier rectifier
4 October 2016 Product data sheet

## 1. General description

Maximum Efficiency General Application (MEGA) Schottky barrier rectifier, encapsulated in a CFP15 (SOT1289) power and flat lead Surface-Mounted Device (SMD) plastic package.

#### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 8 A
- Reverse voltage: V<sub>R</sub> ≤ 100 V
- Low leakage current due to high Schottky barrier technology
- Low forward voltage
- High power capability due to clip-bonding technology and heat sink
- High temperature T<sub>i</sub> ≤ 175 °C
- Small and thin SMD power plastic package, typical height 0.78 mm
- AEC-Q101 qualified

### 3. Applications

- Low voltage rectification
- Automotive LED lighting
- · High efficiency DC-to-DC conversion
- Switch mode power supply
- · Reverse polarity protection
- Low power consumption application

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F</sub>	forward current	$T_{sp} \le 150 {}^{\circ}\text{C};  \delta = 1$	-	-	11.2	Α
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C	-	-	100	V
V <sub>F</sub>	forward voltage	$I_F$ = 5 A; $t_p \le 300 \ \mu s$ ; $\overline{o} \le 0.02$ ; $T_j$ = 25 °C	-	725	-	mV
I <sub>R</sub>	reverse current	$V_R = 80 \text{ V}; t_p \le 3 \text{ ms}; T_j = 25 \text{ °C}; \delta \le 0.03$	-	0.075	-	μA



## 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	А	anode		⊬ <mark>F</mark> M ⊢A
2	А	anode		A aaa-009063
3	K	cathode	2	add 000000
			CFP15 (SOT1289)	

## 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
PMEG100V080ELPD	CFP15	plastic, thermal enhanced ultra thin SMD package; 3 leads; body: 5.8 x 4.3 x 0.78 mm	SOT1289		

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG100V080ELPD	100V L08E

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	100	V
l <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 150 °C; δ = 1		-	11.2	Α
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; f = 20 kHz; $T_{amb} \le 155$ °C; square wave		-	8	А
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	160	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
			[3]	-	3.75	W
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [3] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.

#### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
fro	thermal resistance from junction to ambient		[1][2]	-	-	90	K/W
			[1][3]	-	-	70	K/W
			[1][4]	-	-	40	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		<u>[5]</u>	-	-	3	K/W

<sup>[1]</sup> For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

<sup>[2]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

<sup>[3]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

<sup>[4]</sup> Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

<sup>[5]</sup> Soldering point of cathode tab.

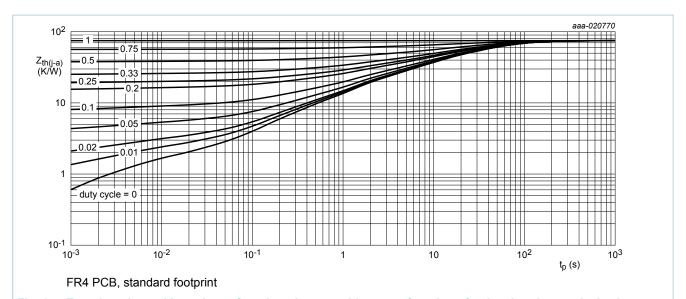


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

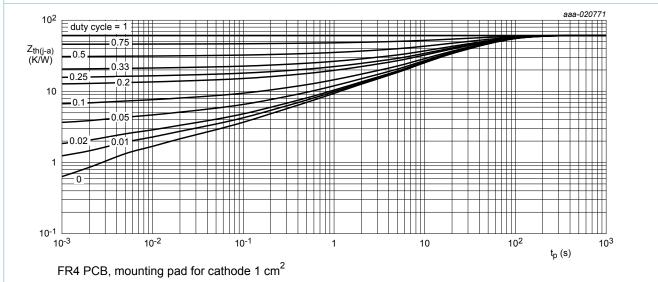
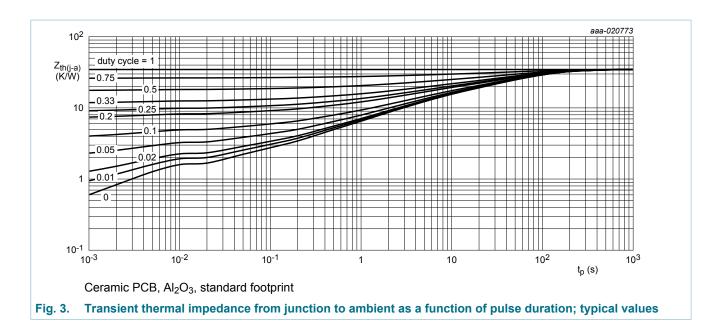


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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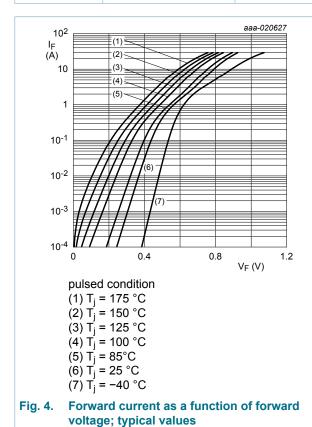


### 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	$I_R$ = 1 mA; $t_p \le 1.2$ ms; $\delta \le 0.12$ ; $T_j$ = 25 °C; pulsed	100	-	-	V
V <sub>F</sub>	forward voltage	$\begin{array}{llllllllllllllllllllllllllllllllllll$	440	-	mV	
			-	565	-	mV
			-	635	740	mV
			-	705	790	mV
			-	725	-	mV
			-	740	-	mV
		$I_F = 8 \text{ A}; t_p \le 300  \mu\text{s}; \delta \le 0.02 ;$ $T_j = 25 ^{\circ}\text{C}$	-	770	850	mV
			-	870	970	mV
			-	570	-	mV
			-	635	740	mV
I <sub>R</sub>	reverse current	$V_R = 60 \text{ V}; t_p \le 3 \text{ ms}; T_j = 25 \text{ °C}; \delta \le 0.03$	-	0.05	-	μA

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$V_R = 80 \text{ V}; t_p \le 3 \text{ ms}; T_j = 25 \text{ °C}; \delta \le 0.03$	-	0.075	-	μA
		$V_R = 100 \text{ V}; t_p \le 3 \text{ ms}; T_j = 25 \text{ °C}; \delta \le 0.03$	-	0.14	0.5	μA
		$V_R = 100 \text{ V}; t_p \le 3 \text{ ms}; T_j = 125 \text{ °C}; \delta \le 0.03$	-	0.3	1.5	mA
		$V_R = 60 \text{ V}; t_p \le 3 \text{ ms}; T_j = 150 \text{ °C}; \delta \le 0.03$	-	0.72	2	mA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	275	-	pF
		V <sub>R</sub> = 4 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	170	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	110	-	pF
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}$ ; $I_R = 0.5 \text{ A}$ ; $I_{R(meas)} = 0.1 \text{ A}$ ; $T_j = 25 \text{ °C}$	-	10	-	ns
V <sub>FRM</sub>	peak forward recovery voltage	$I_F = 0.5 \text{ A}$ ; $dI_F/dt = 20 \text{ A/}\mu\text{s}$ ; $T_j = 25 \text{ °C}$	-	535	-	mV



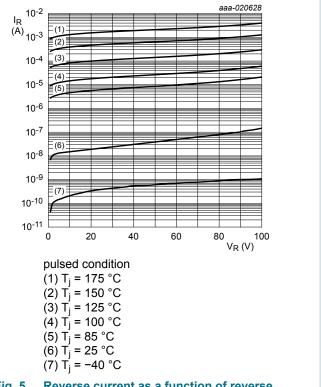


Fig. 5. Reverse current as a function of reverse voltage; typical values

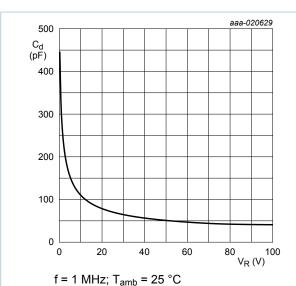
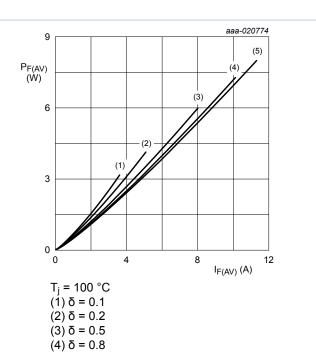
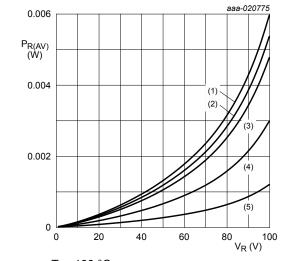


Fig. 6. Diode capacitance as a function of reverse voltage; typical values



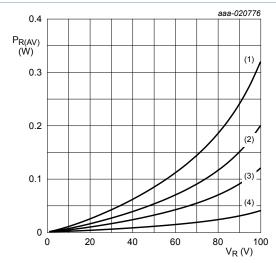
(5)  $\delta$  = 1 Fig. 7. Average forward power dissipation as a function of average forward current; typical values



 $T_j = 100 \,^{\circ}\text{C}$ (1)  $\delta = 1$ (2)  $\delta = 0.9$ (3)  $\delta = 0.8$ (4)  $\delta = 0.5$ 

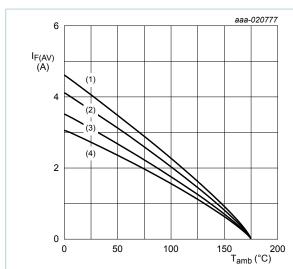
 $(5) \delta = 0.2$ 

Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values



 $T_j = 175 \,^{\circ}\text{C}$ (1)  $\delta = 1$ (2)  $\delta = 0.5$ (3)  $\delta = 0.2$ (4)  $\delta = 0.1$ 

Fig. 9. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

 $T_i = 175 \,{}^{\circ}\text{C}$ 

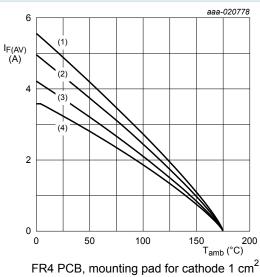
 $(1) \delta = 1; DC$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



 $T_i = 175 \,{}^{\circ}\text{C}$ 

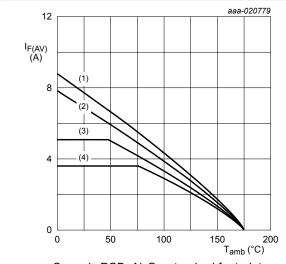
 $(1) \delta = 1; DC$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

T<sub>i</sub> = 175 °C

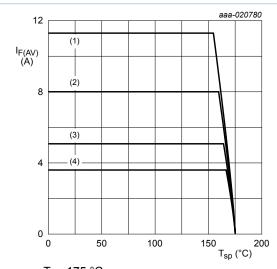
 $(1) \delta = 1 (DC)$ 

(2)  $\delta$  = 0.5; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 12. Average forward current as a function of ambient temperature; typical values



 $T_i = 175 \, ^{\circ}C$ 

 $(1) \delta = 1 (DC)$ 

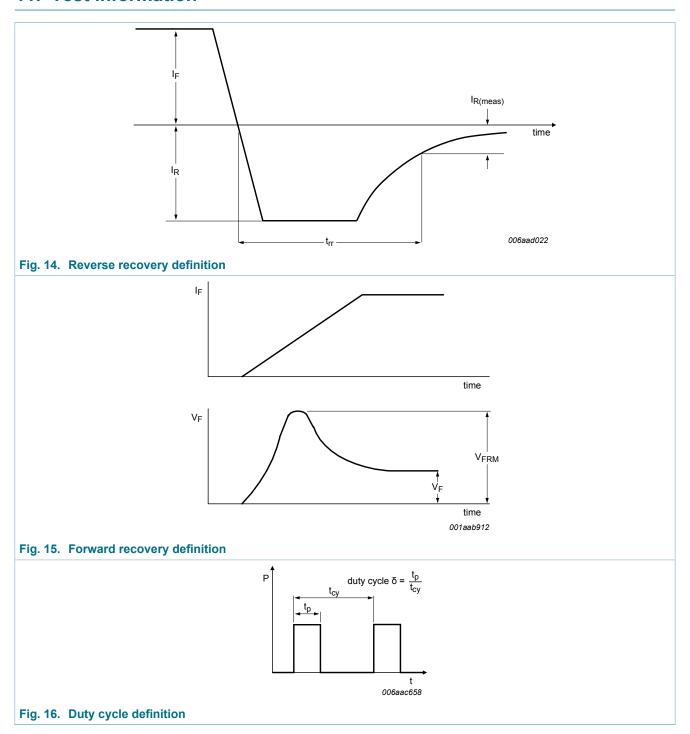
(2)  $\delta$  = 0.5; f = 20 kHz

(3)  $\delta$  = 0.2; f = 20 kHz

(4)  $\delta$  = 0.1; f = 20 kHz

Fig. 13. Average forward current as a function of solder point temperature; typical values

#### 11. Test information

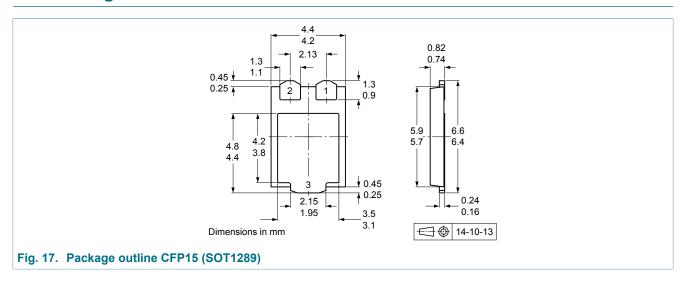


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

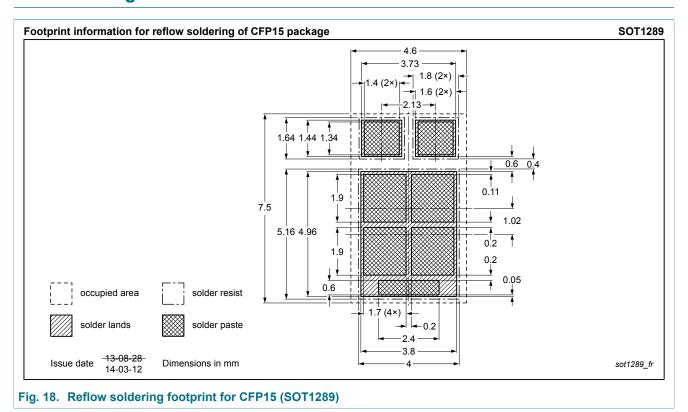
#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

## 12. Package outline



## 13. Soldering



## 14. Revision history

#### Table 8. Revision history

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG100V080ELPD v.3	20161004	Product data sheet	-	PMEG100V080ELPD v.2			
Modifications:	Updated I <sub>R</sub> max	<ul> <li>Updated I<sub>R</sub> maximum value at 100 V, 25 °C</li> </ul>					
PMEG100V080ELPD v.2	20160203	Product data sheet	-	PMEG100V080ELPD v.1			
PMEG100V080ELPD v.1	20151117	Preliminary data sheet	-	-			

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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