20 V, 1.5 A very low V_F MEGA Schottky barrier rectifiersRev. 03 — 15 January 2010Product date

Product data sheet

Product profile 1.

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifiers with an integrated guard ring for stress protection encapsulated in small and flat SMD plastic packages.

Table 1. **Product overview**

| Type number | Package | Package | |
|-------------|----------|---------|--------------|
| | Nexperia | JEITA | |
| PMEG2015EH | SOD123F | - | single diode |
| PMEG2015EJ | SOD323F | SC-90 | single diode |

1.2 Features

- Forward current: ≤ 1.5 A
- Reverse voltage: ≤ 20 V
- Very low forward voltage
- Small and flat lead SMD plastic packages

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low and medium power general applications

1.4 Quick reference data

Table 2. Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------|-----------------|-----------------------------|--------------|-----|-----|------|
| I _F | forward current | $T_{sp} \le 55 \ ^{\circ}C$ | - | - | 1.5 | А |
| V _R | reverse voltage | | - | - | 20 | V |
| V _F | forward voltage | I _F = 1.5 A | <u>[1]</u> - | 560 | 660 | mV |

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[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.

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2. Pinning information

| Description | Simplified outline Symbol |
|-------------|---------------------------|
| cathode | [1] |
| anode | 1 2 1 2 sym001 |
| | cathode |

[1] The marking bar indicates the cathode.

3. Ordering information

| Table 4. Order | ing informati | on | |
|---------------------|---------------|--|---------|
| Type number Package | | | |
| | Name | Description | Version |
| PMEG2015EH | - | plastic surface mounted package; 2 leads | SOD123F |
| PMEG2015EJ | SC-90 | plastic surface mounted package; 2 leads | SOD323F |

4. Marking

| Table 5. Marking codes | |
|--------------------------|--------------|
| Type number | Marking code |
| PMEG2015EH | AD |
| PMEG2015EJ | EL |

5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|--|---------------------------------------|------------|-----|------|------|
| V _R | reverse voltage | | | - | 20 | V |
| I _F | forward current | $T_{sp} \le 55 \ ^{\circ}C$ | | - | 1.5 | А |
| I _{FRM} | repetitive peak forward current | $t_p \le$ 1 ms; $\delta \le$ 0.25 | | - | 5.5 | А |
| I _{FSM} | non-repetitive peak forward current | square wave; t _p = 8 ms | <u>[1]</u> | - | 9 | A |
| P _{tot} | total power dissipation | $T_{amb} \leq 25 \ ^{\circ}C$ | | | | |
| | PMEG2015EH | | [1] | - | 375 | mW |
| | | | [2] | - | 830 | mW |
| | PMEG2015EJ | | [1] | - | 360 | mW |
| | | | [2] | - | 830 | mW |
| Tj | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -65 | +150 | °C |

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Table 6. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|---------------------|------------|-----|------|------|
| T _{stg} | storage temperature | | -65 | +150 | °C |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

6. Thermal characteristics

| Table 7. | Thermal characteristics | | | | | |
|-----------------------|---|-------------|-----------------|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| R _{th(j-a)} | thermal resistance from junction to ambient | in free air | | | | |
| | PMEG2015EH | | <u>[1][2]</u> _ | - | 330 | K/W |
| | | | [2][3] _ | - | 150 | K/W |
| | PMEG2015EJ | | <u>[1][2]</u> _ | - | 350 | K/W |
| | | | [2][3] _ | - | 150 | K/W |
| R _{th(j-sp)} | thermal resistance from junction to solder point | | | | | |
| | PMEG2015EH | | - | - | 60 | K/W |
| | PMEG2015EJ | | - | - | 55 | K/W |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] For Schottky barrier diodes thermal run-away has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses. Nomograms for determining the reverse power losses P_R and $I_{F(AV)}$ rating are available on request.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

7. Characteristics

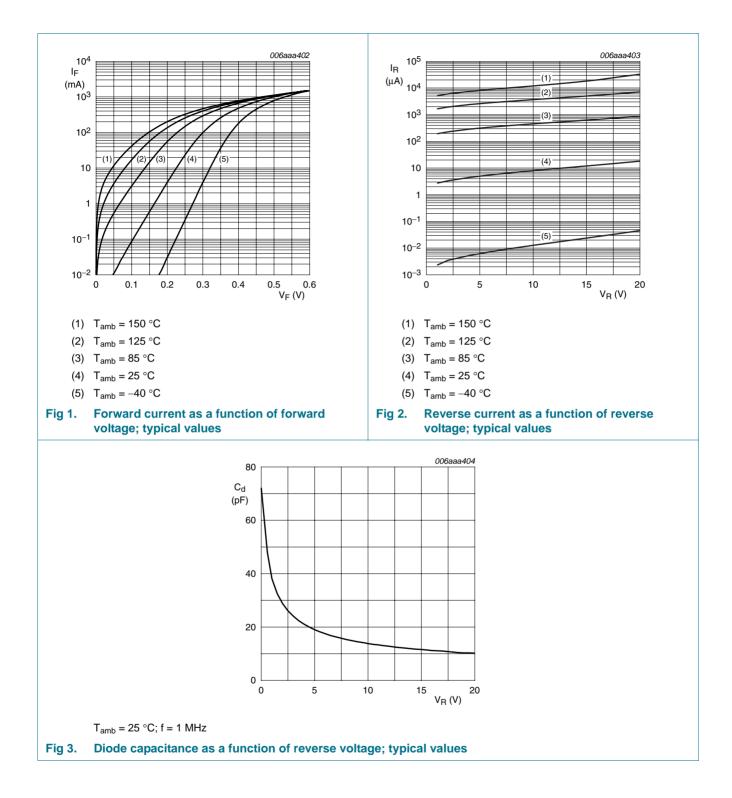
Table 8. Characteristics

 $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified.

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------|-------------------|--------------------------|--------------|-----|-----|------|
| V _F | forward voltage | I _F = 10 mA | <u>[1]</u> - | 240 | 270 | mV |
| | | I _F = 100 mA | <u>[1]</u> _ | 300 | 350 | mV |
| | | I _F = 500 mA | <u>[1]</u> _ | 400 | 460 | mV |
| | | I _F = 1 A | <u>[1]</u> _ | 480 | 550 | mV |
| | | I _F = 1.5 A | <u>[1]</u> _ | 560 | 660 | mV |
| I _R | reverse current | $V_R = 5 V$ | - | 5 | 10 | μΑ |
| | | V _R = 8 V | - | 7 | 20 | μΑ |
| | | V _R = 10 V | - | 8 | 30 | μΑ |
| | | V _R = 15 V | - | 10 | 50 | μΑ |
| | | V _R = 20 V | - | 15 | 70 | μΑ |
| C _d | diode capacitance | $V_{R} = 1 V; f = 1 MHz$ | - | 40 | 50 | pF |

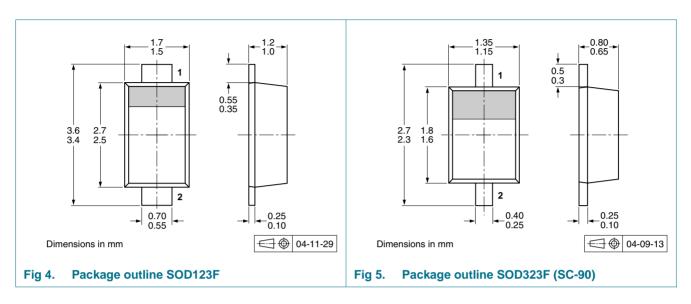
[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.

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8. Package outline



9. Packing information

Table 9. Packing methods

The -xxx numbers are the last three digits of the 12NC ordering code.[1]

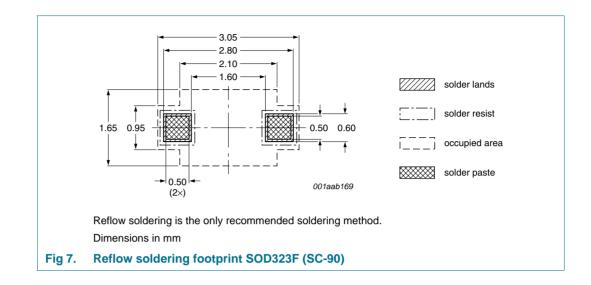
| Type number | Package | Description | Packing quantity | |
|-------------|---------|--------------------------------|------------------|-------|
| | | | 3000 | 10000 |
| PMEG2015EH | SOD123F | 4 mm pitch, 8 mm tape and reel | -115 | -135 |
| PMEG2015EJ | SOD323F | | | |

[1] For further information and the availability of packing methods, see <u>Section 13</u>.

44 4 29 1.6 solder lands solder resist 1.1 1.2 21 16 solder paste 1 occupied area 1 1.1 (2×) Reflow soldering is the only recommended soldering method. Dimensions in mm Fig 6. **Reflow soldering footprint SOD123F**

10. Soldering

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11. Revision history

Table 10.Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|-----------------|--------------|--|---------------|---|
| PMEG2015EH_EJ_3 | 20100115 | Product data sheet | - | PMEG2015EH_EJ_2 |
| Modifications: | | eet was changed to reflect w legal definitions and disc | | ne NXP Semiconductors, vere made to the technical |
| PMEG2015EH_EJ_2 | 20050407 | Product data sheet | - | PMEG2015EJ_1 |
| PMEG2015EJ_1 | 20050302 | Product data sheet | - | - |
| | | | | |

12. Legal information

12.1 Data sheet status

| Document status[1][2] | Product status ^[3] | Definition |
|--------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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