

# PMEG4020EPK

## 40 V, 2 A low VF Schottky barrier rectifier

30 September 2025

**Product data sheet** 

### 1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a leadless ultra small SOD1608 (DFN1608D-2) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 2 A
- Reverse voltage: V<sub>R</sub> ≤ 40 V
- Low forward voltage V<sub>F</sub> ≤ 660 mV
- · Low reverse current
- Solderable side pads
- Package height typ. 0.37 mm
- Ultra small and leadless SMD plastic package

### 3. Applications

- · Low voltage rectification
- · High efficiency DC-to-DC conversion
- · Switch mode power supply
- · LED backlight for mobile application
- Low power consumption applications
- Ultra high-speed switching
- · Reverse polarity protection

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> $\leq$ 130 °C		-	-	2	Α
		$\delta$ = 0.5; f = 20 kHz; square wave; T <sub>amb</sub> $\leq$ 25 °C	[1]	-	-	2	Α
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	40	V
V <sub>F</sub>	forward voltage	$I_F$ = 2 A; pulsed; $t_p \le 300 \ \mu s$ ; $\delta \le 0.02$ ; $T_j$ = 25 °C		-	585	660	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C		-	1	5	μA
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$		-	4	-	ns

<sup>[1]</sup> Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.



## 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		
2	A	anode	Transparent top view DFN1608D-2 (SOD1608)	K <del>【←</del> A sym001

<sup>[1]</sup> The marking bar indicates the cathode.

### 6. Ordering information

### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PMEG4020EPK		plastic, leadless ultra small plastic package with sidewettable flanks (SWF); 2 terminals; 0.94 mm pitch; 1.6 mm x 0.8 mm x 0.37 mm body	SOD1608			

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG4020EPK	0001
	0000

### 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	40	V
I <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 125 °C		-	2.83	А
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; square wave; $T_{sp} \le$ 130 °C		-	2	A
		$\delta$ = 0.5; f = 20 kHz; square wave; $T_{amb} \le$ 25 °C	[1]	-	2	А
I <sub>FRM</sub>	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	4	A
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; square wave; $T_{j(init)}$ = 25 °C		-	5	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	415	mW
			[3]	-	895	mW
			[1]	-	1565	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

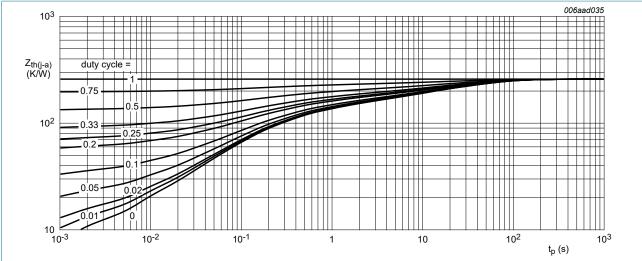
### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub> thermal resistance from junction to ambient	thermal resistance from		[1] [2]	-	-	300	K/W
	junction to ambient		[1] [3]	-	-	140	K/W
	[1]	[1] [4]	-	-	80	K/W	
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[5]	-	-	20	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [5] Soldering point of cathode tab.

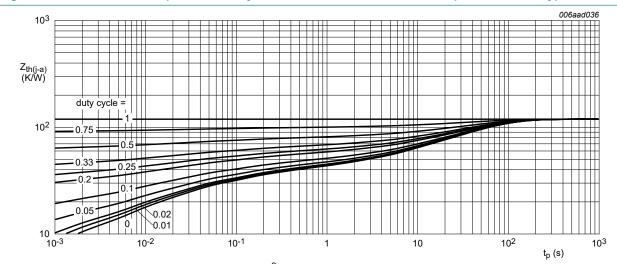
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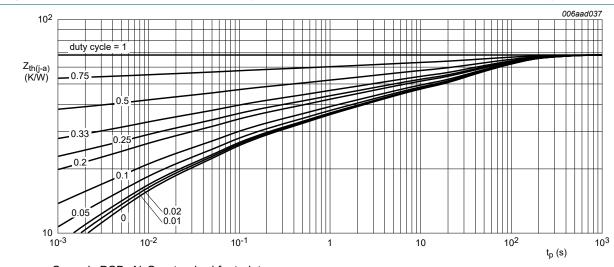
FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

### 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	$I_F$ = 100 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_j$ = 25 °C	-	330	380	mV
		$I_F$ = 500 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_j$ = 25 °C	-	415	480	mV
		$I_F$ = 1 A; pulsed; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_j = 25$ °C	-	490	550	mV
		$I_F$ = 2 A; pulsed; $t_p \le 300 \text{ μs}$ ; $\delta \le 0.02$ ; $T_j$ = 25 °C	-	585	660	mV
I <sub>R</sub> reverse current	V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C	-	1	5	μΑ	
		V <sub>R</sub> = 40 V; T <sub>j</sub> = 25 °C	-	8	30	μΑ
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	75	90	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	30	40	pF
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	4	-	ns
$V_{FRM}$	peak forward recovery voltage	$I_F = 0.5 \text{ A}; dI_F/dt = 20 \text{ A/}\mu\text{s}; T_j = 25 \text{ °C}$	-	440	-	mV

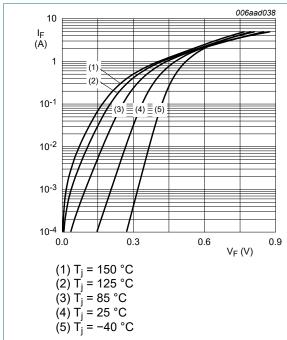


Fig. 4. Forward current as a function of forward voltage; typical values

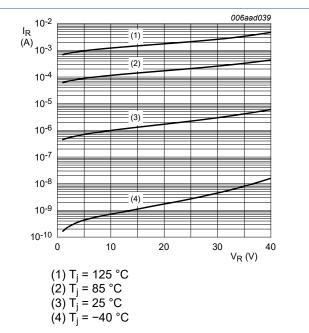


Fig. 5. Reverse current as a function of reverse voltage; typical values

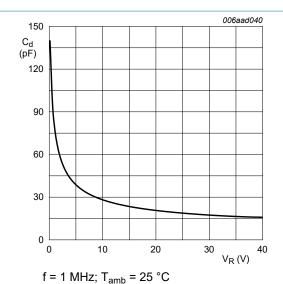
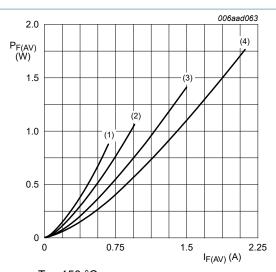
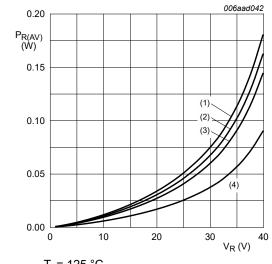


Fig. 6. Diode capacitance as a function of reverse voltage; typical values



 $T_j = 150 \,^{\circ}\text{C}$ (1)  $\delta = 0.1$ (2)  $\delta = 0.2$ (3)  $\delta = 0.5$ (4)  $\delta = 1$ 

Fig. 7. Average forward power dissipation as a function of average forward current; typical values

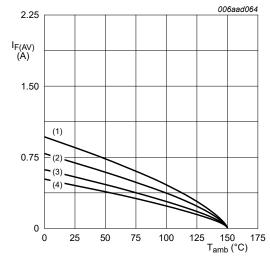


 $T_j = 125 \,^{\circ}\text{C}$ (1)  $\delta = 1$ (2)  $\delta = 0.9$ 

 $(3) \delta = 0.8$ 

 $(4) \delta = 0.5$ 

Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

T<sub>i</sub> = 150 °C

 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

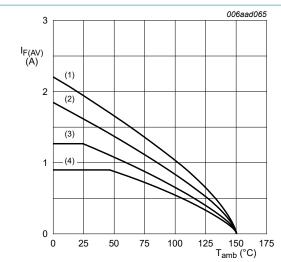
(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values

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FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

 $T_i = 150 \, ^{\circ}C$ 

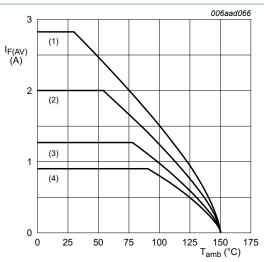
 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

T<sub>i</sub> = 150 °C

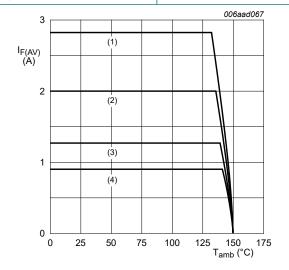
 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



T<sub>i</sub> = 150 °C

 $(1) \delta = 1 (DC)$ 

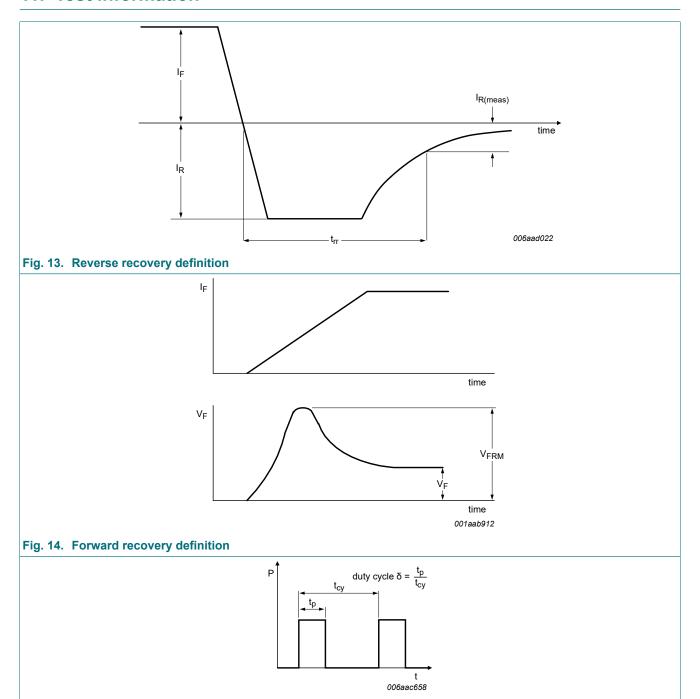
(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 12. Average forward current as a function of solder point temperature; typical values

### 11. Test information



The current ratings for the typical waveforms are calculated according to the equations:

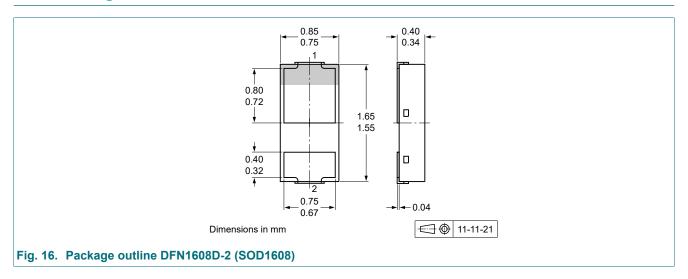
 $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,

 $I_{RMS} = I_{F(AV)}$  at DC,

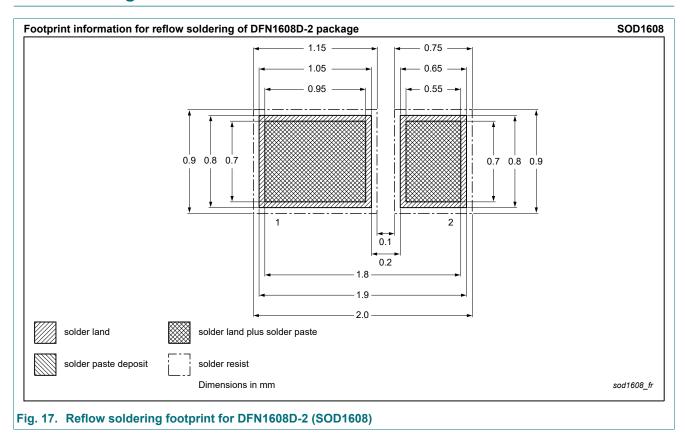
 $I_{RMS} = I_{M} \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

Fig. 15. Duty cycle definition

### 12. Package outline



### 13. Soldering



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## 14. Revision history

#### **Table 8. Revision history**

Table 6. Nevision mistory									
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes					
PMEG4020EPK v.3	20250930	Product data sheet	-	PMEG4020EPK v.2					
Modifications:	<ul> <li>Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).</li> </ul>								
PMEG4020EPK v.2	20140210	Product data sheet	-	PMEG4020EPK v.1					
PMEG4020EPK v.1	20120425	Product data sheet	-	-					

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#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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