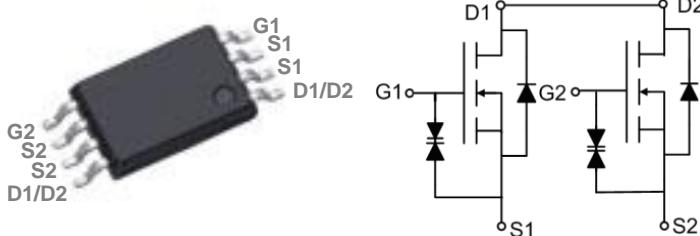


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TSSOP8 Dual Pin Configuration



BVDSS	RDSON	ID
20V	20mΩ	6.3A

Features

- 20V, 6.3A, RDS(ON) 20mΩ@VGS = 4.5V
- G-S ESD Diode Embedded
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Notebook
- Load Switch
- LED applications

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{Gs}	Gate-Source Voltage	± 12	V
I _D	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	6.3	A
	Drain Current – Continuous ($T_A=70^\circ\text{C}$)	5.0	A
I _{DM}	Drain Current – Pulsed ¹	25.2	A
P _D	Power Dissipation ($T_A=25^\circ\text{C}$)	1.25	W
	Power Dissipation – Derate above 25°C	0.01	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	100	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	20	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.02	---	$\text{V}/^\circ\text{C}$
$I_{\text{DS}}^{\text{SS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=85^\circ\text{C}$	---	---	30	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 10\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 10	μA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=4.5\text{V}$, $I_D=0.5\text{A}$	---	16	20	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$, $I_D=0.5\text{A}$	---	19	25	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	0.45	0.7	1	V
			---	-2.7	---	$\text{mV}/^\circ\text{C}$

Dynamic and switching Characteristics

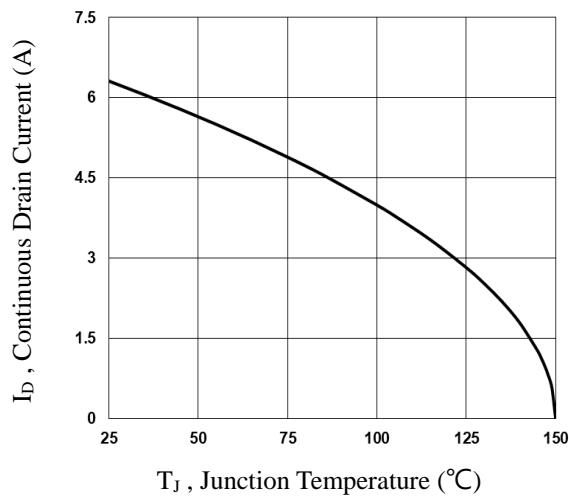
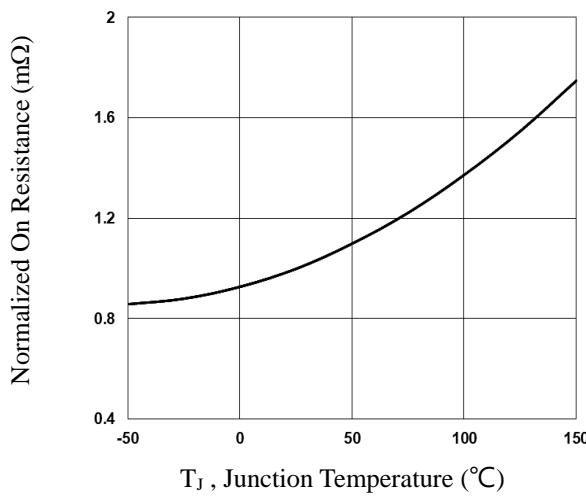
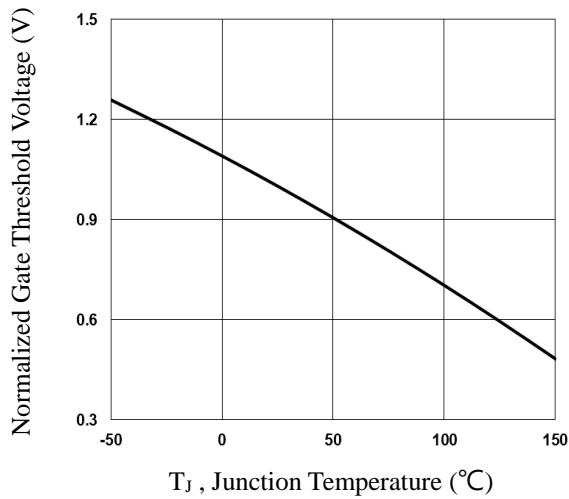
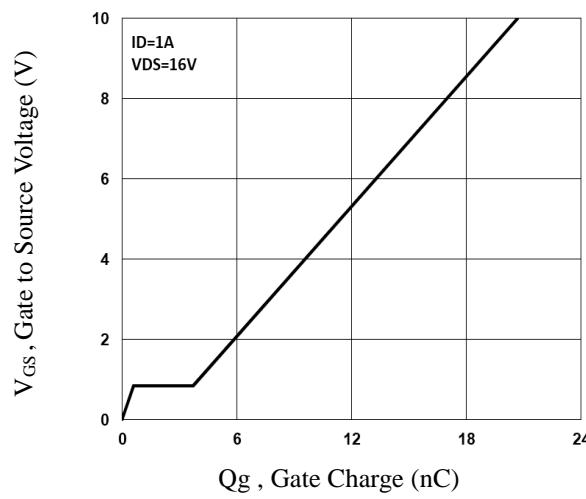
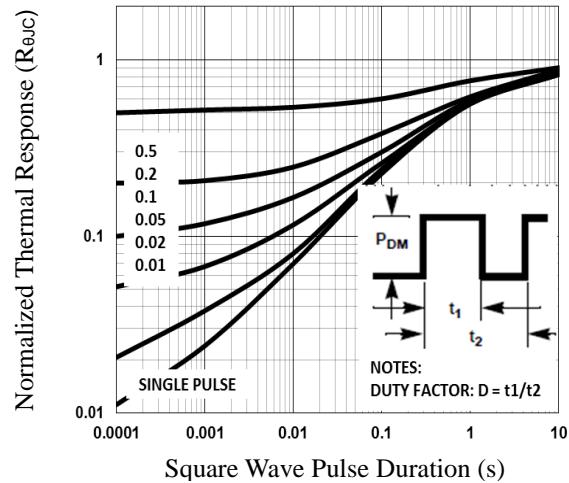
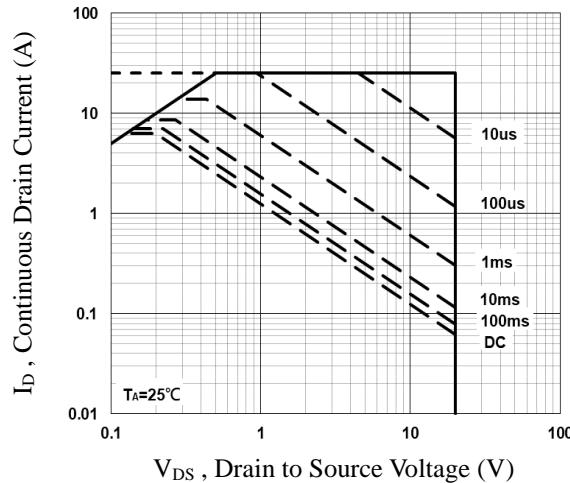
Q_g	Total Gate Charge ^{2, 3}	$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=4.5\text{V}$, $I_D=1\text{A}$	---	9.3	20	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	0.6	2	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	3.1	6	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2, 3}	$V_{\text{DD}}=10\text{V}$, $V_{\text{GS}}=4.5\text{V}$, $R_G=6\Omega$, $I_D=1\text{A}$	---	4.6	9	ns
T_r	Rise Time ^{2, 3}		---	13.4	26	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2, 3}		---	28	56	
T_f	Fall Time ^{2, 3}		---	8.7	18	
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	118	330	pF
C_{oss}	Output Capacitance		---	88	180	
C_{rss}	Reverse Transfer Capacitance		---	17	40	

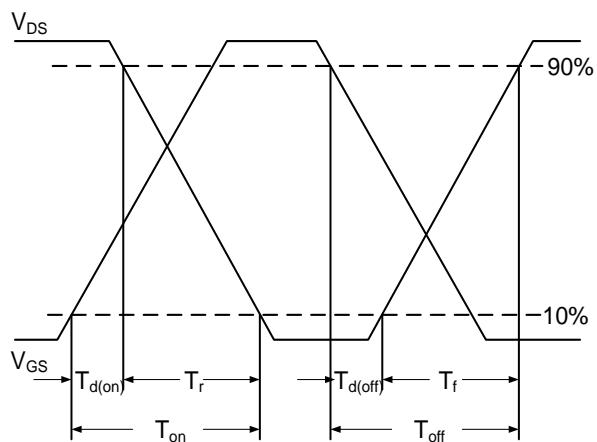
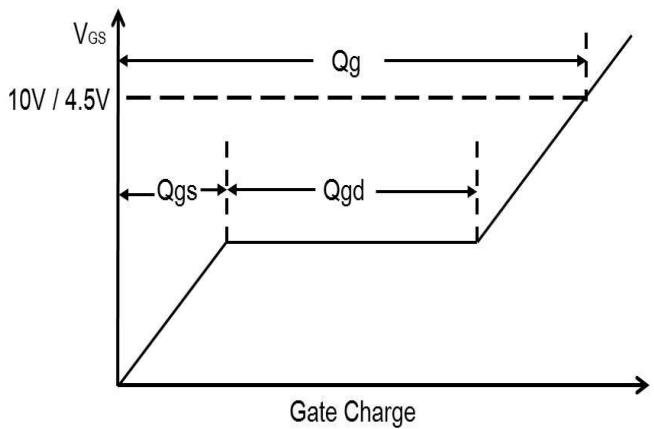
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	6.3	A
			---	---	12.6	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.3	V

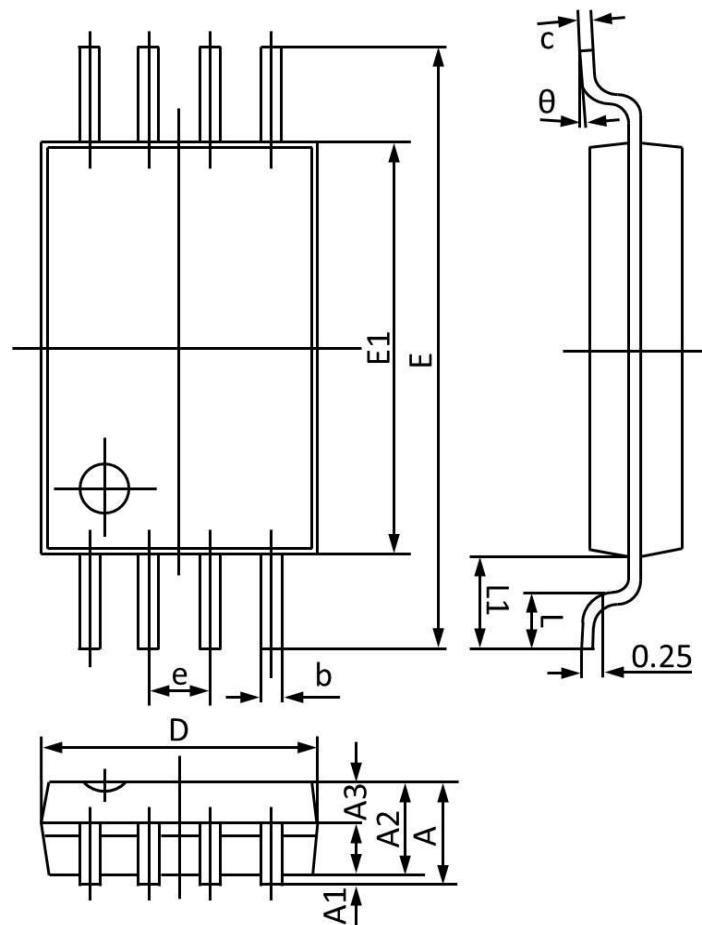
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_J

Fig.2 Normalized $R_{DS(on)}$ vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Characteristics

Fig.5 Normalized Transient Impedance

Fig.6 Maximum Safe Operation Area


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

TSSOP8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.100	1.200	0.044	0.047
A1	0.050	0.150	0.002	0.006
A2	0.900	1.050	0.036	0.041
A3	0.390	0.490	0.016	0.019
b	0.210	0.300	0.009	0.011
c	0.130	0.190	0.006	0.007
D	0.120	0.140	0.004	0.006
E	6.200	6.600	0.244	0.260
E1	4.300	4.500	0.169	0.177
e	0.650(BSC)		0.025(BSC)	
L	0.450	0.750	0.018	0.029
L1	1.000(BSC)		0.039(BSC)	
θ	0°	8°	0°	8°