

PMN30UN 30 V, N-channel Trench MOSFET 29 January 2016

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Trench MOSFET technology
- Low threshold voltage
- Very fast switching
- Enhanced power dissipation capability of 1240 mW

3. Applications

- LED driver
- Power management
- Low-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Quie	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	30	V
V _{GS}	gate-source voltage	-		-12	-	12	V
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	5.7	А
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = 4.5 V; I _D = 4.5 A; T _j = 25 °C		-	33	40	mΩ

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain	6 5 4	D
2	D	drain		
3	G	gate		G-U-U-U-U-U-U-U-U-U-U-U-U-U-U-U-U-U-U-U
4	S	source	TSOP6 (SOT457)	\$ 017aaa253
5	D	drain	-	077888205
6	D	drain	-	

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMN30UN	TSOP6	plastic surface-mounted package (TSOP6); 6 leads	SOT457			

7. Marking

Table 4	4. Marking codes	
Туре	number	Marking code
PMN	30UN	H2

8. Limiting values

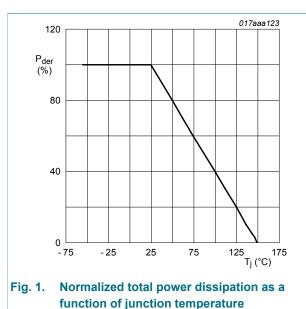
Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

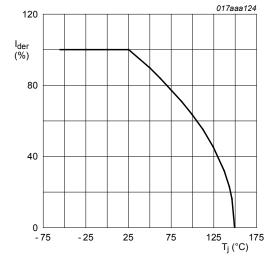
Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V_{GS}	gate-source voltage			-12	12	V
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	5.7	А
		V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	4.5	А
		V_{GS} = 4.5 V; T_{amb} = 100 °C	[1]	-	2.9	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	18	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	530	mW
			[1]	-	1.24	W
		T _{sp} = 25 °C		-	4.46	W
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-dra	in diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	1.2	А

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



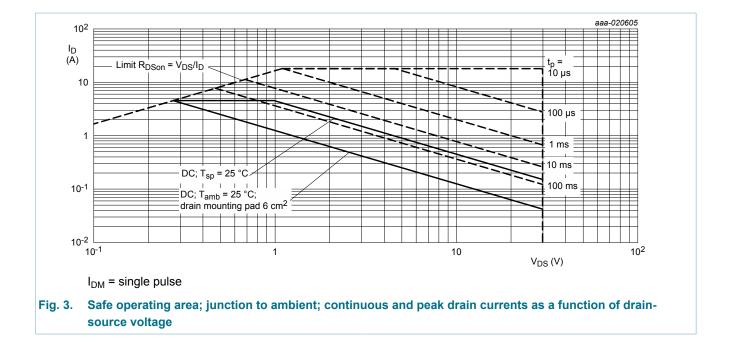
 $P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$





$$I_{der} = \frac{I_D}{I_D(25^{\circ}C)} \times 100 \%$$

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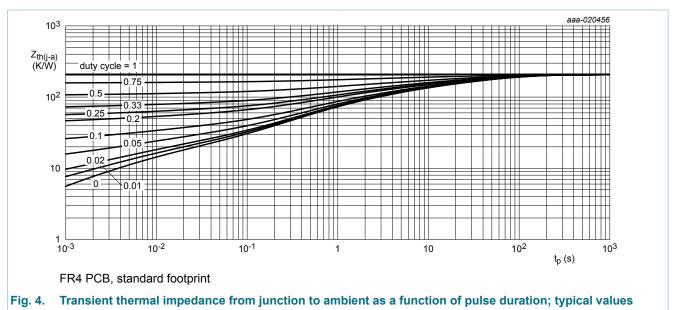
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Thermal characteristics 9.

Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	205	235	K/W
			[2]	-	88	101	K/W
		in free air; t ≤ 5 s	[2]	-	55	63	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	24	28	K/W

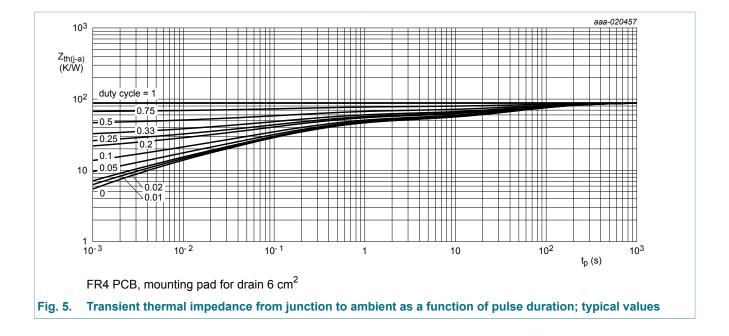
Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint. [1] [2]

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².



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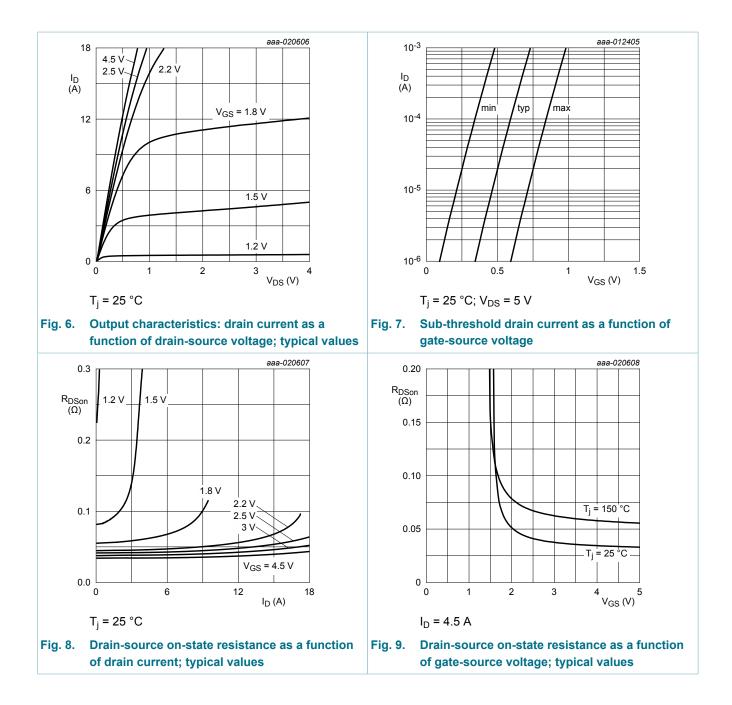


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10. Characteristics

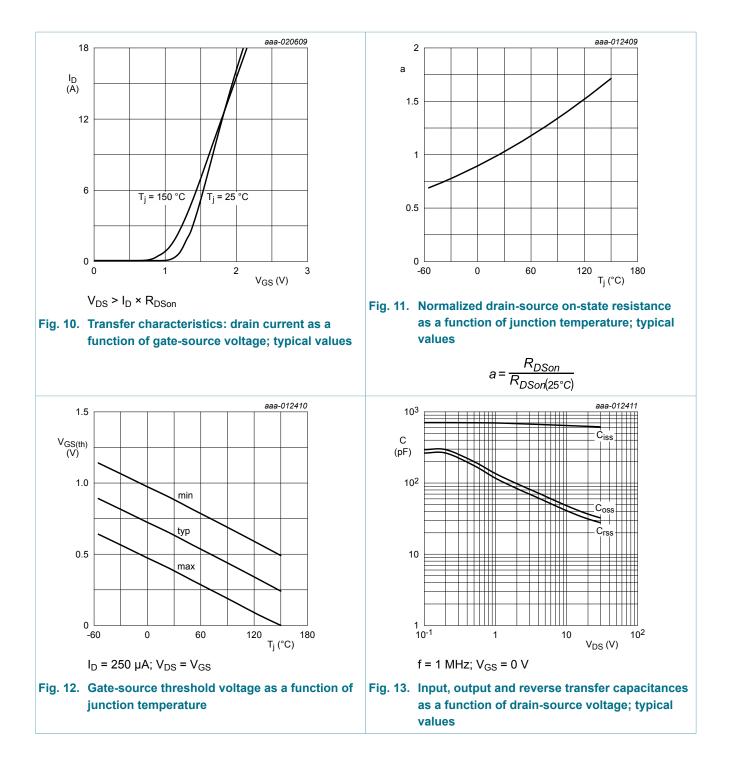
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	11				
V _{(BR)DSS}	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C	30	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} =V _{GS} ; T _j = 25 °C	0.4	0.65	0.9	V
I _{DSS}	drain leakage current	V_{DS} = 30 V; V_{GS} = 0 V; T_j = 25 °C	-	-	1	μA
I _{GSS} gate leakag	gate leakage current	V_{GS} = 12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
		V_{GS} = -12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-100	nA
Been	drain-source on-state	V _{GS} = 4.5 V; I _D = 4.5 A; T _j = 25 °C	-	33	40	mΩ
	resistance	V _{GS} = 4.5 V; I _D = 4.5 A; T _j = 150 °C	-	50	61	mΩ
		V _{GS} = 2.5 V; I _D = 4 A; T _j = 25 °C	-	42	52	mΩ
		V _{GS} = 1.8 V; I _D = 1 A; T _j = 25 °C	-	54	75	mΩ
		V _{GS} = 1.5 V; I _D = 0.1 A; T _j = 25 °C	-	85	200	mΩ
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 2 A; T _j = 25 °C	-	12.6	-	S
R _G	gate resistance	f = 1 MHz; T _j = 25 °C	-	8.7	-	Ω
Dynamic ch	aracteristics	· · · · ·			1	
Q _{G(tot)}	total gate charge	V_{DS} = 15 V; I _D = 3.7 A; V _{GS} = 4.5 V;	-	7	12	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.9	-	nC
Q _{GD}	gate-drain charge	-	-	1.7	-	nC
C _{iss}	input capacitance	V_{DS} = 15 V; f = 1 MHz; V_{GS} = 0 V;	-	635	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	40	-	pF
C _{rss}	reverse transfer capacitance		-	35	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 15 V; I _D = 3.7 A; V _{GS} = 4.5 V;	-	9	-	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	23	-	ns
t _{d(off)}	turn-off delay time		-	34	-	ns
t _f	fall time		-	12	-	ns
Source-drai	in diode	· · · · · · · · · · · · · · · · · · ·	I	-1		
V _{SD}	source-drain voltage	I _S = 1.2 A; V _{GS} = 0 V; T _j = 25 °C	-	0.7	1.2	V

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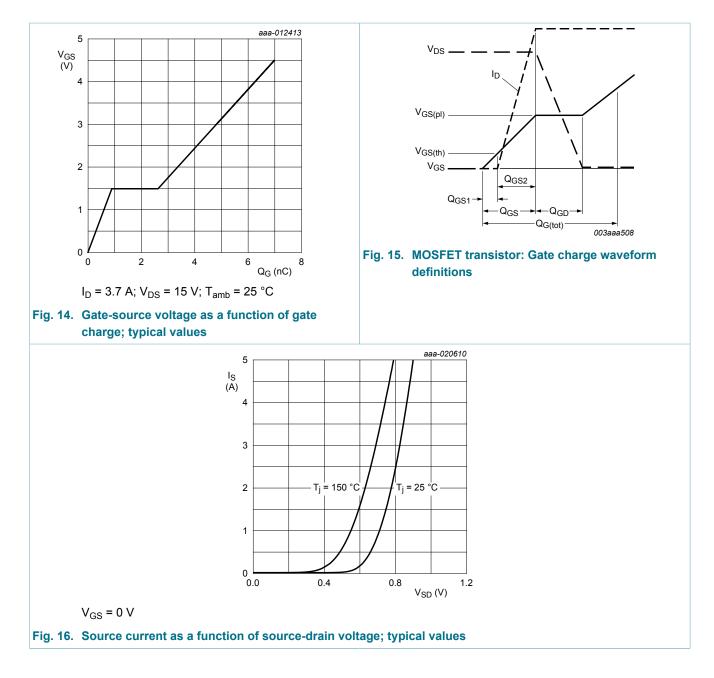
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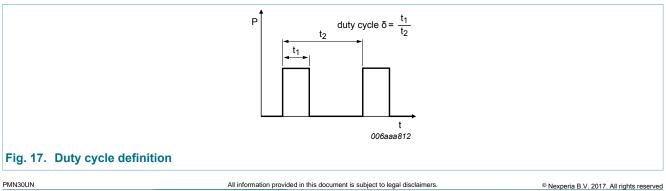


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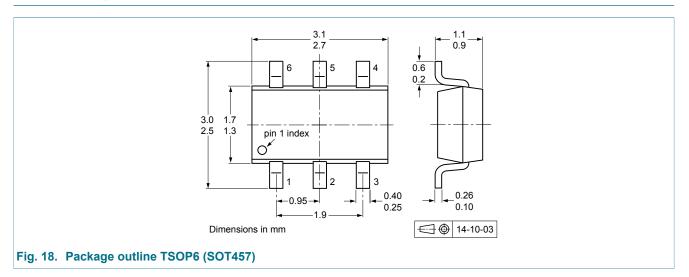


11. Test information

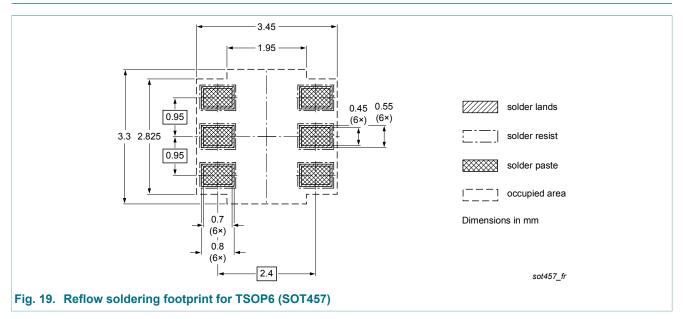


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12. Package outline

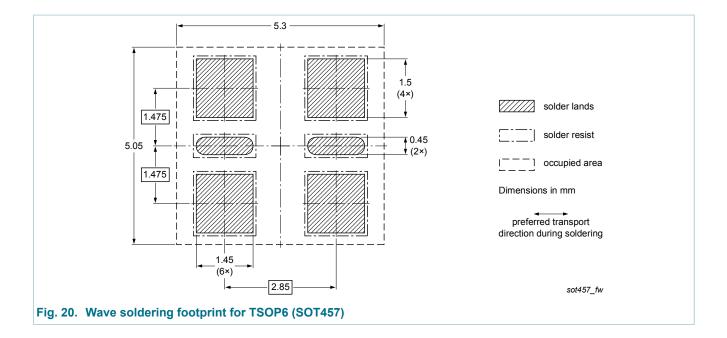


13. Soldering



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14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMN30UN v.1	20160129	Product data sheet	-	-

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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