



NPN switching transistor 15 May 2025

1. General description

NPN switching transistor in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High current (max. 600 mA)
- Low voltage (max. 40 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

• General purpose switching and linear amplification, especially in portable equipment

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	40	V
I _C	collector current		-	-	600	mA
h _{FE}	DC current gain	V_{CE} = 1 V; I _C = 10 mA; T _{amb} = 25 °C	80	-	-	

5. Pinning information

Table 2	. Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	E	emitter		С
3	С	collector		вК
			1 2 SC-70 (SOT323)	E sym123



6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMST4401-Q	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	<u>SOT323</u>			

7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PMST4401-Q	%2X

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	600	mA
I _{CM}	peak collector current			-	600	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

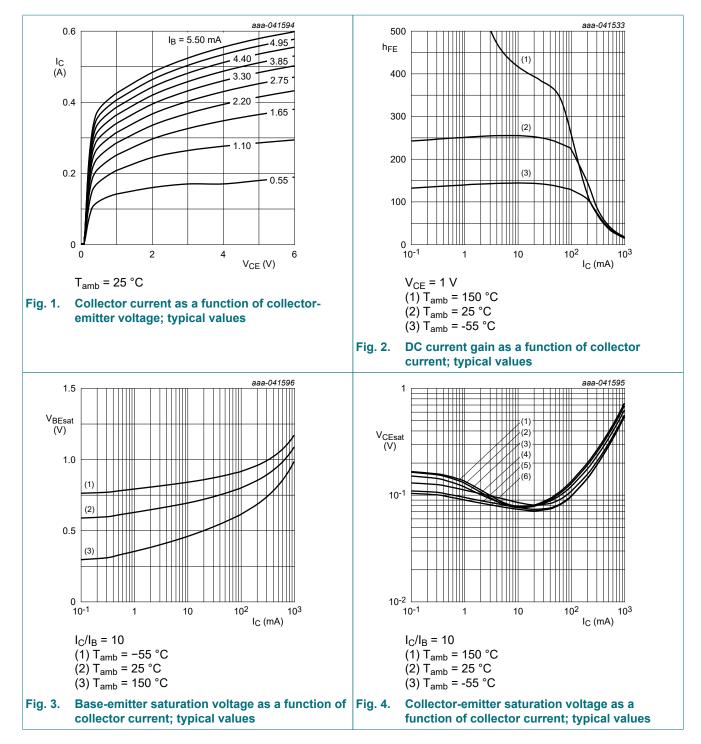
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 60 V; I _E = 0 A; T _{amb} = 25 °C	-	-	50	nA
	current	V _{CB} = 60 V; I _E = 0 A; T _j = 150 °C	-	-	10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 6 V; I _C = 0 A; T _{amb} = 25 °C	-	-	50	nA
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 0.1 mA; T _{amb} = 25 °C	20	-	-	
		V _{CE} = 1 V; I _C = 1 mA; T _{amb} = 25 °C	40	-	-	
		V _{CE} = 1 V; I _C = 10 mA; T _{amb} = 25 °C	80	-	-	
		V_{CE} = 1 V; I _C = 150 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	100	-	300	
		$V_{CE} = 2 \text{ V}; I_C = 500 \text{ mA}; \text{ pulsed}; t_p \le 300 \mu\text{s}; \delta \le 0.02; T_{amb} = 25 ^\circ\text{C}$	40	-	-	
0 Loui	collector-emitter saturation voltage	I_{C} = 150 mA; I_{B} = 15 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	400	mV
		I_{C} = 500 mA; I_{B} = 50 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	750	mV
V _{BEsat}	base-emitter saturation voltage	I_{C} = 150 mA; I_{B} = 15 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	950	mV
		I_{C} = 500 mA; I_{B} = 50 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	1.2	V
C _c	collector capacitance	$V_{CB} = 5 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ i}_{e} = 0 \text{ A}; \text{ f} = 1 \text{ MHz};$ $T_{amb} = 25 ^{\circ}\text{C}$	-	-	8	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	30	pF
f _T	transition frequency	V _{CE} = 10 V; I _C = 20 mA; f = 100 MHz; T _{amb} = 25 °C	250	-	-	MHz
Switching t	imes (between 10% and 90	% levels)		·		
t _d	delay time	I _C = 150 mA; I _{Bon} = 15 mA;	-	-	15	ns
t _r	rise time	I _{Boff} = -15 mA; T _{amb} = 25 °C	-	-	20	ns
t _{on}	turn-on time		-	-	35	ns
t _s	storage time		-	-	200	ns
t _f	fall time		-	-	60	ns
t _{off}	turn-off time		-	-	250	ns

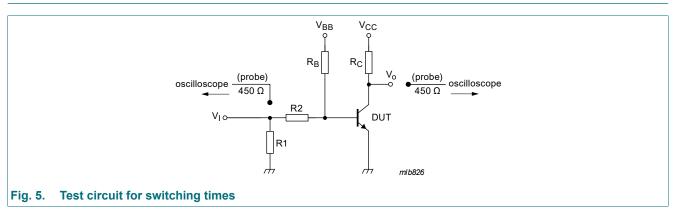
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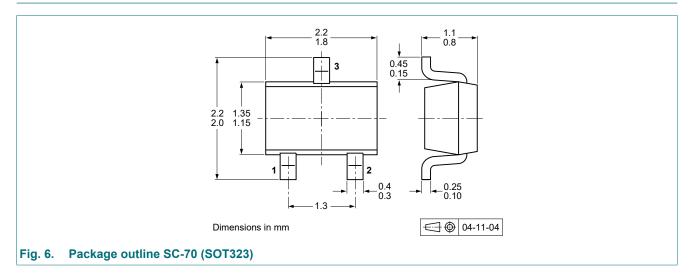
11. Test information



Quality information

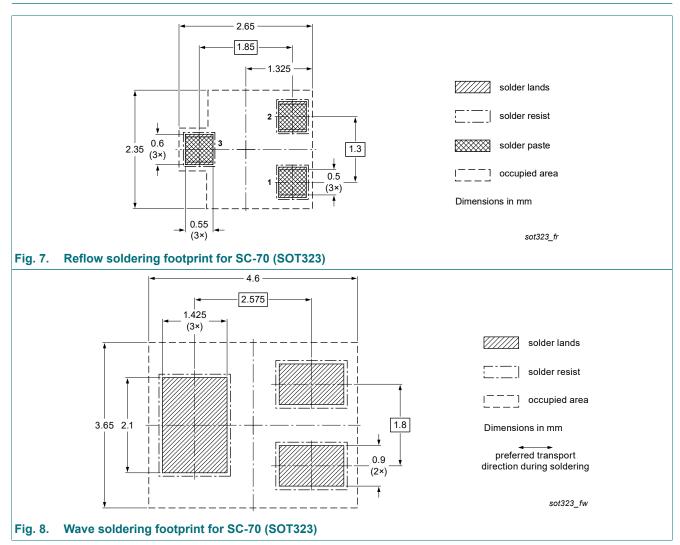
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



Product data sheet

14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMST4401-Q v.2	20250525	Product data sheet	-	PMST4401-Q v.1
Modificatons:		: Conditions corrected at t _{on} : Figures 1 - 4 added n adapted		
PMST4401-Q v.1	20240117	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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