

PMSTA55-Q

60 V, 500 mA PNP general-purpose transistor

24 January 2025

Product data sheet

1. General description

PNP transistor in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package. NPN complement: PMSTA05-Q

2. Features and benefits

- High current (max. 500 mA)
- Collector-emitter voltage: 60 V
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

• Intended for telephony and professional communication equipment.

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-60	V
I _C	collector current			-	-	-500	mA
h _{FE}	DC current gain	V _{CE} = -1 V; I _C = -10 mA; T _{amb} = 25 °C		100	-	-	

5. Pinning information

Table 2	. Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	E	emitter		С
3	С	collector		в
			1 2 SC-70 (SOT323)	E 006aab259



6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMSTA55-Q	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	<u>SOT323</u>			

7. Marking

Table 4. Marking codes						
Type number	Marking code[1]					
PMSTA55-Q	%2H					

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-60	V
V _{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-4	V
I _C	collector current			-	-500	mA
I _{CM}	peak collector current			-	-500	mA
I _{BM}	peak base current			-	-500	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

10. Characteristics

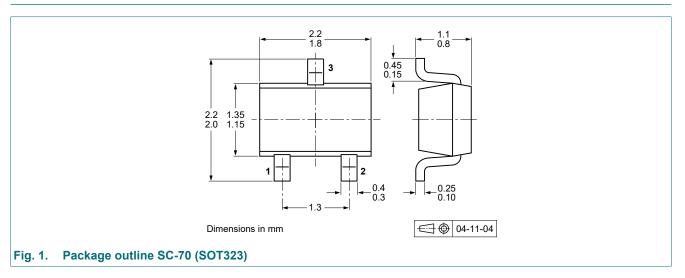
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -60 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = -4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-500	nA
h _{FE}	DC current gain	V _{CE} = -1 V; I _C = -10 mA; T _{amb} = 25 °C	100	-	-	
		V_{CE} = -1 V; I _C = -100 mA; t _p ≤ 300 µs; $\delta \le 0.02$; T _{amb} = 25 °C	100	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -100 mA; I _B = -10 mA; T _{amb} = 25 °C	-	-	-250	mV
V _{BE}	base-emitter voltage	V _{CE} = -1 V; I _C = -100 mA; T _{amb} = 25 °C	-	-	-1.2	V
f _T	transition frequency	V_{CE} = -1 V; I _C = -100 mA; f = 100 MHz; T _{amb} = 25 °C	50	-	-	MHz

11. Test information

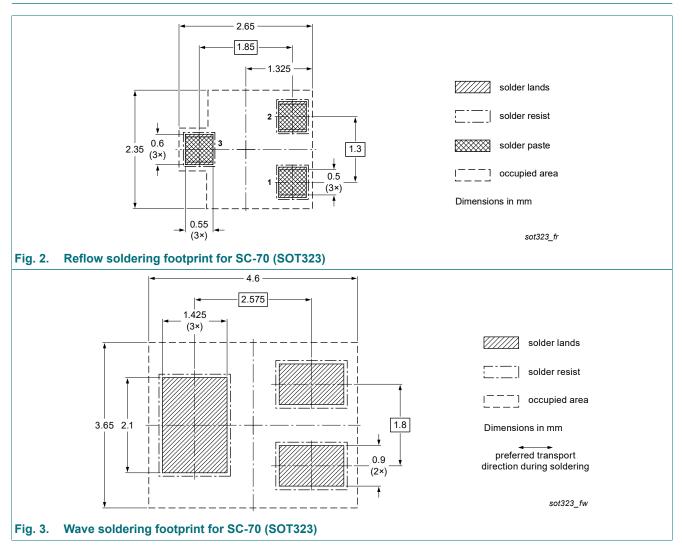
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



Product data sheet

14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMSTA55-Q v.1	20250124	Product data sheet	-	-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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