

General Description

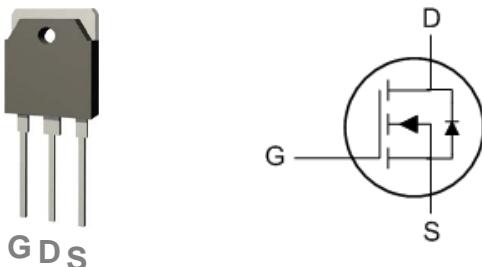
These N-Channel enhancement mode power field effect transistors are using advanced super junction technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply

BVDSS	RDS(ON)	ID
900V	1.2Ω	9A

Features

- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO-3P Pin Configuration



Applications

- High efficient switched mode power supplies
- TV Power
- Adapter/charger
- Server Power
- PV Inverter / UPS

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	900	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	9	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	5.7	A
I_{DM}	Drain Current – Pulsed ¹	36	A
EAS	Single Pulse Avalanche Energy ²	1390	mJ
IAS	Single Pulse Avalanche Current ²	16.7	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	240	W
	Power Dissipation – Derate above 25°C	1.92	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	50	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.52	$^\circ\text{C/W}$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	900	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	---	0.82	---	$\text{V}/^\circ\text{C}$
$I_{\text{DS}}^{\text{SS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=900\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=720\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=4\text{A}$	---	---	1.2	Ω
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	3	---	5	V
$\Delta V_{\text{GS(th)}}$	$V_{\text{GS(th)}}$ Temperature Coefficient		---	-9.4	---	$\text{mV}/^\circ\text{C}$

Dynamic and switching Characteristics

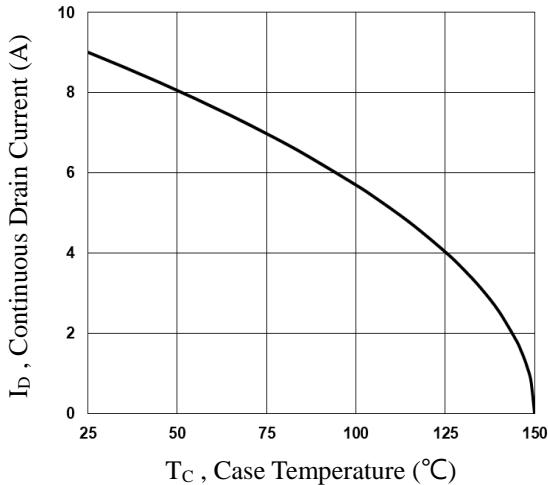
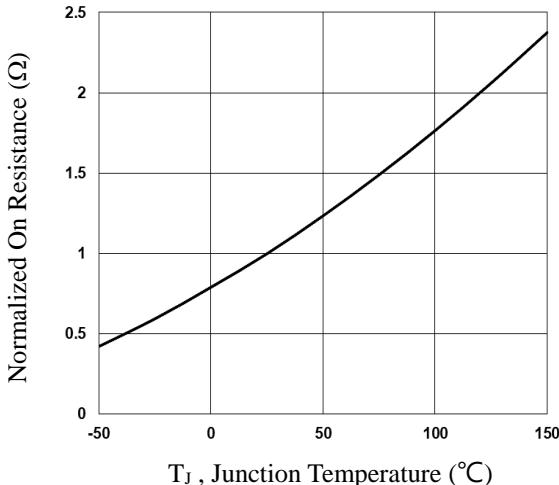
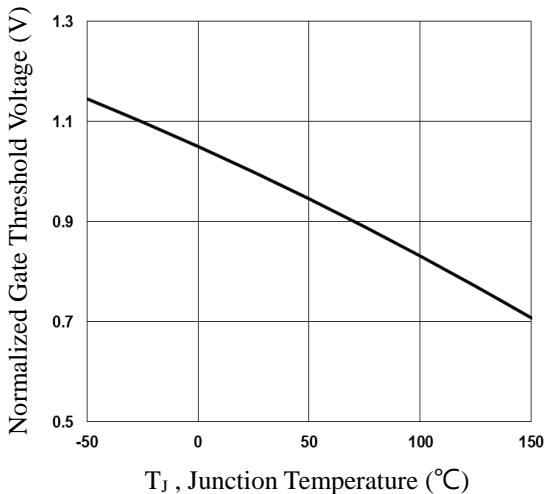
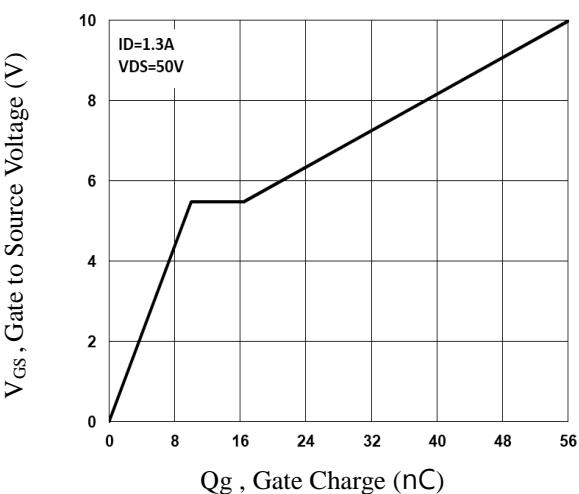
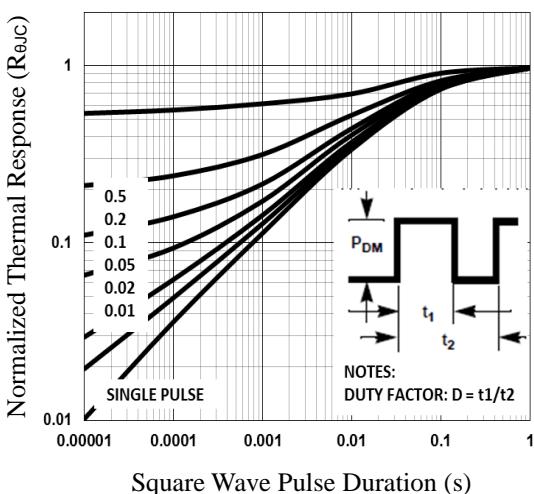
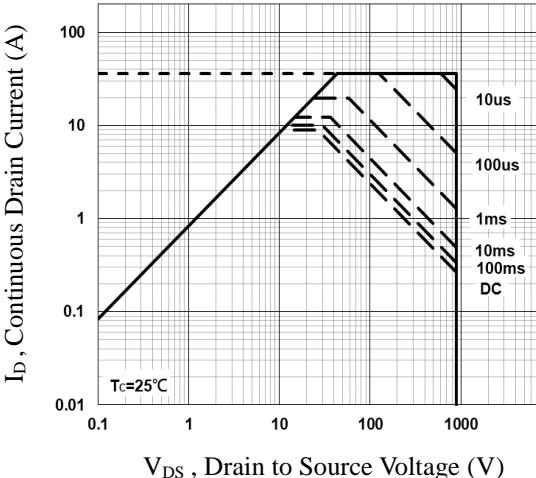
Q_g	Total Gate Charge ^{3,4}	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=1.3\text{A}$	---	56	---	nC
Q_{gs}	Gate-Source Charge ^{3,4}		---	10	---	
Q_{gd}	Gate-Drain Charge ^{3,4}		---	6.4	---	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{3,4}	$V_{\text{DD}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_{\text{G}}=25\Omega$	---	100	---	ns
T_r	Rise Time ^{3,4}		---	170	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{3,4}		---	410	---	
T_f	Fall Time ^{3,4}		---	175	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	2395	---	pF
C_{oss}	Output Capacitance		---	170	---	
C_{rss}	Reverse Transfer Capacitance		---	20	---	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	1.45	---	Ω

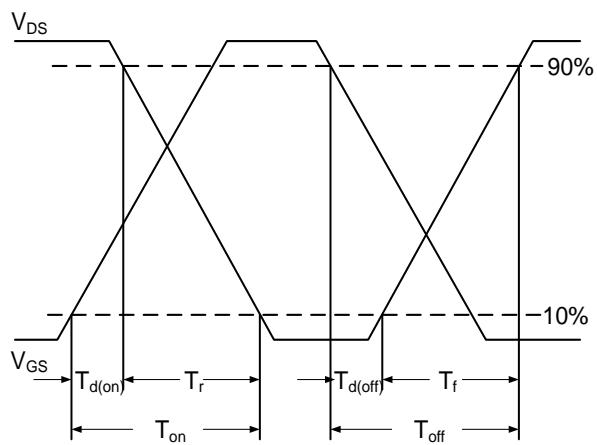
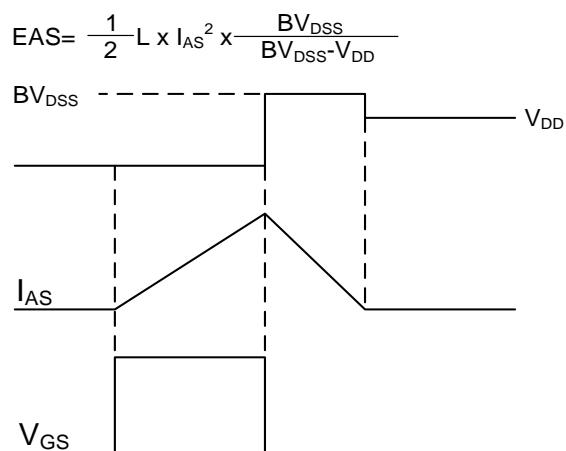
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	9	A
I_{SM}	Pulsed Source Current		---	---	18	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=9\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.4	V
t_{rr}	Reverse Recovery Time ³	$V_{\text{GS}}=0\text{V}$, $I_s=9\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$	---	550	---	ns
Q_{rr}	Reverse Recovery Charge ³	$T_J=25^\circ\text{C}$	---	6.5	---	nC

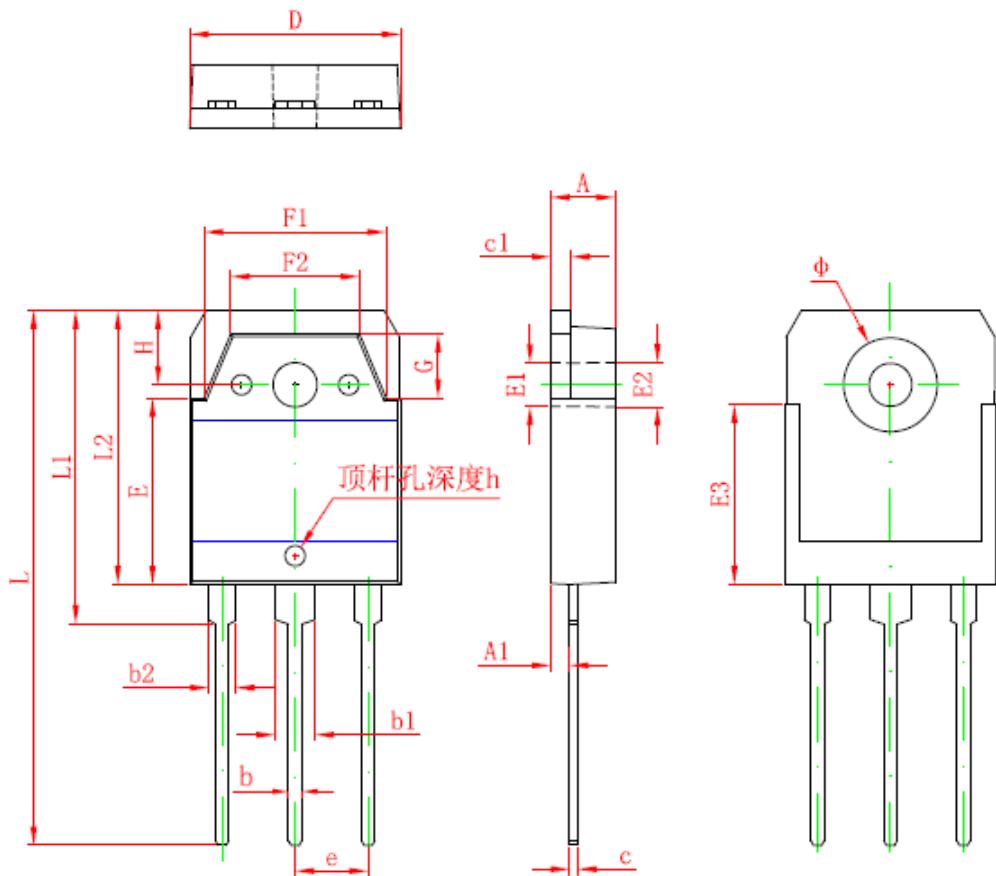
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}}=100\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=10\text{mH}$, $I_{\text{AS}}=16.7\text{A}$, $R_{\text{G}}=25\Omega$, Starting $T_J=25^\circ\text{C}$.
3. The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_c

Fig.2 Normalized $R_{DS(on)}$ vs. T_j

Fig.3 Normalized V_{th} vs. T_j

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Impedance

Fig.6 Maximum Safe Operation Area


Fig.7 Switching Time Waveform

Fig.8 EAS Waveform

TO-3P PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
A	4.600	5.000	E3	13.450REF	
A1	1.200	1.600	F1	13.400	13.800
b	0.800	1.200	F2	9.400	9.800
b1	2.800	3.200	L	39.900	40.300
b2	1.800	2.200	L1	23.200	23.600
c	0.500	0.700	L2	20.300	20.600
c1	1.450	1.650	Φ	6.900	7.100
D	15.450	15.850	G	5.550	5.150
E	13.700	14.100	e	5.000REF	
E1	3.200REF		H	5.000REF	
E2	3.300REF		h	0.300	0.000