**Product data sheet** 

# 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1006-3 (SOT883) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

#### 2. Features and benefits

- Trench MOSFET technology
- Low threshold voltage
- Very fast switching
- ElectroStatic Discharge (ESD) protection > 2 kV HBM
- Leadless ultra small SMD plastic package: 1.0 x 0.6 x 0.48 mm

# 3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	30	V
$V_{GS}$	gate-source voltage			-8	-	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	-	590	mA
Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS}$ = 4.5 V; $I_D$ = 590 mA; $T_j$ = 25 °C		-	550	670	mΩ

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm<sup>2</sup>.



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# 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	1	D I
2	S	source	2 🔲 📗 3	
3	D	drain	Transparent top view  DFN1006-3 (SOT883)	G S 017aaa255

# 6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMZ550UNE	DFN1006-3	DFN1006-3: leadless ultra small plastic package; 3 solder lands	SOT883			

# 7. Marking

Table 4. Marking codes

Type number	Marking code
PMZ550UNE	ZK

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# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	30	V
V <sub>GS</sub>	gate-source voltage			-8	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	590	mA
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	370	mA
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10$ μs		-	2.3	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	310	mW
			[1]	-	400	mW
		T <sub>sp</sub> = 25 °C		-	1670	mW
T <sub>j</sub>	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drain	diode					,
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	380	mA

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm<sup>2</sup>.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

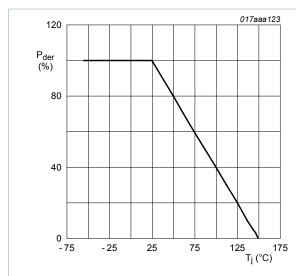


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

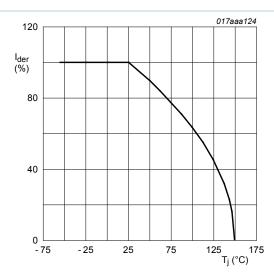


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

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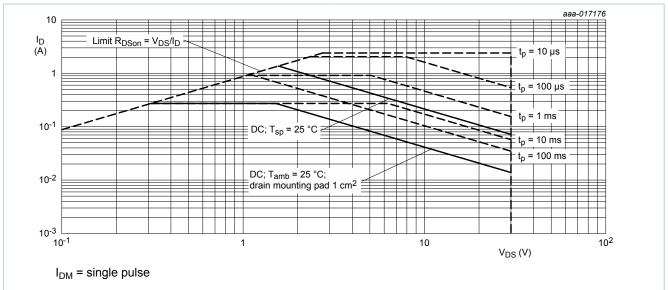


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	350	405	K/W
			[2]	-	270	310	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	65	75	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm<sup>2</sup>.

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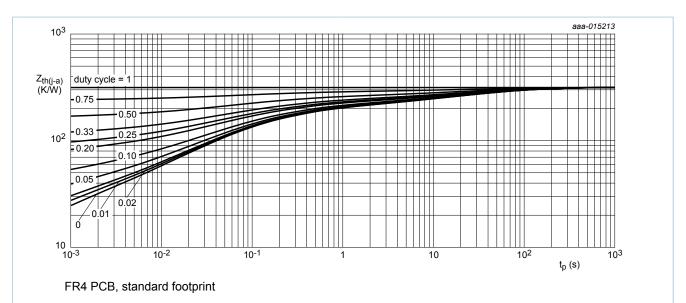
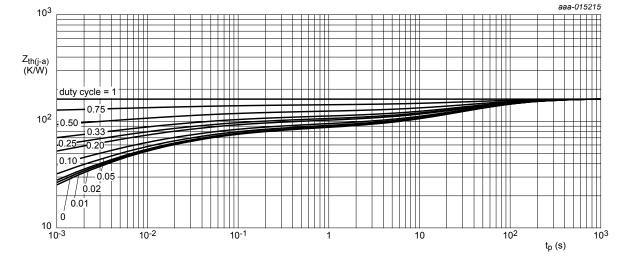


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for drain 1 cm<sup>2</sup>

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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# 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics		'			
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	30	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	0.45	0.7	0.95	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 30 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μΑ
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 8 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	5	μA
		V <sub>GS</sub> = -8 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-5	μA
		V <sub>GS</sub> = 4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μΑ
		V <sub>GS</sub> = -4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-1	μA
		V <sub>GS</sub> = 2.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
		V <sub>GS</sub> = -2.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-100	nA
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 590 mA; T <sub>j</sub> = 25 °C	-	550	670	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 590 mA; T <sub>j</sub> = 150 °C	-	960	1170	mΩ
		V <sub>GS</sub> = 2.5 V; I <sub>D</sub> = 590 mA; T <sub>j</sub> = 25 °C	-	660	900	mΩ
		V <sub>GS</sub> = 1.8 V; I <sub>D</sub> = 80 mA; T <sub>j</sub> = 25 °C	-	770	1120	mΩ
		V <sub>GS</sub> = 1.5 V; I <sub>D</sub> = 10 mA; T <sub>j</sub> = 25 °C	-	890	1500	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; $I_{D}$ = 590 mA; $T_{j}$ = 25 °C	-	600	-	mS
Dynamic ch	naracteristics					
Q <sub>G(tot)</sub>	total gate charge	V <sub>DS</sub> = 15 V; I <sub>D</sub> = 590 mA; V <sub>GS</sub> = 4.5 V;	-	0.6	1.1	nC
$Q_{GS}$	gate-source charge	T <sub>j</sub> = 25 °C	-	0.1	-	nC
$Q_{GD}$	gate-drain charge		-	0.1	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 15 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	30.3	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	5.8	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	4.2	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 15 V; $I_{D}$ = 590 mA; $V_{GS}$ = 4.5 V;	-	4	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega$ ; $T_j = 25 °C$	-	7	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	12	-	ns
t <sub>f</sub>	fall time		-	3	-	ns
Source-drai	in diode		I	1	1	1
V <sub>SD</sub>	source-drain voltage	$I_S$ = 380 mA; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	0.86	1.2	V

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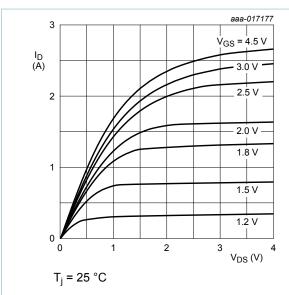
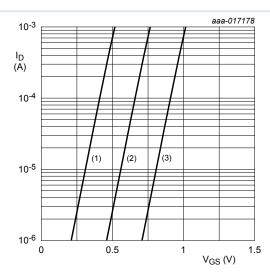


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values



 $T_i = 25 \,^{\circ}C; \, V_{DS} = 5 \,^{\circ}V$ 

- (1) minimum values
- (2) typical values
- (3) maximum values

Fig. 7. Sub-threshold drain current as a function of gate-source voltage

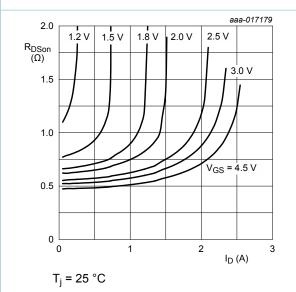


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

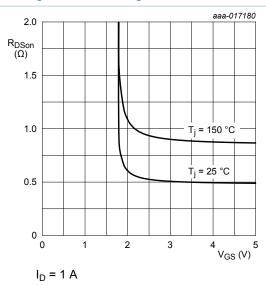


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

### 30 V, N-channel Trench MOSFET

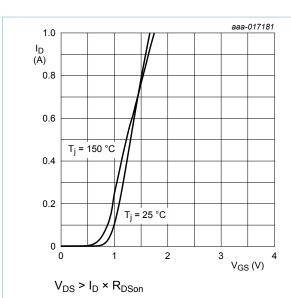


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

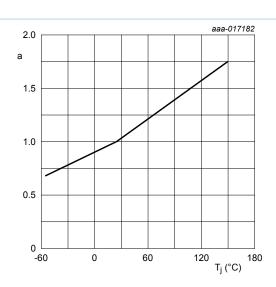
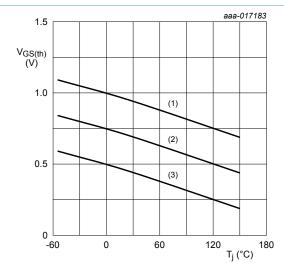


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

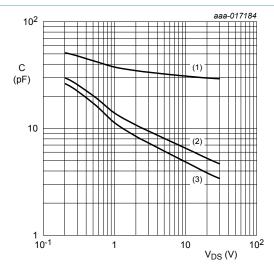
$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$



 $I_D = 0.25 \text{ mA}; V_{DS} = V_{GS}$ 

- (1) maximum values
- (2) typical values
- (3) minimum values

Fig. 12. Gate-source threshold voltage as a function of junction temperature



 $f = 1 MHz; V_{GS} = 0 V$ 

- (1) C<sub>iss</sub>
- (2) C<sub>oss</sub>
- (3)  $C_{rss}$

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

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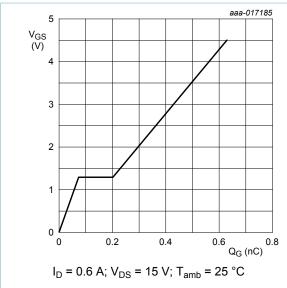


Fig. 15. Gate charge waveform definitions

Fig. 14. Gate-source voltage as a function of gate charge; typical values

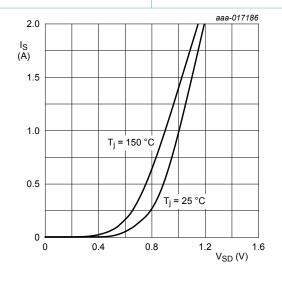
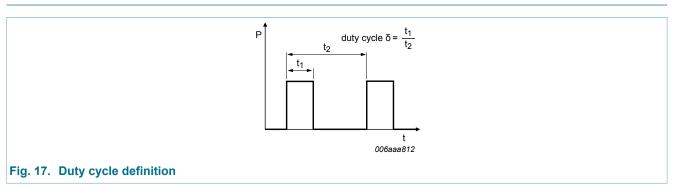


Fig. 16. Source current as a function of source-drain voltage; typical values

# 11. Test information

 $V_{GS} = 0 V$ 



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# 12. Package outline

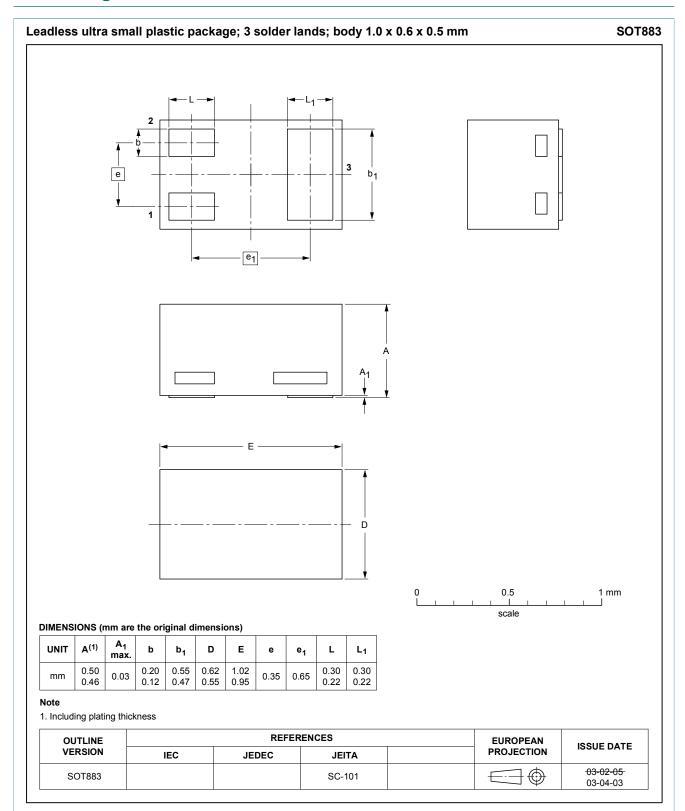
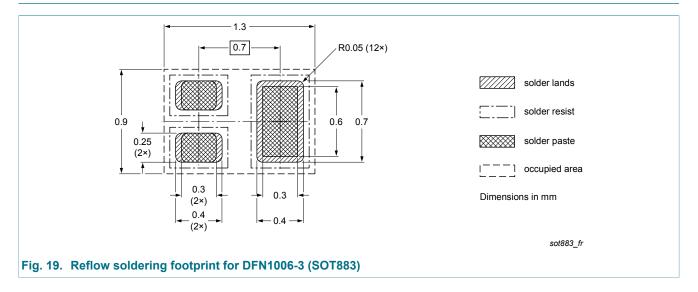


Fig. 18. Package outline DFN1006-3 (SOT883)

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# 13. Soldering



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# 14. Revision history

## Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMZ550UNE v.1	20150325	Product data sheet	-	-

#### 30 V, N-channel Trench MOSFET

## 15. Legal information

#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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