

Electrical Characteristics (Unless otherwise specified, condition shall be $V_{IN}=12V$, $I_O=0.5A$, $V_O=5V$, ON-OFF terminals is open, $T_a=25^\circ C$)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output saturation voltage	V_{SAT}	$I_{SW}=3A$	-	1.4	1.8	V
Reference voltage	V_{ref}	-	1.235	1.26	1.285	V
Reference voltage temperature fluctuation	ΔV_{ref}	$T_j=0$ to $125^\circ C$	-	± 0.5	-	%
Load regulation	$ R_{egL} $	$I_O=0.5$ to $3A$	-	0.2	1.5	%
Line regulation	$ R_{egI} $	$V_{IN}=8$ to $35V$	-	1	2.5	%
Efficiency	η	$I_O=3A$	-	80	-	%
Oscillation frequency	f_O	-	135	150	165	kHz
Oscillation frequency temperature fluctuation	Δf_O	$T_j=0$ to $125^\circ C$	-	± 2	-	%
Overcurrent detecting level	I_L	-	3.6	4.7	5.8	A
Charge current	I_{CHG}	②, ④ terminals is open, ⑤ terminal	-	-10	-	μA
Threshold input voltage	V_{THL}	Duty ratio=0%, ④ terminal=0V, ⑤ terminal	-	1.3	-	V
	V_{THH}	Duty ratio=100%, ④ terminals is open, ⑤ terminal	-	2.3	-	V
ON threshold voltage	$V_{TH(ON)}$	④ terminal=0V, ⑤ terminal	0.7	0.8	0.9	V
Stand-by current	I_{SD}	$V_{IN}=40V$, ⑤ terminal=0V	-	140	400	μA
Output OFF-state consumption current	I_{QS}	$V_{IN}=40V$, ⑤ terminal=0.9V	-	8	16	mA

Fig.1 Standard Test Circuit

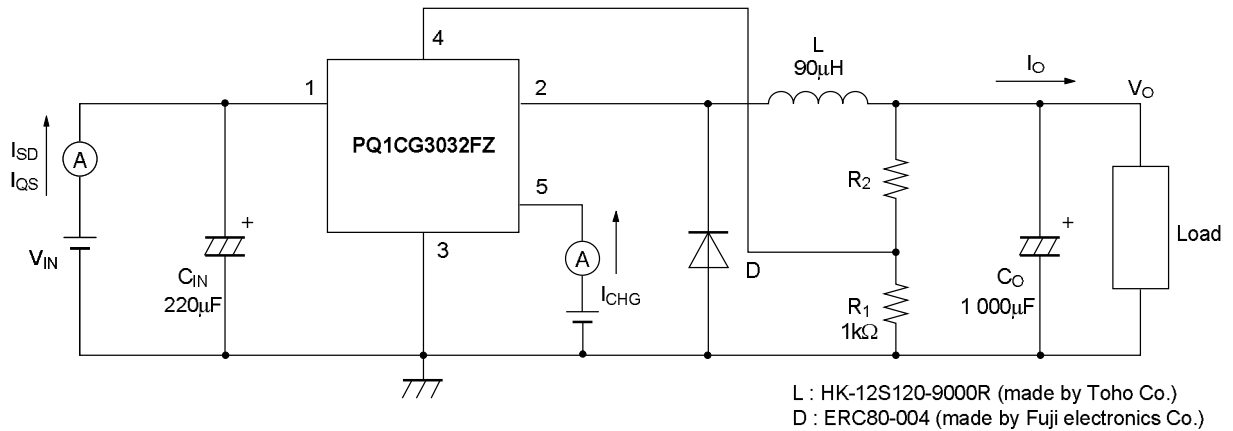


Fig.2 Power Dissipation vs. Ambient Temperature

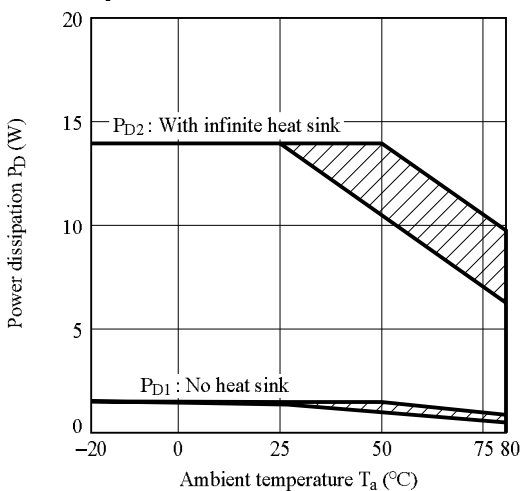


Fig.3 Overcurrent Protection Characteristics (Typical Value)

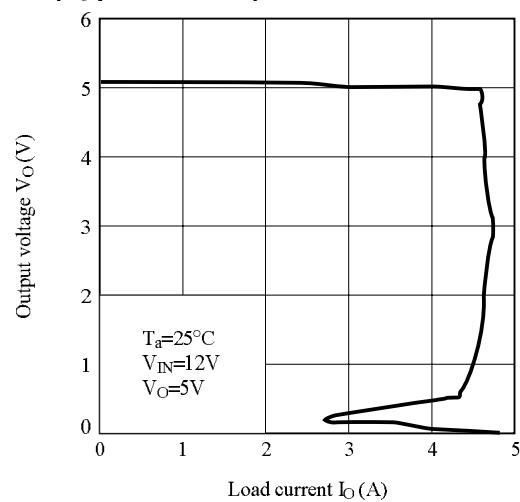


Fig.4 Efficiency vs. Input Voltage

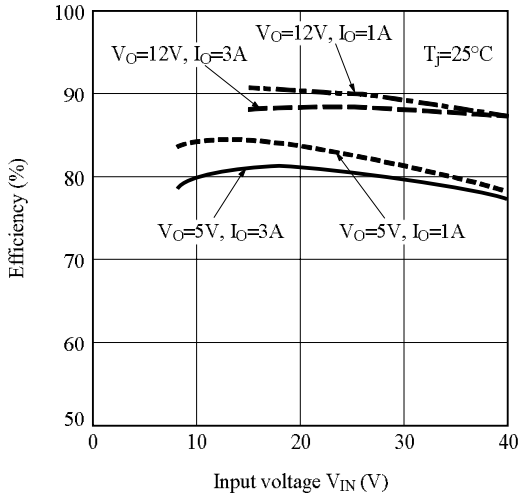


Fig.5 Output Saturation Voltage vs. Switching Current

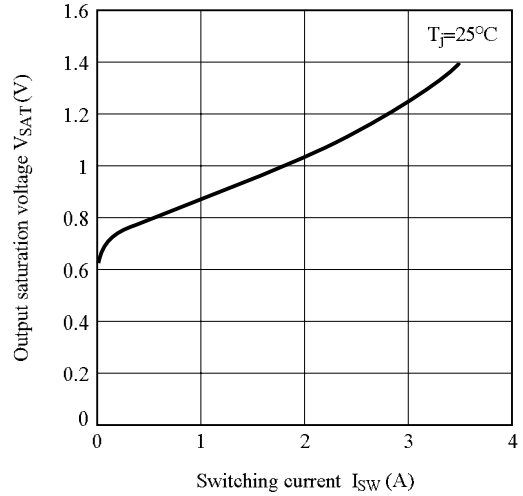


Fig.6 Stand by Current vs. Input Voltage

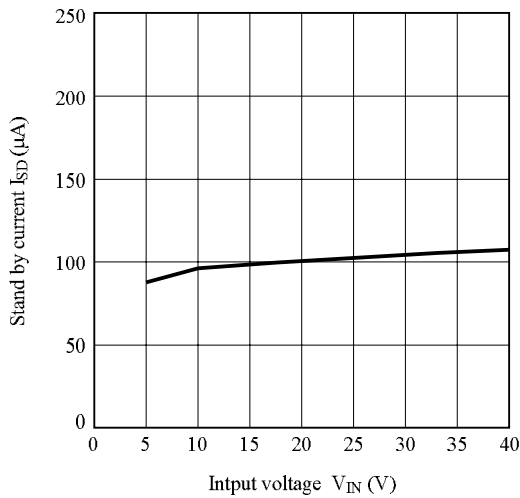


Fig.7 Reference Voltage Fluctuation vs. Junction Temperature

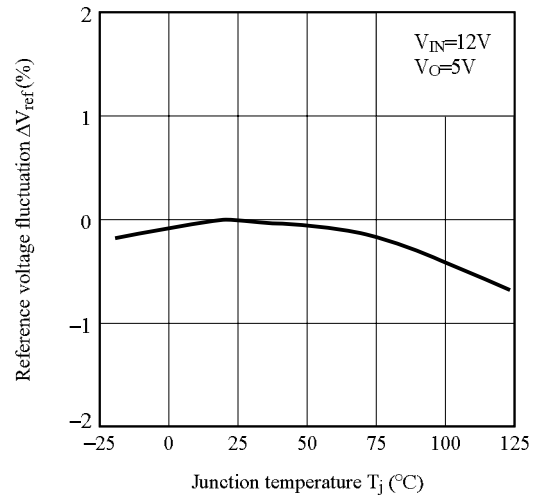


Fig.8 Load Regulation vs. Output Current

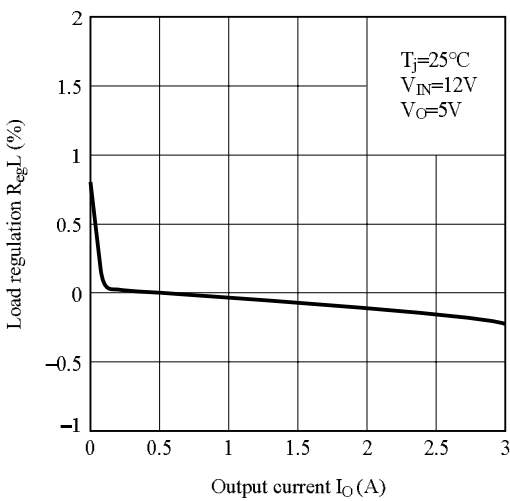


Fig.9 Line Regulation vs. Input Voltage

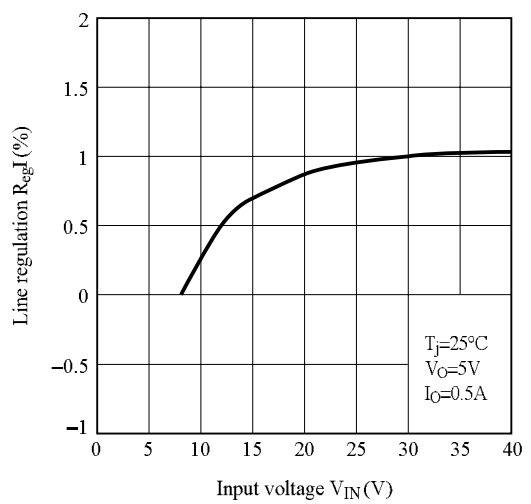


Fig.10 Oscillation Frequency Fluctuation vs. Junction Temperature

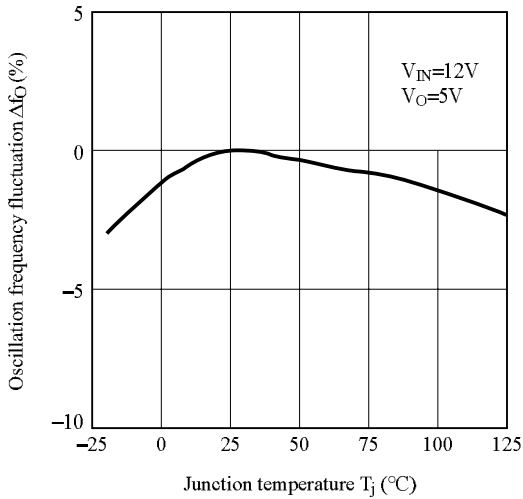


Fig.11 Overcurrent Detection Level Fluctuation vs. Junction Temperature

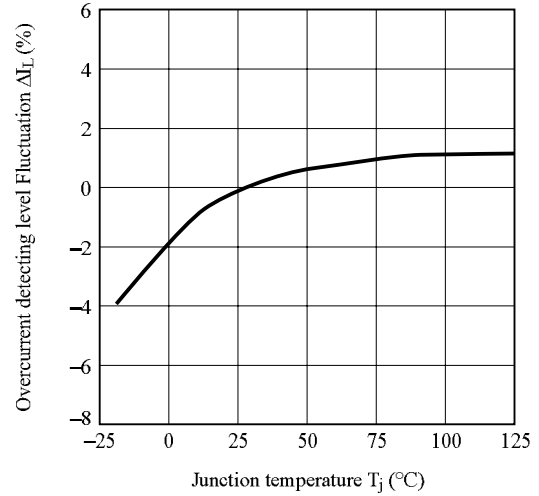


Fig.12 Threshold Voltage vs. Junction Temperature

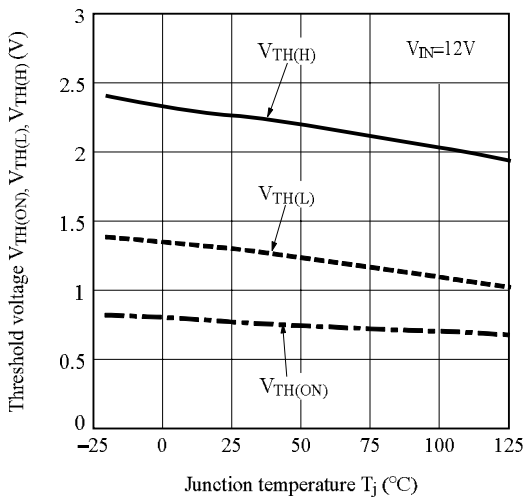


Fig.13 Operating Consumption Current vs. Input Voltage

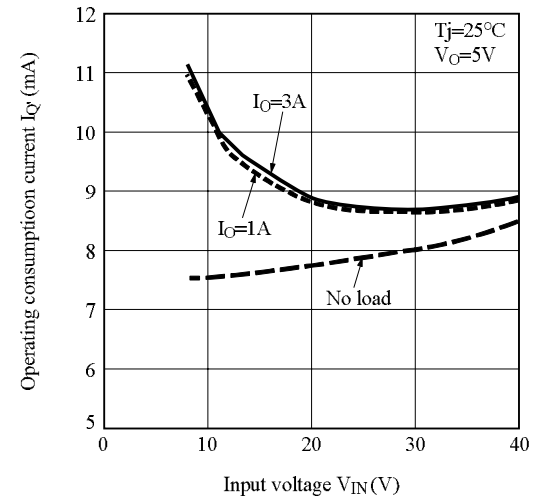


Fig.14 Block Diagram

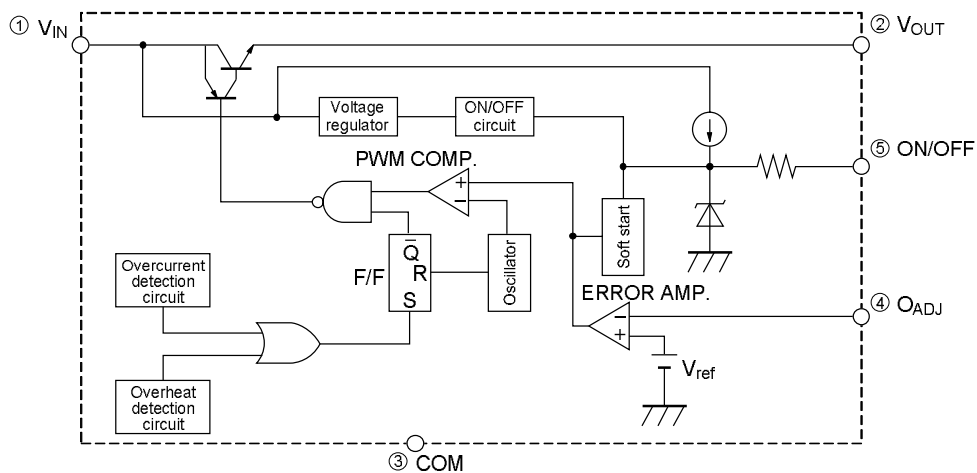


Fig.15 Step Down Type Circuit Diagram

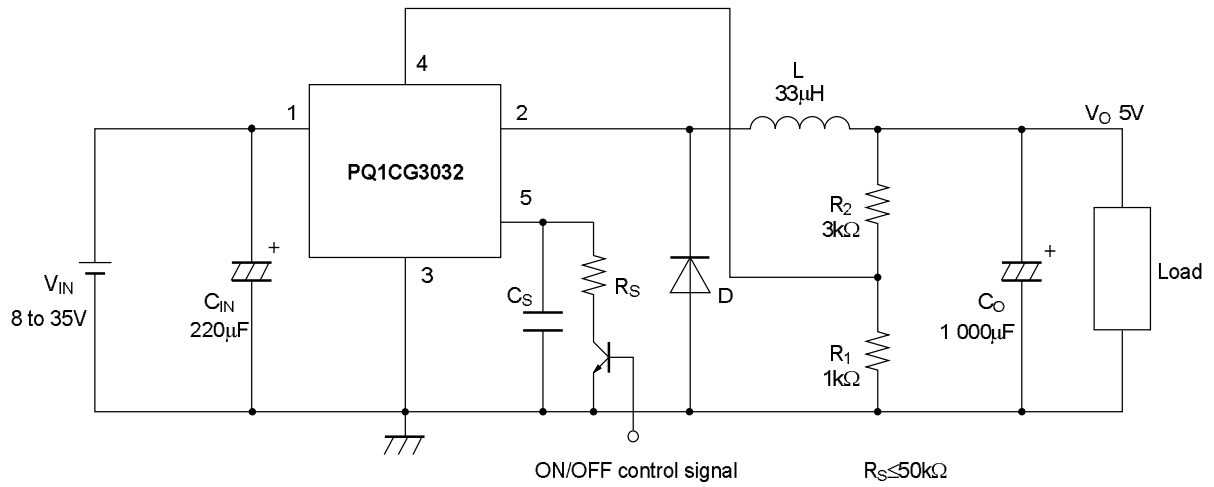
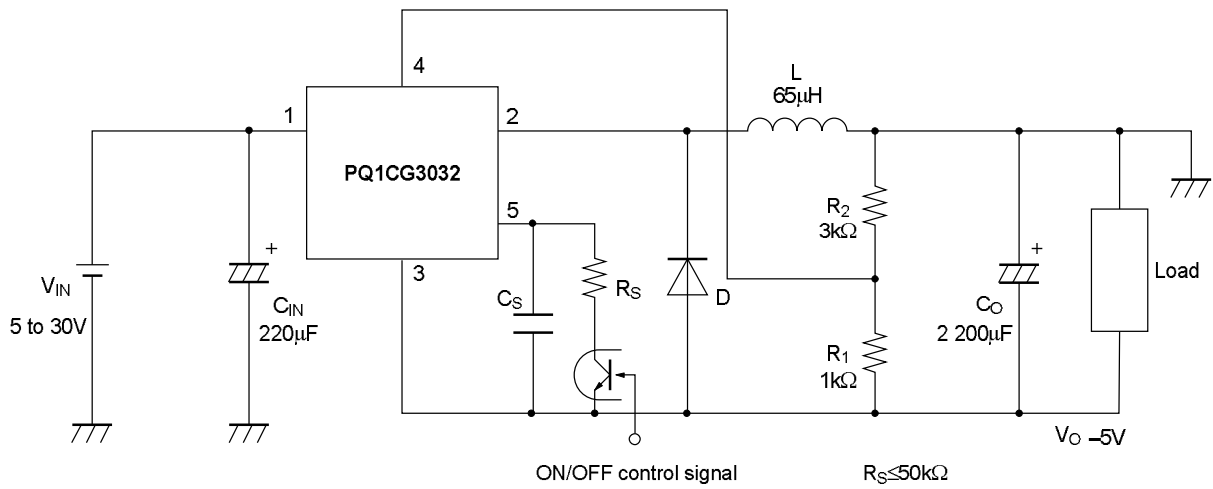
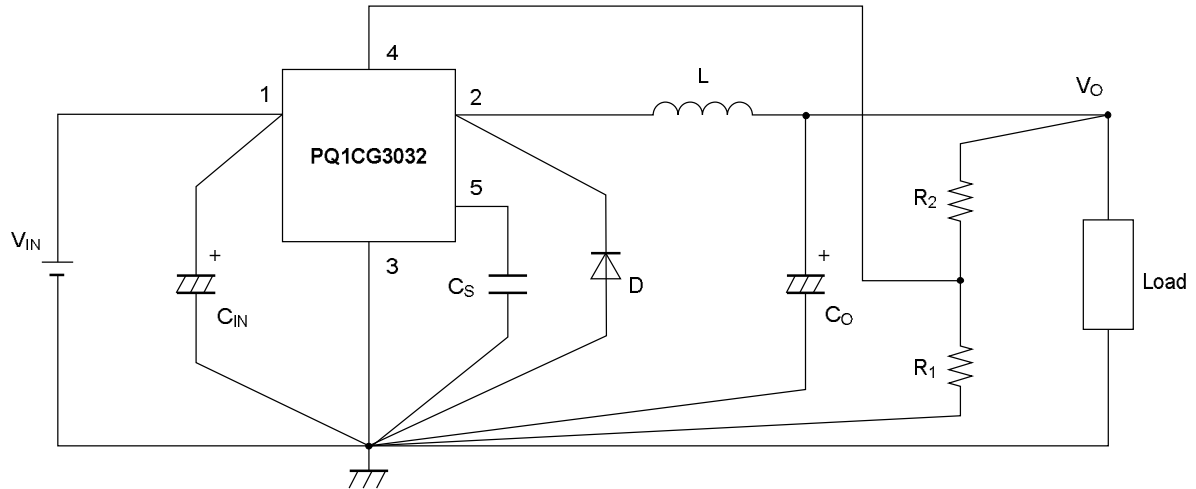


Fig.16 Polarity Inversion Type Circuit Diagram

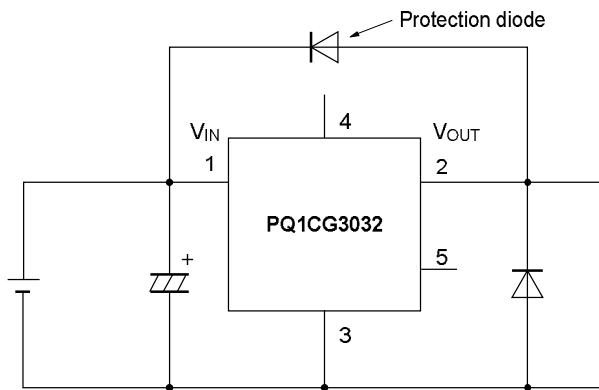


■ Precautions for Use



1. External connection

- (1) Wiring condition is very important. Noise associated with wiring inductance may cause problems. For minimizing inductance, it is recommended to design the thick and short pattern (between large current diodes, input/output capacitors, and terminal 1,2.) Single-point grounding (as indicated) should be used for best results.
- (2) High switching speed and low forward voltage type schottky barrier diode should be recommended for the catch-diode D because it affects the efficiency. Please select the diode which the current rating is at least 1.2 times greater than maximum switching current.
- (3) The output ripple voltage is highly influenced by ESR (Equivalent Series Resistor) of output capacitor, and can be minimized by selecting Low ESR capacitor.
- (4) An inductor should not be operated beyond its maximum rated current so that it may not saturate.
- (5) When voltage that is higher than V_{IN} ①, is applied to V_{OUT} ②, there is the case that the device is broken. Especially, in case V_{IN} ① is shorted to GND in normal condition, there is the case that the device is broken since the charged electric charge in output capacitor (C_O) flows into input side. In such case a schottky barrier diode or a silicon diode shall be recommended to connect as the following circuit.



ON/OFF Control Terminal

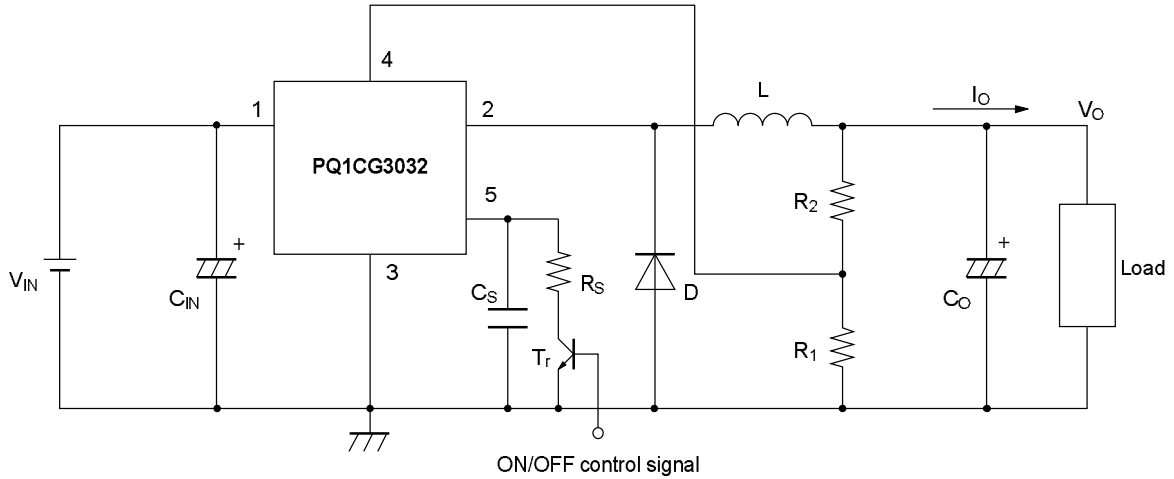
1. In the following circuit, when ON/OFF control terminal ⑤ becomes low by switching transistor Tr on, output voltage may be turned OFF and the device becomes stand-by mode. Dissipation current at stand-by mode becomes Max.400μA.

2. Soft start

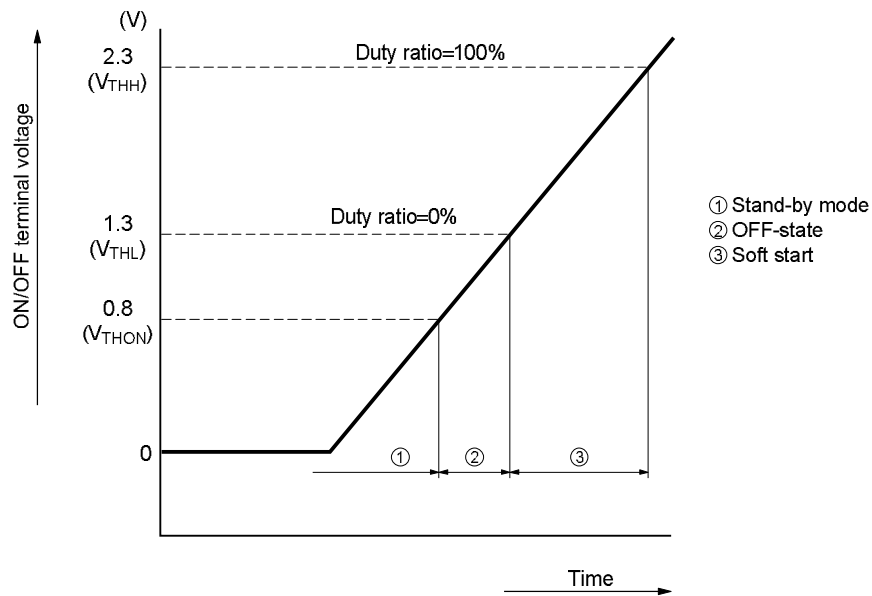
When capacitor Cs is attached, output pulse gradually expanded and output voltage will start softly.

3. ON/OFF control with soft startup

For ON/OFF control with capacitor Cs, be careful not to destroy a transistor Tr by discharge current from Cs, adding a resistor restricting discharge current of Cs.



ON-OFF Terminal Voltage vs. Time



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