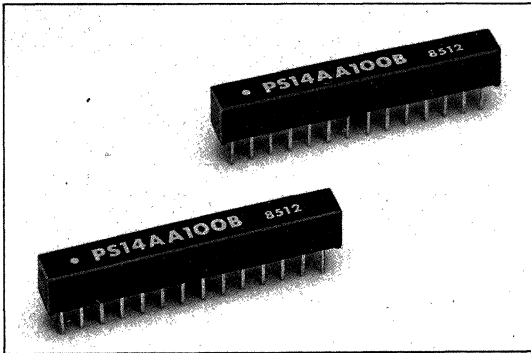




# PS14 SERIES: 14-PIN SIP 10-TAP EQUALLY-SPACED

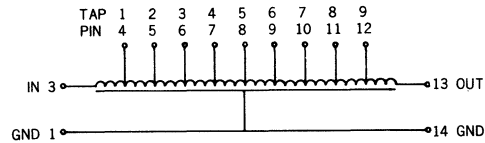


**FEATURES:**

- 14-pin SIP Package
- 10 Equally-Spaced Delay Taps
- Low Output Distortion
- Other Delays Available upon Request

**CIRCUIT CONFIGURATION AND PIN CONNECTIONS:**

**Circuit A**

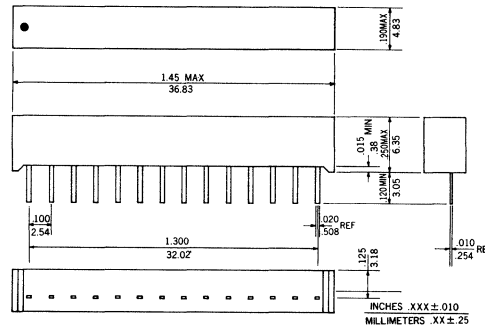


**GENERAL SPECIFICATIONS:**

Total Delay: 10 to 250 ns  
 Tap Delays: 10% increments  
 Characteristic Impedance: 50 ohms ( $\pm 10\%$ )  
 Distortion:  $\pm 10\%$  max.  
 Temperature Coefficient of Delay: 100ppm/ $^{\circ}\text{C}$  max.  
 Operating Temperature:  $0^{\circ}\text{C}$  to  $+70^{\circ}\text{C}$   
 Storage Temperature Range:  $-55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$   
 Insulation Resistance: 1000 megohms min.  
 50VDC,  $25^{\circ}\text{C}$

**PACKAGE DIMENSIONS:**

**Package A**



**PART NUMBER SYSTEM:**

PS14    A    A    100    A

- Characteristic Impedance in ohm  
A=50 ohms
- Total Delay Time in ns  
For examples: 050=50 ns  
100=100 ns
- Circuit Configuration-See schematics Above,  
circuit A is standard, other pin connections  
are available upon request
- Package Dimensions  
Package A is standard
- Basic Series  
Passive delay line, SIP 14 pins

**STANDARD RATINGS:**

PART NO.	IMPEDANCE $Z_0 \pm 10\%(\Omega)$	TOTAL DELAY $td \pm 5\%(ns)$	TAP DELAY (ns)	RISE TIME $tr$ max. (ns)	ATTENUATION max (%)
PS14AA010A	50	$10 \pm 1$	$1 \pm 0.5$	2.5	5
PS14AA020A	50	$20 \pm 2$	$2 \pm 0.5$	4	5
PS14AA030A	50	$30 \pm 2$	$3 \pm 1$	6	5
PS14AA040A	50	40	$4 \pm 2$	8	5
PS14AA050A	50	50	$5 \pm 2$	9	5
PS14AA100A	50	100	$10 \pm 3$	18	6
PS14AA150A	50	150	$15 \pm 3$	28	8
PS14AA200A	50	200	$20 \pm 3$	38	10
PS14AA250A	50	250	$25 \pm 3$	45	12