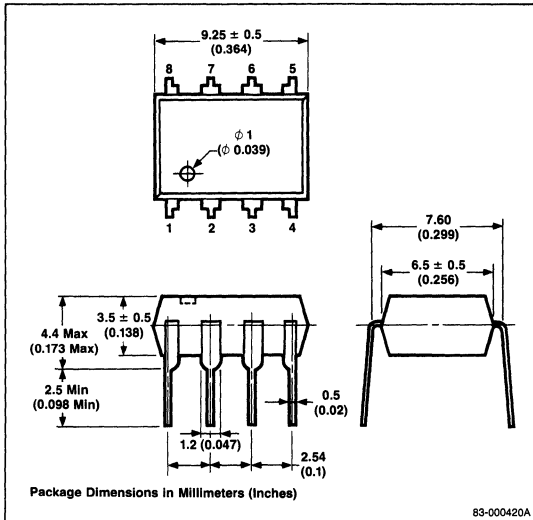


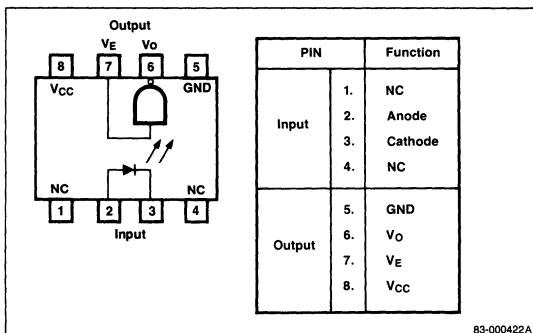
Description

The PS2007B is a high speed photo coupler containing a GaAsP light emitting diode and an integrated detector consisting of a photo diode and a high gain linear amplifier that drives a Schottky clamped open collector output transistor in a plastic DIP (Dual In-line Package).

Package Dimensions



Pin Connection



Features

- Ultra high speed: 50ns typ
- High isolation voltage: 3000V_{DC} min
- Low input current requirement: 5mA
- Economical, compact, plastic dual in-line package
- TTL compatible: 5V supply
- Equivalent to 6N137

Applications

- Line receivers
- Floating power supplies
- Computer and peripheral memory
- Replacement for mechanical and reed relays
- Replacement for pulse transformers

Absolute Maximum Ratings

T_A = +25°C

Diode	
Reverse Voltage, V _R	5V
Forward Current, I _F	10mA
Detector	
Supply Voltage, V _{CC}	7V
Output Voltage, V _O	7V
Output Current, I _O	50mA
Enable Voltage, V _E	5.5V
Power Dissipation, P _D	85mW
Isolation Voltage, BV ¹	3000V _{DC}
Storage Temperature, T _{STG}	-55°C to +125°C
Operating Temperature, T _{OPT}	0°C to +70°C

Electrical Characteristics

$T_A = 0 \text{ to } +70^\circ\text{C}$

Parameter	Symbol	Limits			Unit	Test Conditions
		Min	Typ	Max		
Diode						
Forward Voltage	V_F	1.42	1.7		V	$I_F = 10\text{mA}$, $T_A = +25^\circ\text{C}$
Reverse Current	I_R	0.01	10		μA	$V_R = 5\text{V}$, $T_A = +25^\circ\text{C}$
Capacitance	C_T	60			pF	$V = 0$, $f = 1.0\text{MHz}$
Detector						
High Level Enable Current	I_{EH}	-0.8			mA	$V_{CC} = 5.5\text{V}$, $V_{EH} = 2.0\text{V}$
Low Level Enable Current	I_{EL}	-1.2	-2.0		mA	$V_{CC} = 5.5\text{V}$, $V_{EL} = 0.5\text{V}$
Coupled						
High Level Output Current	I_{OH}	30	250		μA	$V_{CC} = 5.5\text{V}$, $V_O = 5.5\text{V}$, $I_F = 250\mu\text{A}$, $V_E = 2.0\text{V}$
Low Level Output Voltage	V_{OL}	0.4	0.6		V	$V_{CC} = 5.5\text{V}$, $V_E = 2.0\text{V}$, $I_F = 5\text{mA}$, $I_O = 13\text{mA}$
Low Level Supply Current	I_{CCL}	10	18		mA	$V_{CC} = 5.5\text{V}$, $V_E = 2\text{V}$, $I_F = 10\text{mA}$
High Level Supply Current	I_{CCH}	7	15		mA	$V_{CC} = 5.5\text{V}$, $V_E = 0.5\text{V}$, $I_F = 0\text{mA}$

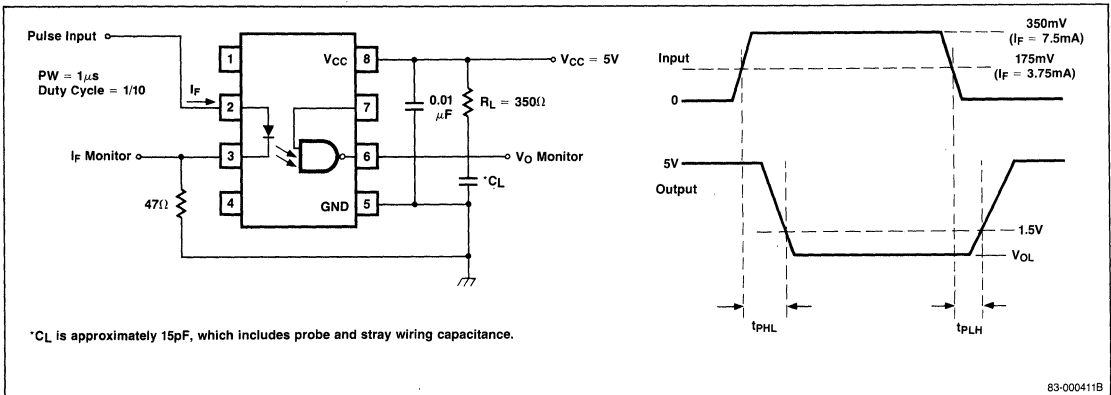
Electrical Characteristics (cont)

$T_A = +25^\circ\text{C}$

Parameter	Symbol	Limits			Unit	Test Conditions
		Min	Typ	Max		
Coupled						
Current Transfer Ratio	CTR		600		%	$I_F = 5\text{mA}$, $V_{CC} = 5\text{V}$, $R_L = 100\Omega$
Isolation Resistance	R_{1-2}		10^{12}		Ω	$V_{IN-OUT} = 1\text{kV}$
Isolation Capacitance	C_{1-2}		0.7		pF	$V = 0$, $f = 1\text{MHz}$
Propagation Delay Time to Low Output Level						
Propagation Delay Time to Low Output Level	t_{PHL2}		50	75	ns	$I_F = 7.5\text{mA}$, $V_{CC} = 5\text{V}$, $R_L = 350\Omega$, $C_L = 15\text{pF}$
Propagation Delay Time to High Output Level						
Propagation Delay Time to High Output Level	t_{PLH2}		50	75	ns	
Propagation Delay Time of Enable to Low Output Level						
Propagation Delay Time of Enable to Low Output Level	t_{EHL}		15		ns	$I_F = 7.5\text{mA}$, $V_{CC} = 5\text{V}$, $R_L = 350\Omega$, $V_{EH} = 3\text{V}$, $C_L = 15\text{pF}$
Propagation Delay Time of Enable to High Output Level						
Propagation Delay Time of Enable to High Output Level	t_{ELH}		30		ns	

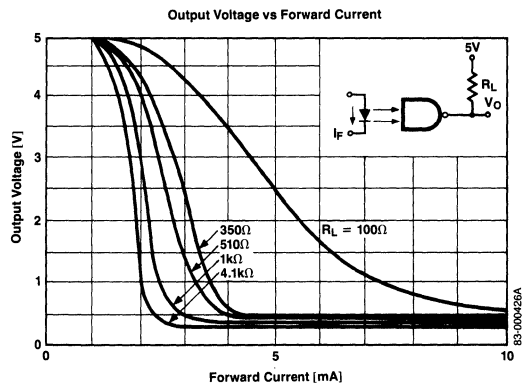
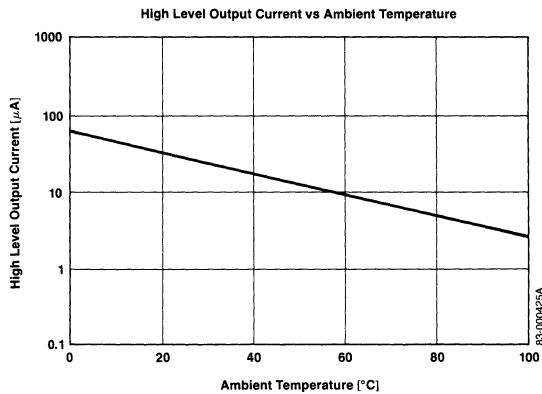
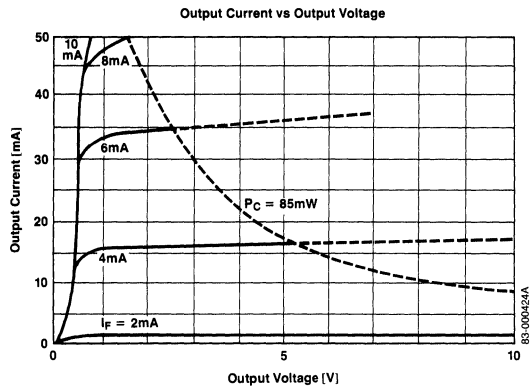
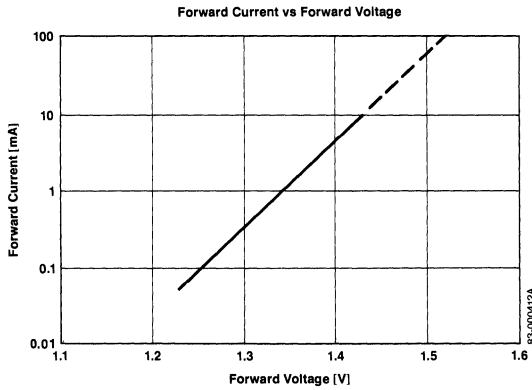
Notes: 1. Test Conditions: DC voltage for 1 min at $T_A = +25^\circ\text{C}$, RH = 60% between input (pins 1, 2, 3, and 4 common) and output (Pins 5, 6, 7, and 8 common).
2. Measuring Circuit.

Measuring circuit

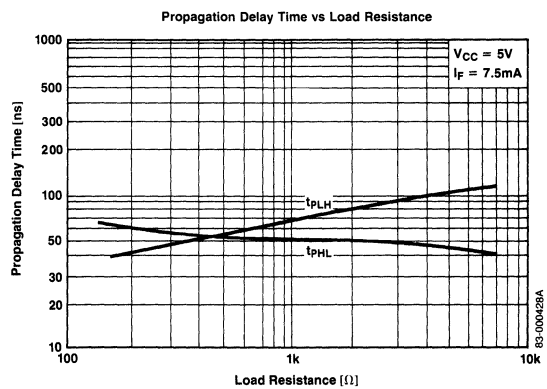
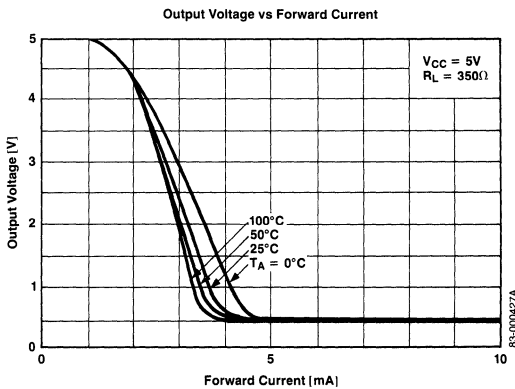


Typical Characteristics

$T_A = +25^\circ\text{C}$



5



Typical Characteristics (cont)

$T_A = +25^\circ\text{C}$

