PSC2065B1

650 V, 20 A SiC Schottky diode in bare die

7 May 2025

Product data sheet

1. General description

Nexperia introduces leading edge Silicon Carbide (SiC) Schottky diode for ultra-high performance, low loss, high efficiency power conversion applications. The Merged PiN Schottky (MPS) diode delivered as bare die in Tape and Reel (T & R) offers temperature independent capacitive turnoff, zero recovery switching behavior combined with an outstanding figure-of-merit ($Q_C \times V_F$) and improves the robustness expressed in a high I_{FSM} .

2. Features and benefits

- · Zero forward and reverse recovery
- · Temperature independent fast and smooth switching performance
- Outstanding figure-of-merit (Q_c x V_F)
- · High I_{FSM} capability
- · High power density
- · Reduced system costs
- System miniaturization
- Reduced EMI

3. Applications

- Switch Mode Power Supply (SMPS)
- AC-DC and DC-DC converter
- Battery charging infrastructure
- Server and telecom power supply
- Uninterruptible Power Supply (UPS)
- · Photovoltaic inverters

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DC}	DC blocking voltage		[1]	650	-	-	V
IF	forward current	δ = 1; T _c ≤ 111 °C	[2]	-	-	20	Α
$Q_{\mathbb{C}}$	total capacitive charge	$V_R = 400 \text{ V}; \text{ dI}_F/\text{dt} = 200 \text{ A/}\mu\text{s}; \text{ I}_F = 20 \text{ A};$ $T_j = 25 ^{\circ}\text{C}$	[2]	-	41	-	nC

- [1] Parameters 100% tested.
- [2] Validation performed on TO-263-2 with mold compound.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode (back side)		
2	A	anode (top side)	Transparent top view Bare die (NBD2-04)	A → J K aaa-0038726

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PSC2065B1	Bare die	Bare die product; 1.99 mm × 1.99 × 0.11 mm die size	NBD2-04			

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{RRM}	repetitive peak reverse voltage	T _j = 25 °C		-	650	V
dv/dt	diode dv/dt ruggedness	0 ≤ V _R ≤ 480 V		-	100	V/ns
I _F	forward current	δ = 1; T _c ≤ 111 °C	[1]	-	20	Α
I _{FSM}	non-repetitive peak	t_p = 10 µs; square wave; T_c = 25 °C	[1]	-	780	А
	forward current	t_p = 10 ms; half sine-wave; T_c = 25 °C	[1]	-	95	Α
		t_p = 10 ms; half sine-wave; T_c = 150 °C	[1]	-	80	Α
∫i ² dt	i ² t value	t _p = 10 ms; T _c = 25 °C	[1]	-	45	A²s
		t_p = 10 ms; T_c = 150 °C	[1]	-	32	A²s
P _{tot}	total power dissipation	T _c ≤ 25 °C	[1]	-	98	W
T _j	junction temperature		[1]	-55	175	°C
T _{amb}	ambient temperature		[1]	-55	175	°C
T _{stg}	storage temperature		[1]	-65	175	°C

^[1] Validation performed on TO-263-2 with mold compound.

8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-c)}	thermal resistance from junction to case		[1]	-	1.2	1.5	K/W

^[1] Validation performed on TO-263-2 with mold compound.

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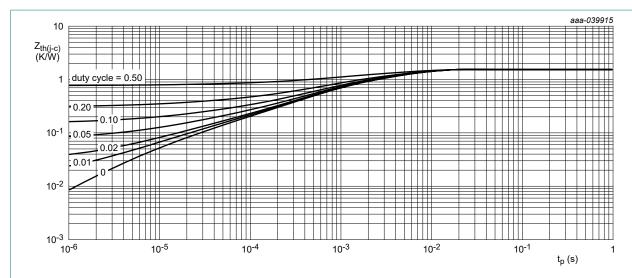


Fig. 1. Transient thermal impedance as a function of pulse duration; maximum values

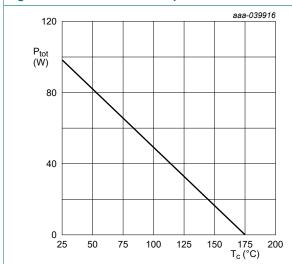


Fig. 2. Power dissipation; maximum values

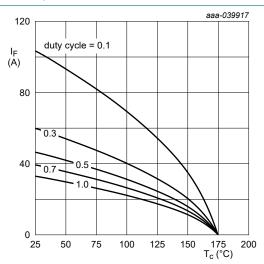


Fig. 3. Forward current as a function of case temperature; maximum values

9. Characteristics

Table 6. Characteristics

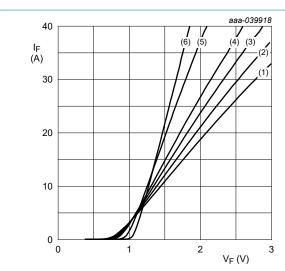
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DC}	DC blocking voltage		[1]	650	-	-	V
V _F	forward voltage	I _F = 20 A; T _j = 25 °C	[1]	-	1.5	1.8	V
		I _F = 20 A; T _j = 150 °C	[2]	-	2	2.6	V
I _R	reverse current	V _R = 650 V; T _j = 25 °C	[1]	-	1	180	μΑ
		V _R = 650 V; T _j = 150 °C	[2]	-	10	1250	μΑ
C _d	diode capacitance	f = 1 MHz; V _R = 1 V; T _j = 25 °C	[2]	-	680	-	pF
		f = 1 MHz; V _R = 400 V; T _j = 25 °C	[2]	-	73	-	pF
Q_C	total capacitive charge	$V_R = 400 \text{ V}; \text{ dI}_F/\text{dt} = 200 \text{ A/}\mu\text{s}; \text{ I}_F = 20 \text{ A}; $ $T_j = 25 ^{\circ}\text{C}$	[2]	-	41	-	nC

^{1]} Parameters 100% tested.

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^[2] Validation performed on TO-263-2 with mold compound.

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Pulsed condition:

(1) $T_j = 175 \,^{\circ}C$

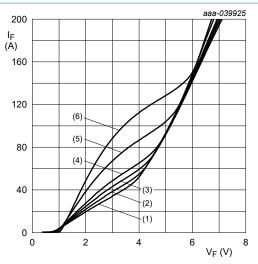
(2) $T_j^J = 150 °C$ (3) $T_j = 125 °C$

 $(4) T_i = 100 °C$

(5) $T_i = 25 °C$

 $(6) T_i = -55 ^{\circ}C$

Fig. 4. Forward current as a function of forward voltage; typical values



Pulsed condition:

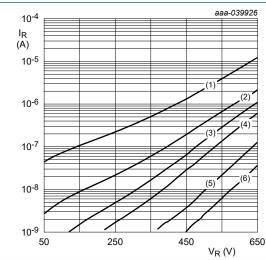
(1) $T_j = 175 \, ^{\circ}C$

(2) $T_j = 150 \,^{\circ}\text{C}$ (3) $T_j = 125 \,^{\circ}\text{C}$ (4) $T_j = 100 \,^{\circ}\text{C}$

(5) $T_i = 25$ °C

(6) $T_j = -55 \, ^{\circ}C$

Fig. 5. Forward characteristics in surge current as a function of forward voltage; typical values



Pulsed condition:

(1) $T_i = 175 \,^{\circ}C$

(2) $T_i = 150 \, ^{\circ}\text{C}$

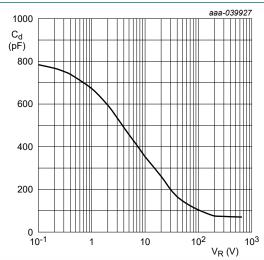
(3) $T_j = 125 \, ^{\circ}C$

(4) $T_i = 100 \, ^{\circ}C$

 $(5) T_i = 25 ^{\circ}C$

(6) $T_i = -55 \,^{\circ}C$

Fig. 6. Reverse current as a function of reverse voltage; typical values



f = 1 MHz; T_{amb} = 25 °C

Fig. 7. Diode capacitance as a function of reverse voltage; typical values

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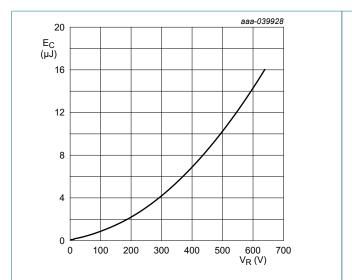


Fig. 8. Capacitance stored energy as a function of reverse voltage; typical values

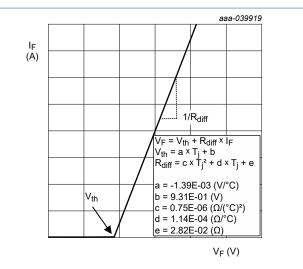
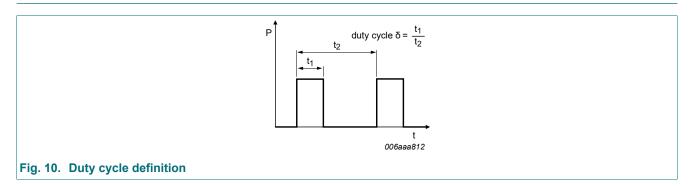


Fig. 9. Simplified forward characteristics mode

10. Test information

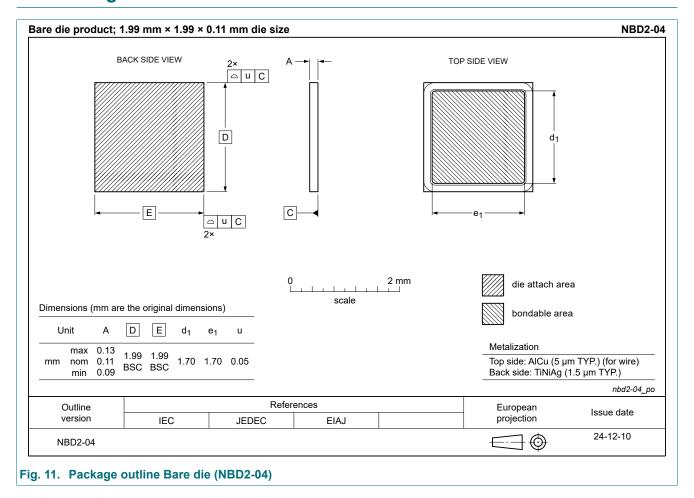


Quality information

The reliability of the bare die product was tested in the TO-263-2 package with epoxy mold compound.

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11. Package outline



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12. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PSC2065B1 v.1	20250507	Product data sheet	-	-

13. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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