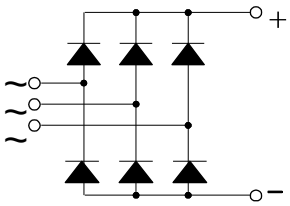
Three Phase Rectifier Bridge

PSD 35T

Preliminary Data Sheet

I_{dAVM} = 38 A
V_{RRM} = 800 V to 2000 V

V_{RSM} V	V_{RRM} V	Type Number
800	800	PSD 35T/08
1200	1200	PSD 35T/12
1400	1400	PSD 35T/14
1600	1600	PSD 35T/16
1800	1800	PSD 35T/18
2000	2000	PSD 35T/20



Symbol	Test Conditions			Maximum Ratings	
I_{dAVM}	T_c = 85˚C per module			38	A
I_{FSM}	T_{vj} = 45˚C, V_R = 0 V	t = 10 ms	50 Hz, sine	400	A
		t = 8.3 ms	60 Hz, sine	440	A
	T_{vj} = T_{vjm}, V_R = 0 V	t = 10 ms	50 Hz, sine	360	A
		t = 8.3 ms	60 Hz, sine	400	A
$\int i^2dt$	T_{vj} = 45˚C, V_R = 0 V	t = 10 ms	50 Hz, sine	800	A^2s
		t = 8.3 ms	60 Hz, sine	810	A^2s
	T_{vj} = T_{vjm}, V_R = 0 V	t = 10 ms	50 Hz, sine	650	A^2s
		t = 8.3 ms	60 Hz, sine	670	A^2s
T_{vj}				-40 ... +150	˚C
T_{vjm}				150	˚C
T_{stg}				-40 ... +150	˚C
V_{isol}	50/60 Hz, RMS	t = 1 min		2500	V~
	I_{isol} ≤ 1mA	t = 1 s		3000	V~
M_d	Mounting torque	(M4)		1.5	Nm
	Terminal connection torque	(M4)		1.5	Nm
Weight	typ.			110	g

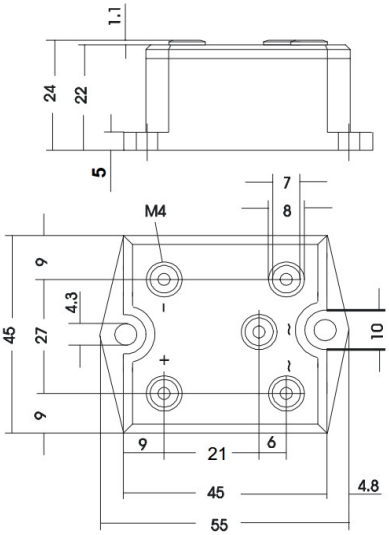
Symbol	Test Conditions			Characteristic Value	
I_R	V_R = V_{RRM}	T_{vj} = 25˚C	≤	0.3	mA
	V_R = V_{RRM}	T_{vj} = T_{vjm}	≤	5.0	mA
V_F	I_F = 150 A	T_{vj} = 25˚C	≤	2.2	V
V_{TO}	For power-loss calculations only			0.85	V
r_T	T_{vj} = T_{vjm}			12	mΩ
$R_{th(j-c)}$	per diode; DC current			4.2	K/W
	per module			0.7	K/W
$R_{th(j-s)}$	per diode; DC current			4.8	K/W
	per module			0.8	K/W
d_S	Creeping distance on surface			6.2	mm
d_A	Creeping distance on air			6.2	mm
a	Maximum allowable acceleration			50	m/s^2

Features
- Package with screw terminals
- Isolation voltage 3000 V~
- Mesa glass-passivated chips
- Blocking voltage up to 2000 V
- Low forward voltage drop
- UL registered E 148688

Applications
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Advantages
- Easy to mount with two screws
- Space and weight savings
- Improved temperature and power cycling capability

Package style and outline



Dimensions in mm (1mm = 0.0394“)

Data according to IEC 60747 refers to a single diode unless otherwise stated

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