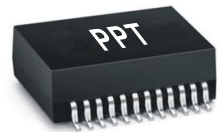


Designed to Support 1:1 Turns Ratio Transceivers

- ◆ Designed for long haul Gigabit Ethernet (100/1000Base-T) full duplex applications
- ◆ Cable interface for isolation and low Common mode emissions
- ◆ Compliant with IEEE 802.3ab standard for 1000Base-T
- ◆ Remark: Contact PPT for further requirements



Electrical specifications @25°C -operating temperature: 0°C to70°C

Part Number	Turns Ratio (±5%)	Line Side Inductance (μH Min.)	Leakage Inductance (μH Max.)	CWW (PF Max.)	DCR (Ω Max.)	Hi-pot (Vrms)
PSF-24411	1CT:1CT	350	0.5	35	2	1500
PSF-24412	1CT:1CT	350	0.5	35	2	1500
PSF-24414	1CT:1CT	350	0.5	28	1.5	1500
PSF-24415J	1CT:1CT	350	0.5	35	2	1500

Part Number	Insertion Loss (dB Max.)	Return Loss (dB Min)				Crosstalk (dB Min.)			DCMR (dB Min.)
	1-100MHz	1-30MHz	60-80MHz	100MHz	30MHz	60MHz	100MHz	1-100MHz	
PSF-24411	1.4	-18	-12	-10	-40	-35	-30	-33	
PSF-24412	1.1	-18	-12	-10	-42	-33	-33	-30	
PSF-24414	1.2	-16	-12	-10	-35	-30	-30	-30	
PSF-2445J	1.2	-18	-15	-10	-42	-37	-33	-30	

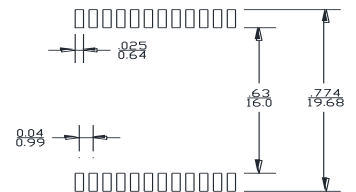
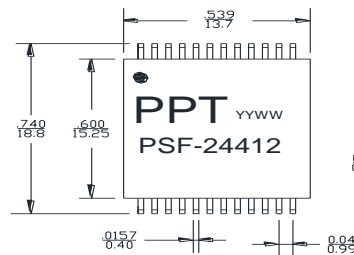
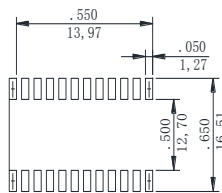
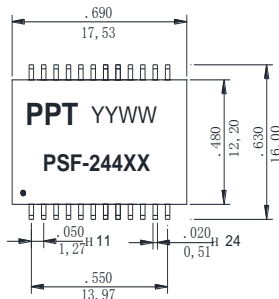
Note: 1.Test condition: Line Side Inductance 350 uH Min. @100KHz, 100mV with 8mA DC Bias

2.Test condition (PSF-24415J) Operating temperature rang is -40°C TO +85°C: Line Side Inductance @100KHz, 100mV with 8mA DC Bias

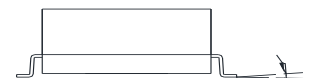
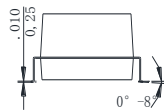
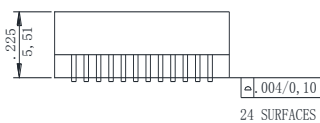
Mechanicals

PSF-24411

PSF-24412



Suggested PCB Pad Layout

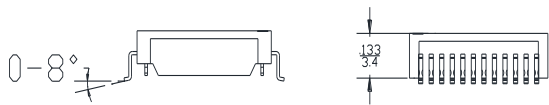
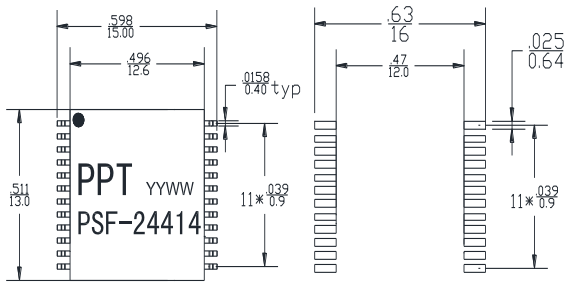


REMARKS:

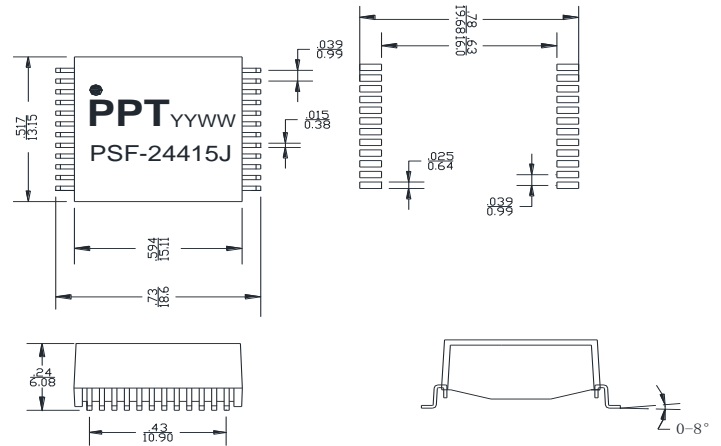
dimension: $\frac{\text{inches}}{\text{mm}}$

unless otherwise specified, all tolerances are: $\pm \frac{0.01}{0.25}$

PSF-24414



PSF-24415J



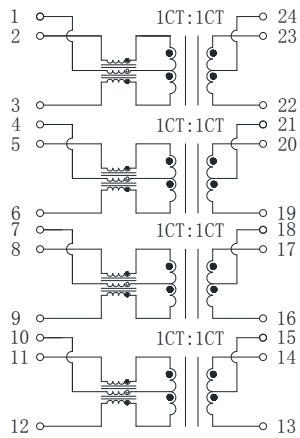
REMARKS:

dimension: $\frac{\text{inches}}{\text{mm}}$

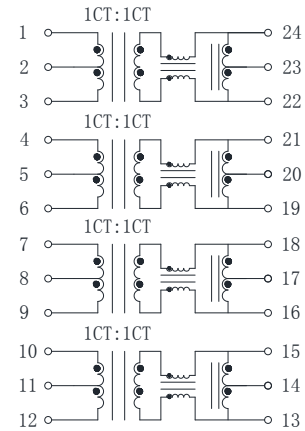
unless otherwise specified, all tolerances are: $\pm \frac{0.01}{0.25}$

Schematics

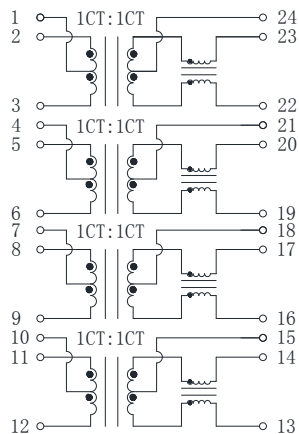
PSF-24411



PSF-24412



PSF-24414



PSF-24415J

