

## Isc N-Channel MOSFET Transistor

## PSMN130-200D

### • FEATURES

- With To-252(DPAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### • APPLICATIONS

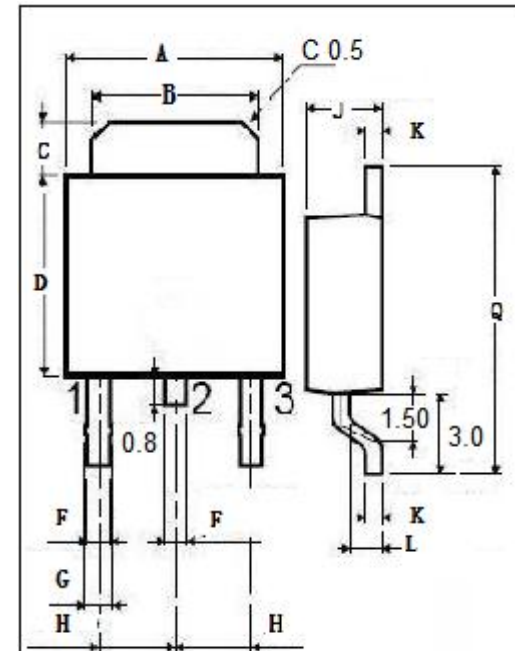
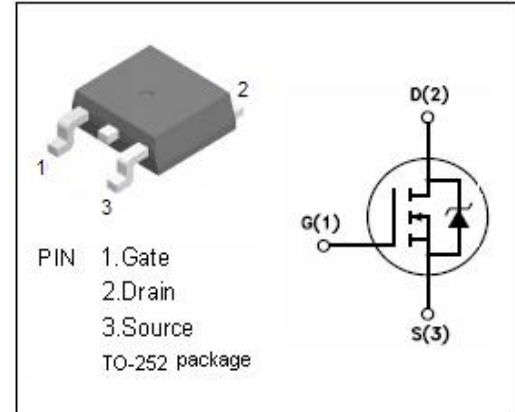
- Switching applications

### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DS}$	Drain-Source Voltage	200	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous	20	A
$I_{DM}$	Drain Current-Single Pulsed	80	A
$P_D$	Total Dissipation @ $T_c=25^\circ\text{C}$	150	W
$T_{ch}$	Max. Operating Junction Temperature	175	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-55~175	$^\circ\text{C}$

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.0	$^\circ\text{C/W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	50	$^\circ\text{C/W}$



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
E	0.65	
F	0.75	
G	2.10	2.50
H	2.10	2.40
I	0.40	0.60
J	0.90	1.10
K	9.90	10.1

**Isc N-Channel MOSFET Transistor****PSMN130-200D****ELECTRICAL CHARACTERISTICS** $T_c=25^{\circ}\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V$ ; $I_D=0.25mA$	200			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ ; $I_D=1.0mA$	2.0		4.0	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V$ ; $I_D=25A$		120	130	$m\Omega$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 10V$ ; $V_{DS}=0V$			$\pm 0.1$	$\mu A$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=150V$ ; $V_{GS}=0V$			10	$\mu A$
$V_{SDF}$	Diode forward voltage	$I_{SD}=25A$ , $V_{GS}=0V$			1.2	V

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