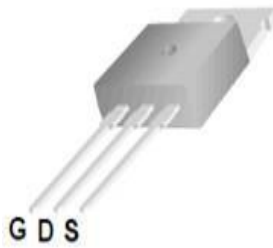


# PT530BA

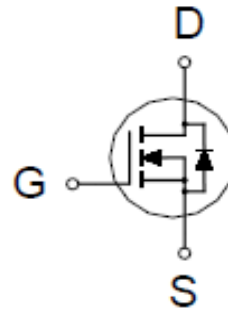
## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

| $V_{(BR)DSS}$ | $R_{DS(ON)}$                   | $I_D^2$ |
|---------------|--------------------------------|---------|
| 30V           | 4.3m $\Omega$ @ $V_{GS} = 10V$ | 89A     |



TO-220



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

| PARAMETERS/TEST CONDITIONS                     |                                   | SYMBOL         | LIMITS     | UNITS            |
|--|-----------------------------------|----------------|------------|------------------|
| Drain-Source Voltage                           |                                   | $V_{DS}$       | 30         | V                |
| Gate-Source Voltage                            |                                   | $V_{GS}$       | $\pm 20$   |                  |
| Continuous Drain Current <sup>2</sup>          | $T_C = 25\text{ }^\circ\text{C}$  | $I_D$          | 89         | A                |
|  | $T_C = 100\text{ }^\circ\text{C}$ |                | 56         |                  |
| Pulsed Drain Current <sup>1</sup>              |                                   | $I_{DM}$       | 150        |                  |
| Avalanche Current                              |                                   | $I_{AS}$       | 48.5       |                  |
| Avalanche Energy                               | $L = 0.1\text{ mH}$               | $E_{AS}$       | 117        | mJ               |
| Power Dissipation                              | $T_C = 25\text{ }^\circ\text{C}$  | $P_D$          | 62.5       | W                |
|  | $T_C = 100\text{ }^\circ\text{C}$ |                | 25         |                  |
| Operating Junction & Storage Temperature Range |                                   | $T_j, T_{stg}$ | -55 to 150 | $^\circ\text{C}$ |

### THERMAL RESISTANCE RATINGS

| THERMAL RESISTANCE  | SYMBOL          | TYPICAL | MAXIMUM | UNITS                       |
|---------------------|-----------------|---------|---------|-----------------------------|
| Junction-to-Case    | $R_{\theta JC}$ |         | 2       | $^\circ\text{C} / \text{W}$ |
| Junction-to-Ambient | $R_{\theta JA}$ |         | 60      |                             |

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Package limitation current is 60A.

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## N-Channel Enhancement Mode MOSFET

### ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)

| PARAMETER   | SYMBOL                                 | TEST CONDITIONS   | LIMITS  |      |      | UNITS |
|---|--|---|---|------|------|-------|
|   |  |   | MIN   | TYP  | MAX  |       |
| <b>STATIC</b>   |  |   |   |      |      |       |
| Drain-Source Breakdown Voltage  | V <sub>(BR)DSS</sub>                   | V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA                        | 30  |      |      | V     |
| Gate Threshold Voltage  | V <sub>GS(th)</sub>                    | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA          | 1   | 1.7  | 3    |       |
| Gate-Body Leakage   | I <sub>GSS</sub>                       | V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V                        |   |      | ±100 | nA    |
| Zero Gate Voltage Drain Current   | I <sub>DSS</sub>                       | V <sub>DS</sub> = 24V, V <sub>GS</sub> = 0V                         |   |      | 1    | μA    |
|   |  | V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C |   |      | 10   |       |
| Drain-Source On-State Resistance <sup>1</sup>                                 | R <sub>DS(ON)</sub>                    | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 20A                        |   | 4.1  | 7.9  | mΩ    |
|   |  | V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A                         |   | 3.6  | 4.3  |       |
| Forward Transconductance <sup>1</sup>   | g <sub>fs</sub>                        | V <sub>DS</sub> = 5V, I <sub>D</sub> = 20A                          |   | 85   |      | S     |
| <b>DYNAMIC</b>  |  |   |   |      |      |       |
| Input Capacitance   | C <sub>iss</sub>                       | V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V, f = 1MHz               |   | 2740 |      | pF    |
| Output Capacitance  | C <sub>oss</sub>                       |   |   | 364  |      |       |
| Reverse Transfer Capacitance  | C <sub>rss</sub>                       |   |   | 248  |      |       |
| Gate Resistance   | R <sub>g</sub>                         | V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz                |   | 1.1  |      | Ω     |
| Total Gate Charge <sup>2</sup>  | Q <sub>g</sub> (V <sub>GS</sub> =10V)  | V <sub>DS</sub> = 15V, I <sub>D</sub> = 20A                         |   | 54   |      | nC    |
|   | Q <sub>g</sub> (V <sub>GS</sub> =4.5V) |   |   | 28   |      |       |
| Gate-Source Charge <sup>2</sup>   | Q <sub>gs</sub>                        |   |   | 9    |      |       |
| Gate-Drain Charge <sup>2</sup>  | Q <sub>gd</sub>                        |   |   | 13   |      |       |
| Turn-On Delay Time <sup>2</sup>   | t <sub>d(on)</sub>                     |   | V <sub>DD</sub> = 15V<br>I <sub>D</sub> ≅ 20A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> = 6Ω |      | 18   |       |
| Rise Time <sup>2</sup>  | t <sub>r</sub>                         |   |   | 90   |      |       |
| Turn-Off Delay Time <sup>2</sup>  | t <sub>d(off)</sub>                    |   |   | 47   |      |       |
| Fall Time <sup>2</sup>  | t <sub>f</sub>                         |   |   | 37   |      |       |
| <b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b> |  |   |   |      |      |       |
| Continuous Current <sup>3</sup>   | I <sub>S</sub>                         |   |   |      | 89   | A     |
| Forward Voltage <sup>1</sup>  | V <sub>SD</sub>                        | I <sub>F</sub> = 20A, V <sub>GS</sub> = 0V                          |   |      | 1.3  | V     |
| Reverse Recovery Time   | t <sub>rr</sub>                        | V <sub>GS</sub> = 0V, di <sub>S</sub> /dt = 100A / μS               |   | 22   |      | nS    |
| Reverse Recovery Charge   | Q <sub>rr</sub>                        |   |   | 9.1  |      | nC    |

<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

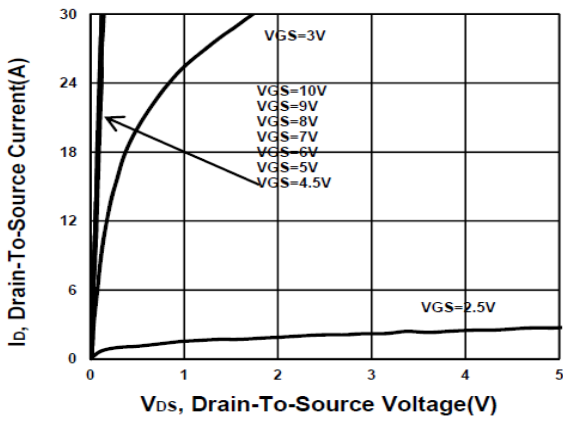
<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Package limitation current is 60A.

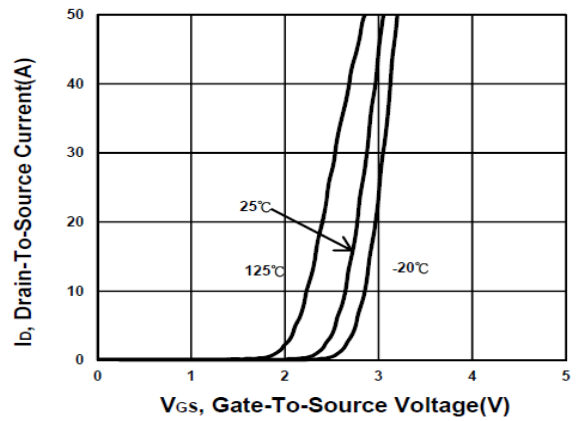
# PT530BA

## N-Channel Enhancement Mode MOSFET

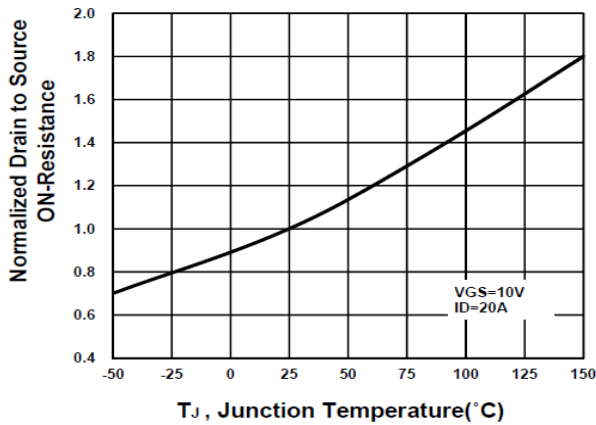
**Output Characteristics**



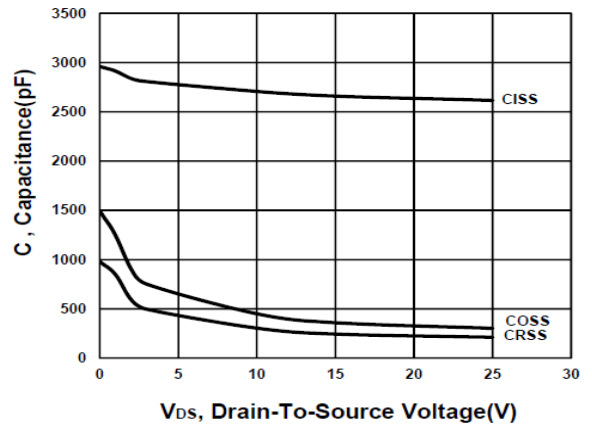
**Transfer Characteristics**



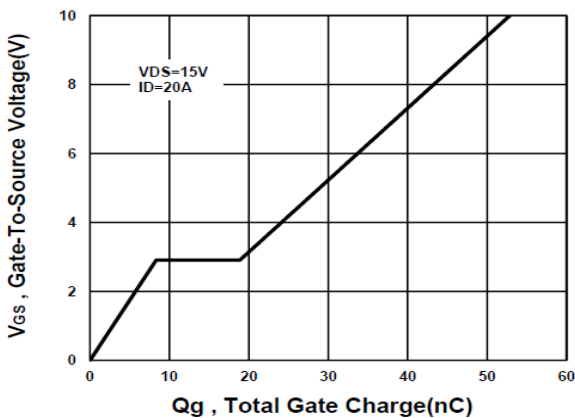
**On-Resistance VS Temperature**



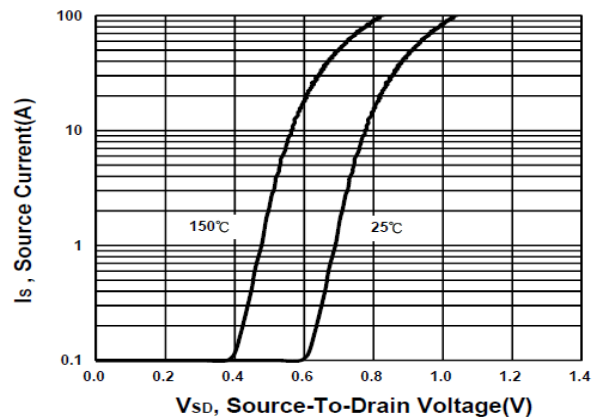
**Capacitance Characteristic**



**Gate charge Characteristics**



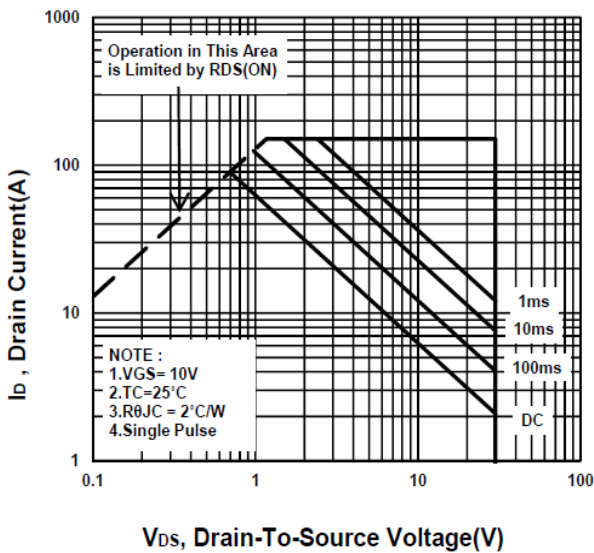
**Source-Drain Diode Forward Voltage**



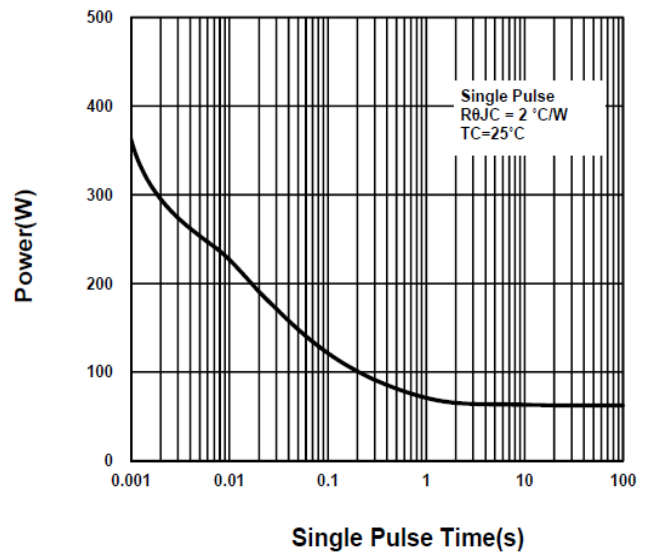
# PT530BA

## N-Channel Enhancement Mode MOSFET

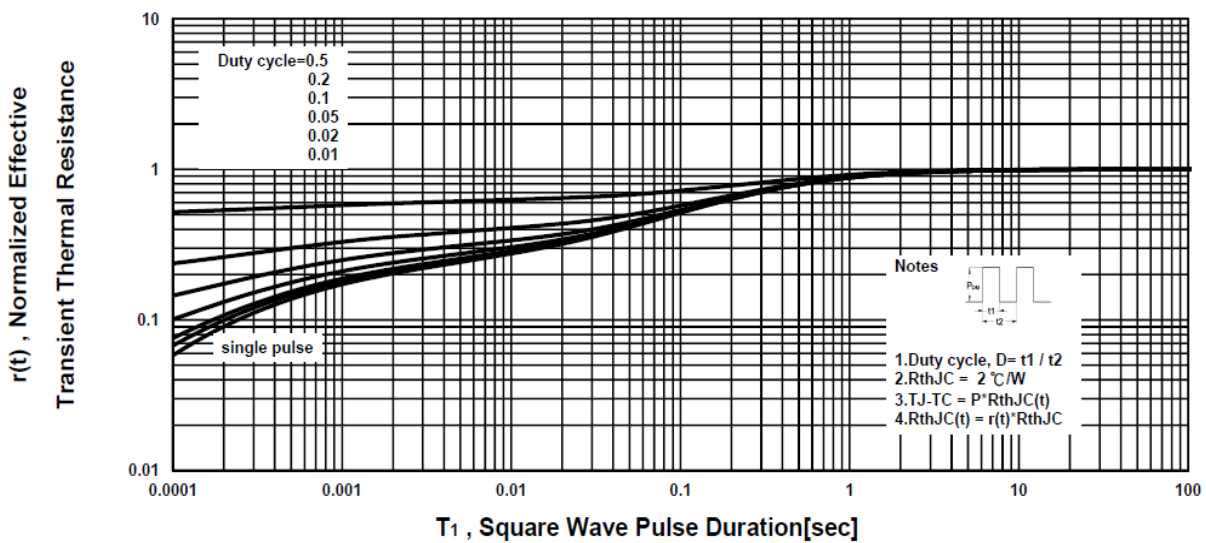
**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**



# PT530BA

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### TO-220 (3-Lead) MECHANICAL DATA

| Dimension | mm     |       |        | Dimension | mm    |      |       |
|-----------|--------|-------|--------|-----------|-------|------|-------|
|           | Min.   | Typ.  | Max.   |           | Min.  | Typ. | Max.  |
| A         | 9.652  | 10.16 | 11.5   | H         | 2.04  | 2.54 | 3.04  |
| B         | 2.54   | 2.79  | 3.048  | I         | 1.15  | 1.52 | 1.778 |
| C         | 17.3   |       | 22.86  | J         | 3.556 | 4.57 | 4.826 |
| D         | 26.924 | 29.03 | 31.242 | K         | 0.508 | 1.3  | 1.45  |
| E         | 14.224 | 15.45 | 16.510 | L         | 1.89  | 2.69 | 3.09  |
| F         | 8.382  | 9.20  | 9.40   | M         | 0.34  | 0.5  | 0.6   |
| G         | 0.381  | 0.81  | 1.016  | N         |       |      |       |

