





# DC-DC CONVERTERS POLA Non-isolated

- 6 A output current
- 3.3 V input voltage
- Wide-output voltage adjust (0.8 Vdc to 2.5 Vdc)
- Auto-track<sup>™</sup> sequencing<sup>\*</sup>
- Pre-bias start-up capability
- Efficiencies up to 94%
- Output ON/OFF inhibit
- Output voltage sense
- Point-of-Load-Alliance (POLA) compatible
- Available RoHS compliant

The PTH03050 is a next generation series of non-isolated dc-dc converters offering some of the most advanced POL features available in the industry. The primary new feature provides for sequencing between multiple modules, a function, which is becoming a necessity for powering advanced silicon including DSP's, FPGA's and ASIC's requiring controlled power-up and power-down. Other industry leading features include pre-bias start-up capability and efficiencies up to 94%. The PTH03050 has an input voltage of 2.95 Vdc to 3.65 Vdc and offers a wide 0.8 Vdc to 2.5 Vdc output voltage range with up to 6 A output current, which allows for maximum design flexibility and a pathway for future upgrades.

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated  $C_{in}$  = 100  $\mu$ F,  $C_{out}$  = 0  $\mu$ F

OUTPUT SPECIFICATIONS		www.Data
Voltage adjustability	(See Note 4)	0.8-2.5 Vdc
Setpoint accuracy		±2.0% Vo
Line regulation		±10 mV typ.
Load regulation		±12 mV typ.
Total regulation		±3.0% Vo
Minimum load		0 A
Ripple and noise	20 MHz bandwi	dth 20 mV pk-pk
Temperature co-efficient	-40 °C to +85 °C	2 ±0.5% Vo
Transient response (See Note 5)	Oversł	70 µs recovery time noot/undershoot 100 mV

### **INPUT SPECIFICATIONS**

Input voltage range	(See Note 3)	2.95-3.65 Vdc
Input current	No load	10 mA typ.
Remote ON/OFF	(See Note 1)	Positive logic
Start-up time		1 V/ms
Undervoltage lockout		3.7-4.3 Vdc typ.
Track input voltage	Pin 2 (See Note 6, 7)	±0.3 Vin

### International Safety Standard Approvals



UL/cUL CAN/CSA-C22.2 No. 60950-1-03/UL 60950-1, File No. E174104

TÜV Product Service (EN60950) Certificate No. B 04 06 38572 044 CB Report and Certificate to IEC60950, Certificate No. US/8292/UL



**NEW Product** 



SPECIFICATIONS

# **EMC CHARACTERISTICS**

Electrostatic discharge	EN61000-4
Conducted immunity	EN61000-4
Radiated immunity	EN61000-4

### EN61000-4-2, IEC801-2 EN61000-4-6 EN61000-4-3

## GENERAL SPECIFICATIONS

Efficiency	(See Efficiency	Table)	94% max.
Insulation voltage			Non-isolated
Switching frequency		600	kHz typ. ±50 kHz
Approvals and standards			EN60950 UL/cUL60950
Material flammability			UL94V-0
Dimensions	(L x W x H)		x 12.57 x 8.50 mm x 0.495 x 0.335 in
Weight			2.9 g (0.10 oz)
MTBF	Telcordia SR-33	32	7,092,000 hours
ENVIRONMENTAL SPE	ECIFICATIONS		
Thermal performance (See Note 2)	Operating ambi temperature	ent,	-40 °C to +85 °C

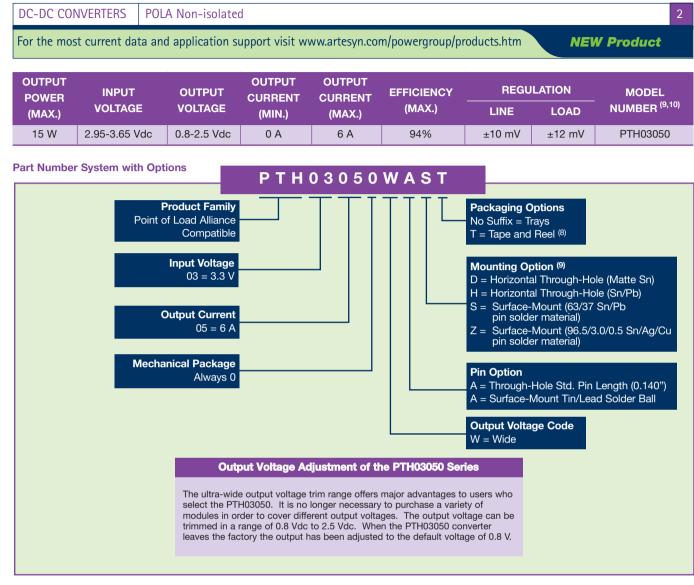
Thermal performance (See Note 2)	Operating ambient, temperature Non-operating	-40 °C to +85 °C -40 °C to +125 °C
MSL ('Z' suffix only)	JEDEC J-STD-020C	Level 3
PROTECTION		
Short-circuit	Auto reset	12 A typ.

\*Auto-track™ is a trade mark of Texas Instruments









### Notes

- Remote ON/OFF. Positive Logic 1
- ON: Pin 3 open; or V > Vin - 0.5 V
- Pin 3 GND; or V < 0.8 V (min 0.2 V). OFE
- See Figure 1 for safe operating curve.
- A 100 µF electrolytic input capacitor is required for proper operation. The 3 capacitor must be rated for a minimum of 300 mA rms of ripple current.
- 4 An external output capacitor is not required for basic operation. Adding 100  $\mu$ F of distributed capacitance at the load will improve the transient response.
- 1 A/µs load step, 50 to 100%  $I_{omax}$ ,  $C_{out} = 100 \ \mu$ F. If utilized Vout will track applied voltage by ±0.3 V (up to Vo set point). 6 The pre-bias start-up feature is not compatible with Auto-Track<sup>™</sup>. This is because when the module is under Auto-Track<sup>™</sup> control, it is fully active and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track<sup>™</sup> function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 153 for more details.
- Tape and reel packaging only available on the surface-mount versions.
- To order Pb-free (RoHS compatible) surface-mount parts replace the mounting option 'S' with 'Z', e.g. PTH03050WAZ. To order Pb-free (RoHS compatible) through-hole parts replace the mounting option 'H' with 'D', e.g. PTH03050WAD.
- 10 NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at http://www.artesyn.com/powergroup/products.htm to find a suitable alternative.

EFFICIENCY TABLE (I <sub>O</sub> = 4 A)	
OUTPUT VOLTAGE	EFFICIENCY
Vo = 1.0 V	87%
Vo = 1.2 V	88%
Vo = 1.5 V	90%
Vo = 1.8 V	91%
Vo = 2.0 V	92%
Vo = 2.5 V	94%







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**NEW Product** 

3

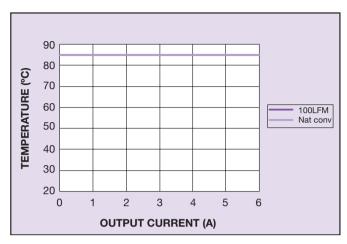


Figure 1 - Safe Operating Area Vin = 3.3 V, Output Voltage = 2.5 V (See Note A)

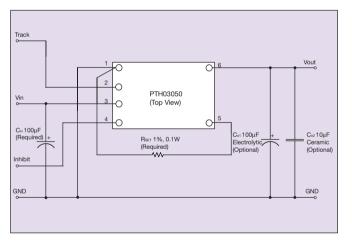


Figure 3 - Standard Application

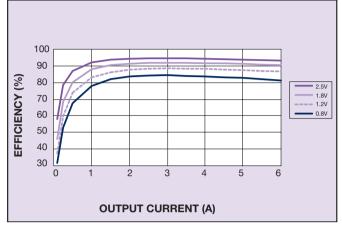


Figure 2 - Efficiency vs Load Current Vin = 3.3 V (See Note B)

### Notes

- Α SOA curves represent the conditions at which internal components are
- within the Artesyn derating guidelines. Characteristic data has been developed from actual products tested at в 25 °C. This data is considered typical data for the converter.







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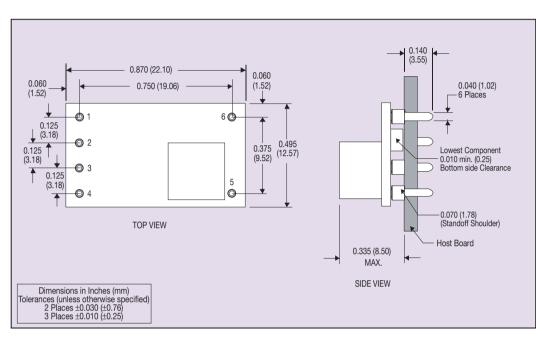
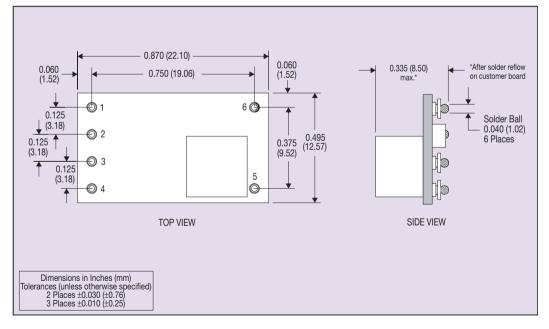


Figure 4 - Plated Through-Hole Mechanical Drawing

PIN CONNECTIONS		
PIN NO.	FUNCTION	
1	Ground	
2	Track	
3	Vin	
4	Inhibit*	
5	Vo adjust	
6	Vout	

\*Denotes negative logic: Open = Normal operation Ground = Function active





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Please consult our website for the following items: V Application Note