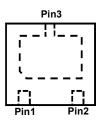


# **Uni-directional 12V High Capacitance TVS**

### **Description**

The PTVSHC3N12VU transient voltage suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC3N12VU protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC3N12VU is available in a DFN2×2-3L package with working voltages of 12 volt.



Pin configuration

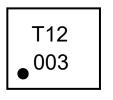
#### **Feature**

- $\rightarrow$  4500W Peak pulse power per line (t<sub>P</sub> = 8/20 $\mu$ s)
- DFN2×2-3L package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)
   ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 80A (5/50ns),
   IEC 61000-4-5 (Lightning) 200A (8/20us)

# Pin 1,2 OPin 3 Circuit Diagram

#### **Applications**

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- > Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players



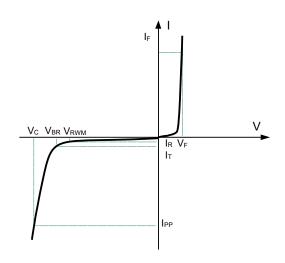
Marking (Top View)

#### **Mechanical Characteristics**

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- ➤ Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➤ Pin flatness:≤3mil

# **Electronics Parameter**

Symbol	Parameter		
V <sub>RWM</sub>	Peak Reverse Working Voltage		
I <sub>R</sub>	Reverse Leakage Current @ V <sub>RWM</sub>		
$V_{BR}$	Breakdown Voltage @ I <sub>⊺</sub>		
lτ	Test Current		
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ I <sub>PP</sub>		
P <sub>PP</sub>	Peak Pulse Power		
CJ	Junction Capacitance		
l <sub>F</sub>	Forward Current		
V <sub>F</sub>	Forward Voltage @ I <sub>F</sub>		



## Electrical characteristics per line@25℃( unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V <sub>RWM</sub>				12	V
Breakdown Voltage	V <sub>BR</sub>	I <sub>t</sub> =1mA	14	14.5		V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =12V			1	μA
Maximum Reverse Peak Pulse	I <sub>PP</sub>			200		Α
Clamping Voltage	Vc	I <sub>PP</sub> =40A t <sub>P</sub> = 8/20μs		16	18	V
Clamping Voltage	Vc	I <sub>PP</sub> =90A t <sub>P</sub> = 8/20μs		18	20	V
Clamping Voltage	Vc	I <sub>PP</sub> =140A t <sub>P</sub> = 8/20μs		20.5	24	V
Clamping Voltage	Vc	I <sub>PP</sub> =185 A t <sub>P</sub> = 8/20μs		25	30	V
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> =0V f = 1MHz		1000	1500	pF

# Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power ( t <sub>P</sub> = 8/20µS )	P <sub>pp</sub>	4500	W
Lead Soldering Temperature	TL	260 (10 sec)	$^{\circ}$ C
Operating Temperature	TJ	-55 to 150	$^{\circ}$ C
Storage Temperature	T <sub>STG</sub>	-55 to 150	$^{\circ}$

## **Typical Characteristics**



Fig 1.Pulse Waveform

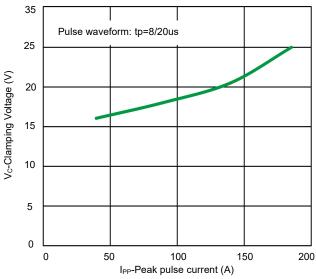


Fig 3. Clamping voltage vs. Peak pulse current

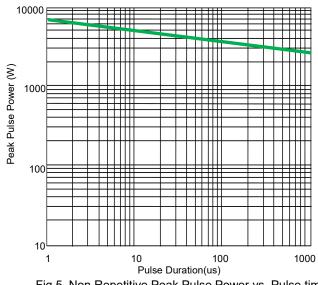


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time

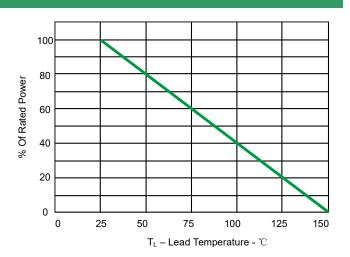


Fig 2.Power Derating Curve

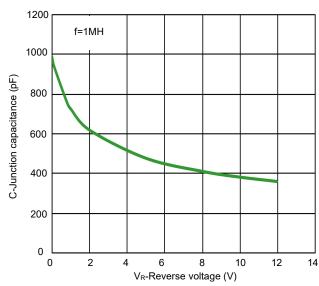
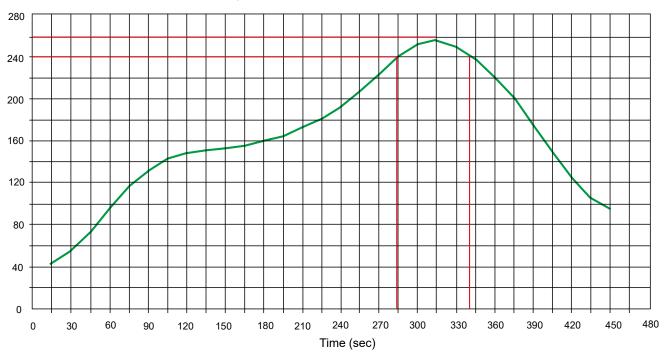


Fig 4. Capacitance vs. Reveres voltage

## **Solder Reflow Recommendation**

Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

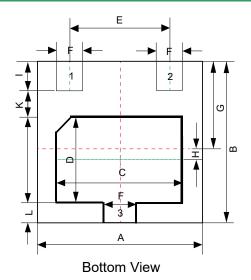


## **PCB Design**

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

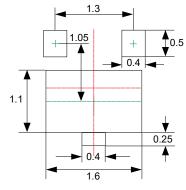
- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- > Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

## Product dimension (DFN2×2-3L)





Dim	Millimeters		
Dim	MIN	MAX	
Α	1.90	2.10	
В	1.90	2.10	
С	1.40	1.60	
D	0.90	1.15	
E	1.30BSC		
F	0.25	0.40	
G	0.90	1.10	
Н	0.20	0.30	
I	0.32	0.48	
J	0.50	0.65	
K	0.20	0.45	
L	0.15	0.30	



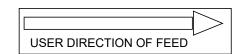
Unit:mm

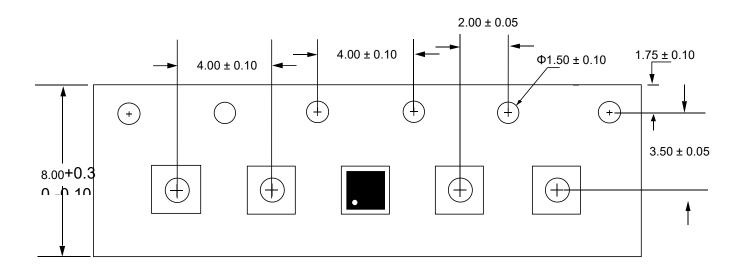
Recommended Soldering Pad

# Ordering information

Device	Package	Reel	Shipping
PTVSHC3N12VU	DFN2×2-3L (Pb-Free)	7"	3000 / Tape & Reel

## Load with information





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