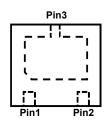


PTVSHC3N15VUH

Transient Voltage Suppressor

Description

The PTVSHC3N15VUH transient voltage suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC3N15VUH protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC3N15VUH is available in a DFN2 \times 2-3L package with working voltages of 15 volt.



Pin configuration

Feature

- 5700W Peak pulse power per line (t_P = 8/20µs)
- DFN2×2-3L package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)
 ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 80A (5/50ns),
 IEC 61000-4-5 (Lightning) 170A (8/20us)

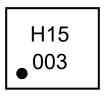
Applications

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- > Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness∶≤3mil



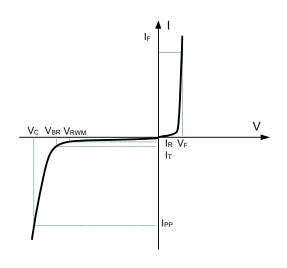


Marking (Top View)

PTVSHC3N15VUH

Electronics Parameter

Symbol	Parameter	
VRWM	Peak Reverse Working Voltage	
I _R	Reverse Leakage Current @ V _{RWM}	
V _{BR}	Breakdown Voltage @ I⊤	
Ιτ	Test Current	
I _{PP}	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
P _{PP}	Peak Pulse Power	
CJ	Junction Capacitance	
lF	Forward Current	
V _F	Forward Voltage @ I _F	



Electrical characteristics per line@25°C(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}				15	V
Breakdown Voltage	V _{BR}	I _t =1mA	16		19	V
Reverse Leakage Current	I _R	V _{RWM} =15V			1	μA
Clamping Voltage	Vc	I _{PP} =100A t _P = 8/20μs		27.5	29	V
Clamping Voltage	Vc	I _{PP} =138Α t _P = 8/20μs		30.5	31	V
Clamping Voltage	Vc	I _{PP} =170A t _P = 8/20µs		33.5	35	V
Junction Capacitance	Cj	V _R =0V f = 1MHz		980		pF

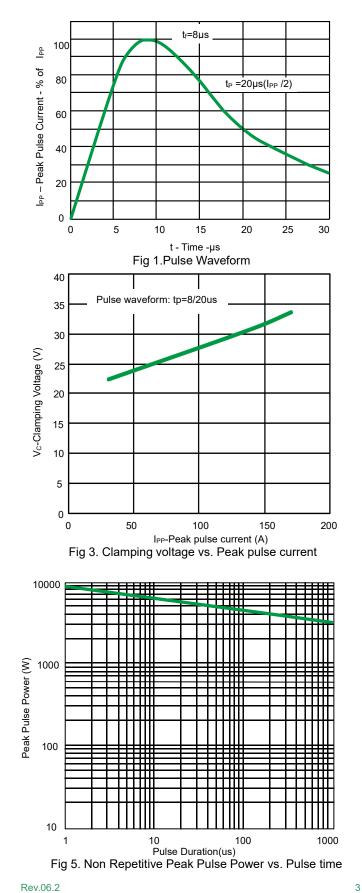
Notes : Measured from pin 3 to pin 1 and pin 2.

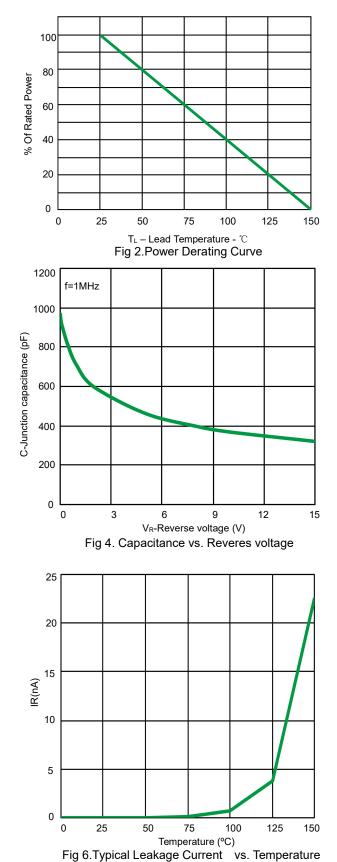
Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power ($t_P = 8/20\mu S$)	P _{pp}	5700	W
Lead Soldering Temperature	TL	260 (10 sec)	°C
Operating Temperature	TJ	-55 to 150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

PTVSHC3N15VUH

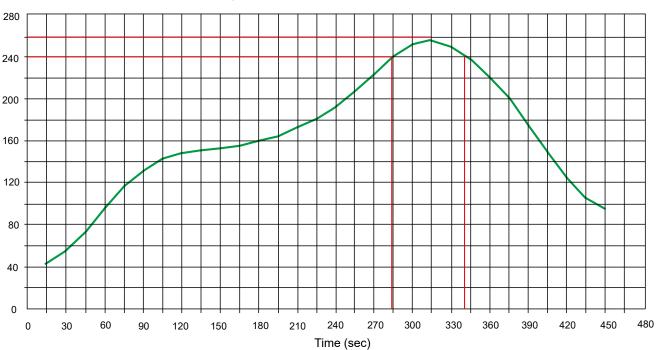
Typical Characteristics





PTVSHC3N15VUH

Solder Reflow Recommendation



Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

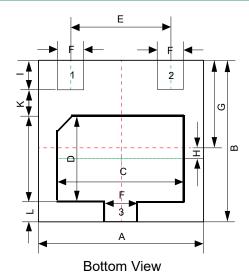
PCB Design

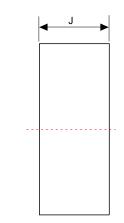
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

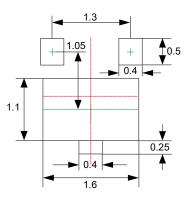
PTVSHC3N15VUH

Product dimension (DFN2×2-3L)





Dim	Millimeters		
Dim	MIN	МАХ	
А	1.90	2.10	
В	1.90	2.10	
С	1.40	1.60	
D	0.90	1.15	
E	1.30BSC		
F	0.25	0.40	
G	0.90	1.10	
н	0.20	0.30	
I	0.32	0.48	
J	0.50	0.65	
К	0.20	0.45	
L	0.15	0.30	



Unit:mm

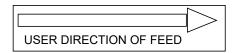
Recommended Soldering Pad

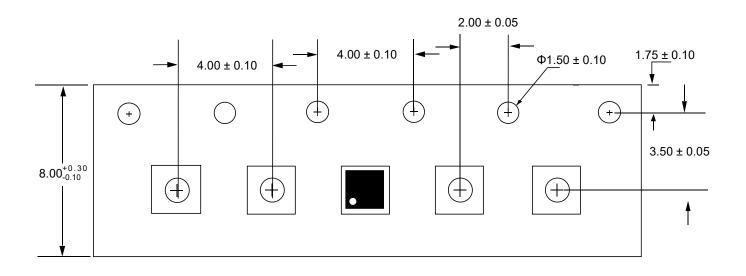
Ordering information

Device	Package	Reel	Shipping
PTVSHC3N15VUH	DFN2×2-3L (Pb-Free)	7"	3000 / Tape & Reel

PTVSHC3N15VUH

Load with information





Unit: mm

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