UTC UNISONIC TECHNOLOGIES CO., LTD

PUMZ1

Preliminary

NPN/PNP SILICON TRANSISTOR

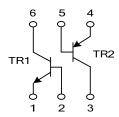
NPN/PNP GENERAL PURPOSE **TRANSISTORS**

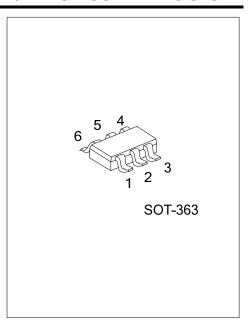
DESCRIPTION

The UTC PUMZ1 is a NPN/PNP transistor, specially used in general purpose of switching and amplifying applications. Thus, two NPN/PNP transistors are operated independently in an SOT-363 package.

FEATURES

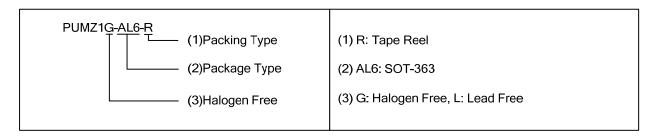
- * Low Current: 100mA (MAX.)
- * Low Voltage: 40V (MAX.)
- * Less Number of Components And Boardspace Required
- **SYMBOL**



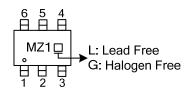


ORDERING INFORMATION

Ordering Number		Deelvere	Pin Assignment					Doolsing	
Lead Free	Halogen Free	Package	1	2	3	4	5	6	Packing
PUMZ1L-AL6-R	PUMZ1G-AL6-R	SOT-363	E1	B1	C2	E2	B2	C1	Tape Reel



MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified.)

DADAMETED	OVADOL	DATINIOO	LINUT			
PARAMETER	SYMBOL	RATINGS	UNIT			
Per Transistor; For The PNP Transistor With Negative Polarity						
Collector- Base Voltage	V_{CBO}	50	V			
Collector-Emitter Voltage	V_{CEO}	40	V			
Emitter-Base Voltage	V_{EBO}	5	V			
Collector Current (DC)	Ic	100	mA			
Peak Collector Current	I _{CM}	200	mA			
Total Power Dissipation	P_{D}	300	mW			
Junction Temperature	T_J	150	Ŝ			
Storage Temperature	T _{STG}	-55 ~ + 150	°C			

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL RATINGS		UNIT	
Junction to Ambient	θ_{JA}	416	K/W	

Note: Device mounted on an FR4 printed-circuit board.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT			
Per Transistor; For The PNP Transistor With Negative Polarity									
Collect Cut-off Current	lono.	$V_{CB} = 30V, I_{E} = 0$			100	nA			
Collect Cut-on Current		$V_{CB} = 30V, I_{E} = 0, T_{J} = 150^{\circ}C$			10	μΑ			
Emitter Cut-off Current	I _{EBO}	$V_{EB} = 4V$, $I_C = 0$			100	nA			
DC Current Gain	h _{FE}	V _{CE} =6V, I _C =1mA	120						
Collector-Emitter Saturation Voltage(Note)	V _{CE(SAT)}	$I_C = 50 \text{mA}, I_B = 5 \text{mA}$			200	mV			
Collector Consoitance		I _E =i _e =0; V _{CB} =12V; f =1MHz			1.5	pF			
Collector Capacitance TR2	C _C				2.2	pF			
Transition Frequency	f_{T}	$V_{CE} = 12V$, $I_{C} = 2mA$, $f = 100MHz$	100			MHz			

Note: Pulse test: $t_P \le 300 \ \mu s$; $\delta \le 0.02$.

^{2.} Device mounted on an FR4 printed-circuit board.

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