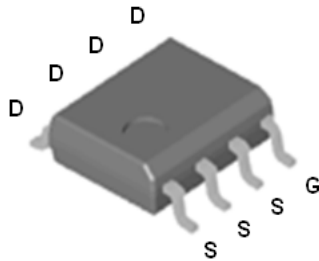


PV600BA

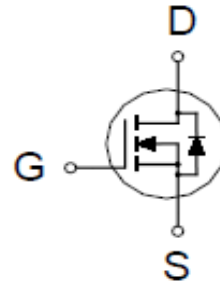
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	10.8m Ω @ $V_{GS} = 10V$	10A



SOP-8



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_A = 25^\circ\text{C}$	I_D	10	A
	$T_A = 70^\circ\text{C}$		8	
Pulsed Drain Current ¹		I_{DM}	40	
Avalanche Current		I_{AS}	17.6	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	15.5	mJ
Power Dissipation	$T_A = 25^\circ\text{C}$	P_D	1.95	W
	$T_A = 70^\circ\text{C}$		1.25	
Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		64	$^\circ\text{C} / \text{W}$
Junction-to-Case	$R_{\theta JC}$		25	

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.3	1.7	2.3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24V, V_{GS} = 0V$			1	μA
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 55\text{ }^{\circ}C$			10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 10A$		10.6	15	m Ω
		$V_{GS} = 10V, I_D = 10A$		8.3	10.8	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 10A$		40		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$		616		pF
Output Capacitance	C_{oss}			108		
Reverse Transfer Capacitance	C_{rss}			76		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		2.8		Ω
Total Gate Charge ²	$Q_g(V_{GS}=10V)$	$V_{DS} = 15V, I_D = 10A$		13.8		nC
	$Q_g(V_{GS}=4.5V)$			7.2		
Gate-Source Charge ²	Q_{gs}			1.7		
Gate-Drain Charge ²	Q_{gd}			3.9		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DS} = 15V,$ $I_D \cong 10A, V_{GS} = 10V, R_{GEN} = 6\Omega$		15		nS
Rise Time ²	t_r			41		
Turn-Off Delay Time ²	$t_{d(off)}$			50		
Fall Time ²	t_f			27		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25\text{ }^{\circ}C$)						
Continuous Current	I_S				1.8	A
Forward Voltage ¹	V_{SD}	$I_F = 10A, V_{GS} = 0V$			1.1	V
Diode Reverse Recovery Time	t_{rr}	$I_F = 10A, di/dt = 100A/\mu S$		12		nS
Diode Reverse Recovery Charge	Q_{rr}			3		nC

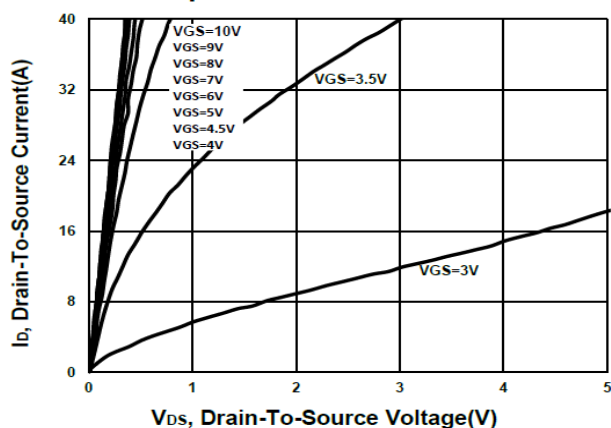
¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

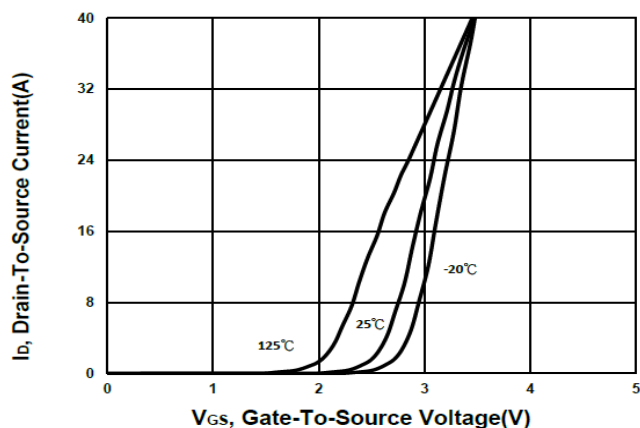
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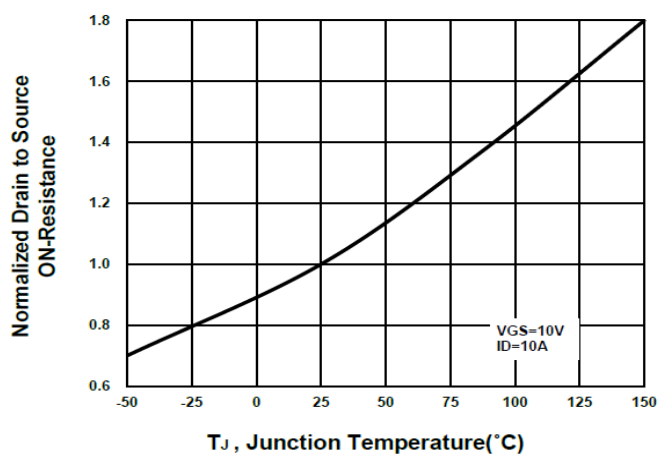
Output Characteristics



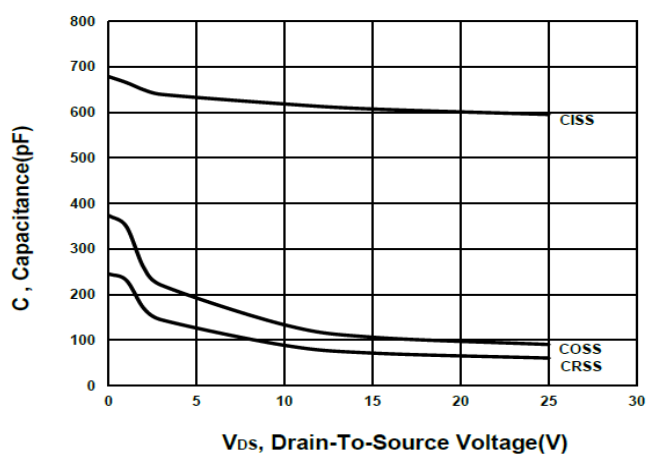
Transfer Characteristics



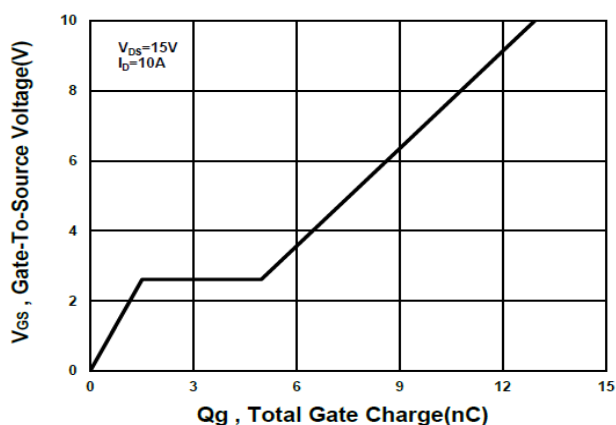
On-Resistance VS Temperature



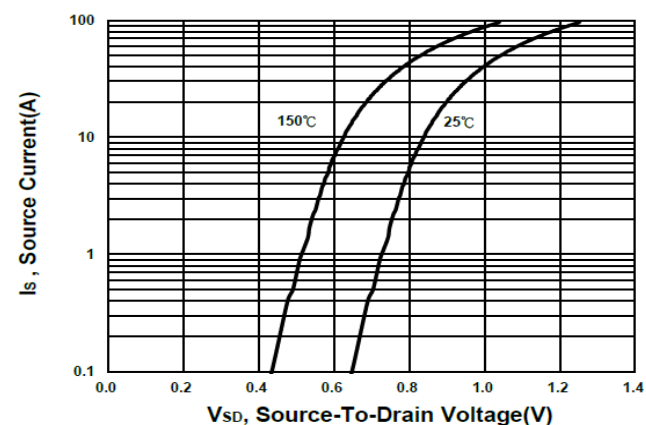
Capacitance Characteristic



Gate charge Characteristics



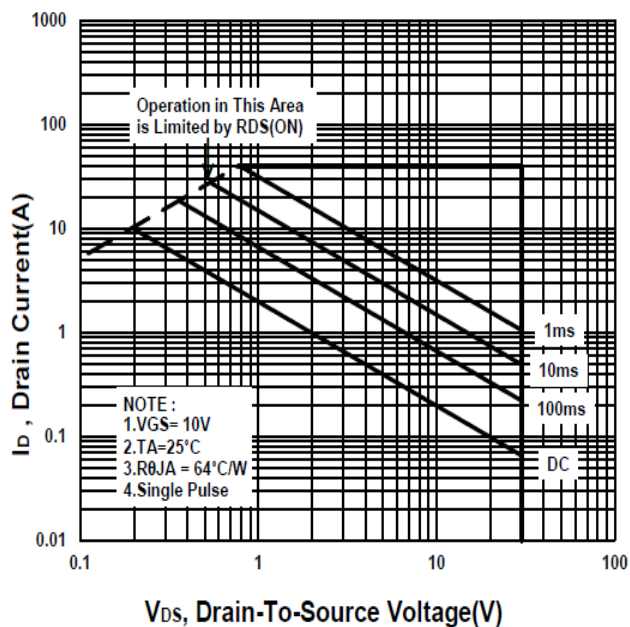
Source-Drain Diode Forward Voltage



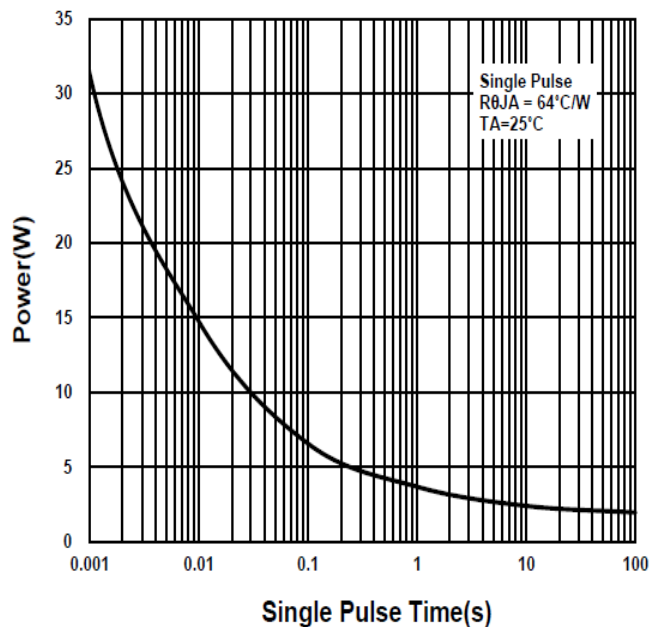
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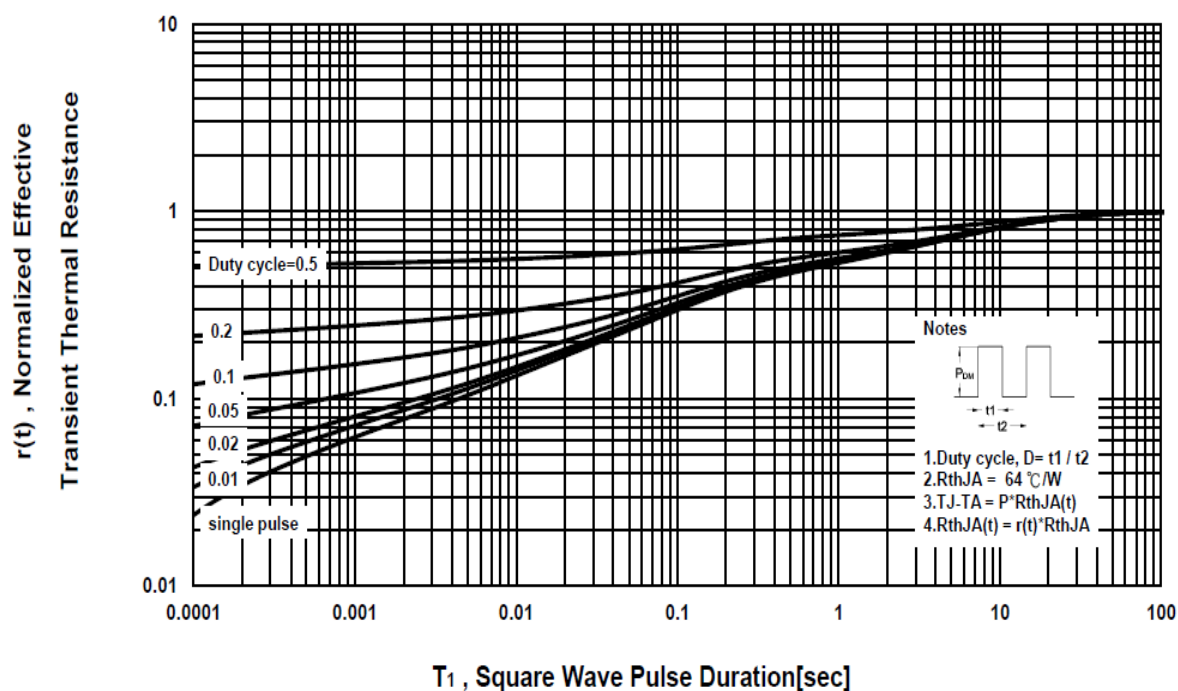
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



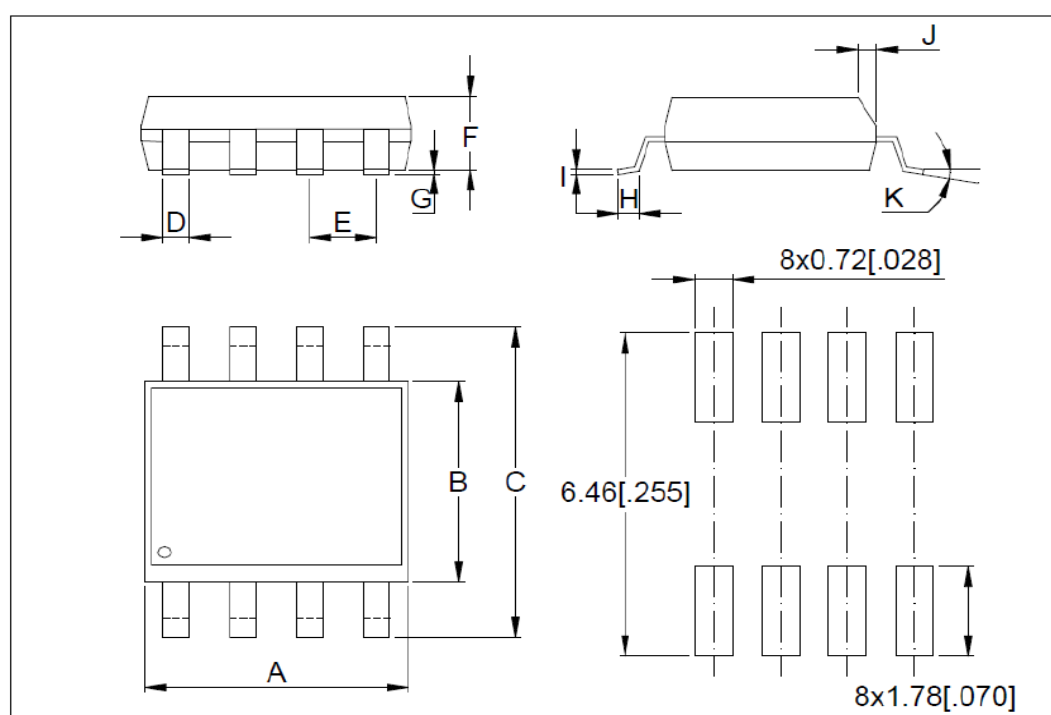
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Package Dimension

SOP-8 MECHANICAL DATA

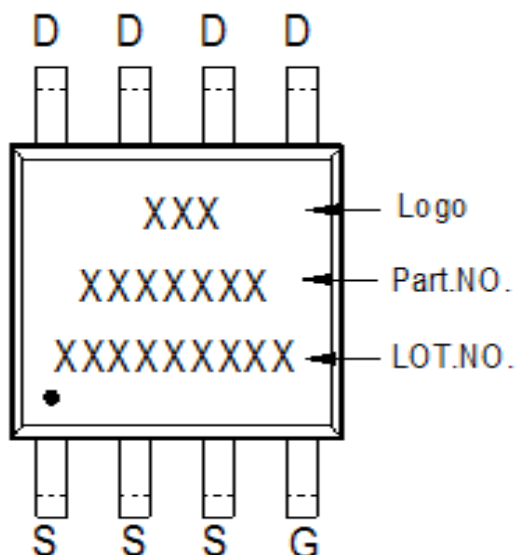
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				



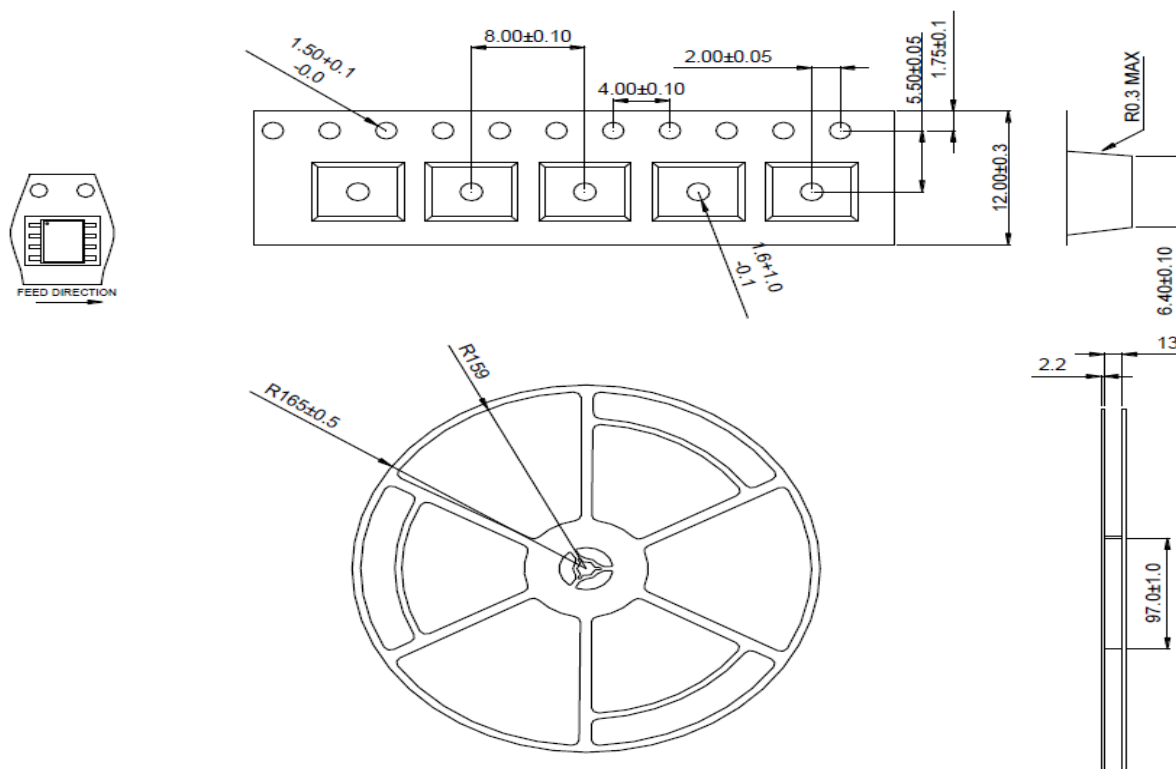
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A. Marking Information



B. Tape&Reel Information:2500pcs/Reel



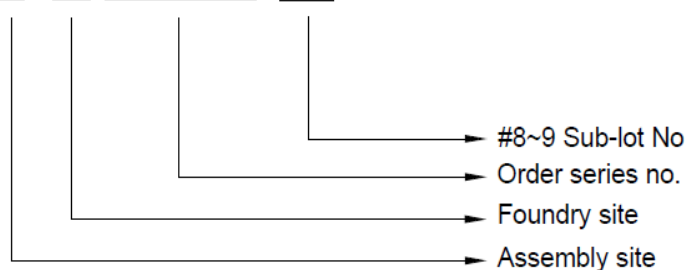
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C. Lot.No. & Date Code rule

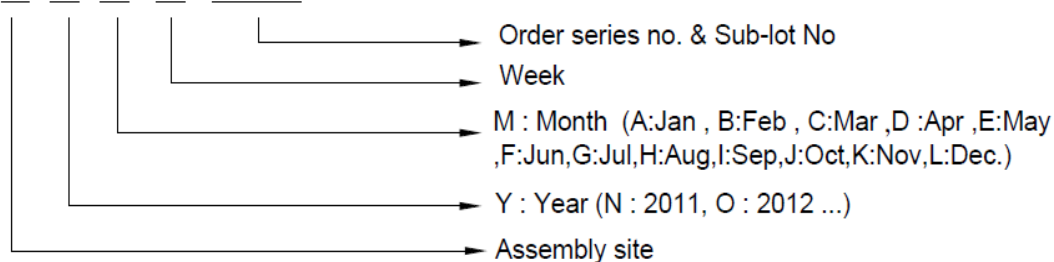
1.LOT.NO.

M N 15M21 03



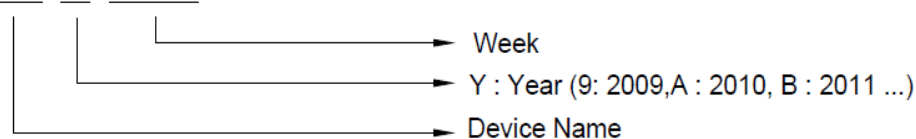
2.Date Code

D Y M X XXX



3.Date Code (for Small package)

XX Y WW



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D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文"0"和数字"0", "G"和"Q"的字型即可)
3	Great Power	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	Pb Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
12	Scan info	Device / Lot / D/C / QTY , Insert " / " between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least