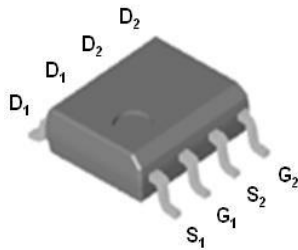


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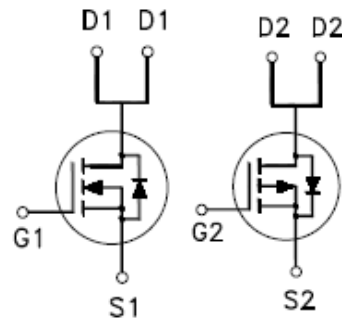
N&P-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D	Channel
30V	22m Ω @ $V_{GS} = 10V$	7A	N
-30V	28m Ω @ $V_{GS} = -10V$	-6.4A	P



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ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	CH.	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	N	30	V
			P	-30	
Gate-Source Voltage		V_{GS}	N	± 20	
			P	± 20	
Continuous Drain Current	$T_A = 25\text{ }^{\circ}\text{C}$	I_D	N	7	A
			P	-6.4	
	$T_A = 70^{\circ}\text{C}$		N	5.6	
			P	-5.1	
Pulsed Drain Current ¹		I_{DM}	N	25	
			P	-23	
Avalanche Current		I_{AS}	N	12	
			P	-19.9	
Avalanche Energy	L = 0.1mH	E_{AS}	N	7.2	mJ
			P	19.8	
Power Dissipation ³	$T_A = 25\text{ }^{\circ}\text{C}$	P_D	N	2	W
			P		
	$T_A = 70\text{ }^{\circ}\text{C}$		N	1.3	
			P		
Junction & Storage Temperature Range		T_j, T_{stg}		-55 to 150	$^{\circ}\text{C}$

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N&P-Channel Enhancement Mode MOSFET

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL		TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$t \leq 10s$	$R_{\theta JA}$	N-ch		60	°C / W
	Steady-State				77	
Junction-to-Ambient ²	$t \leq 10s$	$R_{\theta JA}$	P-ch		60	
	Steady-State				85	

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

³The Power dissipation is based on $R_{\theta JA} t \leq 10s$ value.

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	CH.	LIMITS			UNITS
				MIN	TYP	MAX	
STATIC							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	N	30			V
		$V_{GS} = 0V, I_D = -250\mu A$	P	-30			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	N	1	1.75	2.5	
		$V_{DS} = V_{GS}, I_D = -250\mu A$	P	-1	-1.5	-2.5	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$	N			± 100	nA
		$V_{DS} = 0V, V_{GS} = \pm 20V$	P			± 100	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24V, V_{GS} = 0V$	N			1	μA
		$V_{DS} = -24V, V_{GS} = 0V$	P			-1	
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 55^\circ C$	N			10	
		$V_{DS} = -20V, V_{GS} = 0V, T_J = 55^\circ C$	P			-10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 6A$	N		22	32	m Ω
		$V_{GS} = -4.5V, I_D = -5A$	P		32	45	
		$V_{GS} = 10V, I_D = 7A$	N		16	22	
		$V_{GS} = -10V, I_D = -6A$	P		22	28	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10V, I_D = 7A$	N		30		S
		$V_{DS} = -10V, I_D = -6A$	P		17		

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N&P-Channel Enhancement Mode MOSFET

DYNAMIC							
Input Capacitance	C_{iss}	N-Channel $V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$	N		300		pF
			P		817		
Output Capacitance	C_{oss}	P-Channel $V_{GS} = 0V, V_{DS} = -15V, f = 1MHz$	N		65		
			P		138		
Reverse Transfer Capacitance	C_{rss}		N		43		
			P		113		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$	N		2.3		Ω
			P		12		
Total Gate Charge ²	Q_g	N-Channel $V_{DS} = 15V, V_{GS} = 10V, I_D = 7A$	N		7.4		nC
			P		20		
Gate-Source Charge ²	Q_{gs}	P-Channel $V_{DS} = -15V, V_{GS} = -10V, I_D = -6A$	N		0.8		
			P		1.7		
Gate-Drain Charge ²	Q_{gd}		N		2.1		
			P		5.1		
Turn-On Delay Time ²	$t_{d(on)}$	N-Channel $V_{DS} = 15V, I_D \cong 7A, V_{GS} = 10V, R_{GEN} = 6\Omega$	N		15		nS
			P		20.8		
Rise Time ²	t_r	P-Channel $V_{DS} = -15V, I_D \cong -6A, V_{GS} = -10V, R_{GEN} = 6\Omega$	N		15		
			P		16.6		
Turn-Off Delay Time ²	$t_{d(off)}$		N		32		
			P		44		
Fall Time ²	t_f		N		15		
			P		14		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)							
Continuous Current	I_S		N			1.8	A
			P			-2	
Forward Voltage ¹	V_{SD}	$I_F = 7A, V_{GS} = 0V$	N			1.1	V
		$I_F = -6A, V_{GS} = 0V$	P			-1	
Reverse Recovery Time	t_{rr}	$I_F = 7A, di_F/dt = 100A / \mu S$ $I_F = -6A, di_F/dt = 100A / \mu S$	N		9.5		nS
			P		12.2		
Reverse Recovery Charge	Q_{rr}		N		3		nC
			P		3.5		

¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.

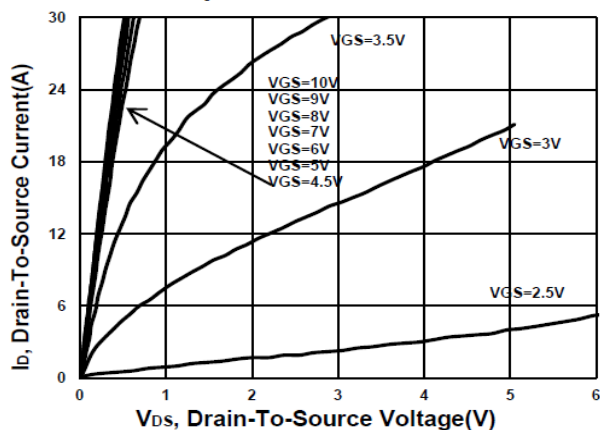
²Independent of operating temperature.

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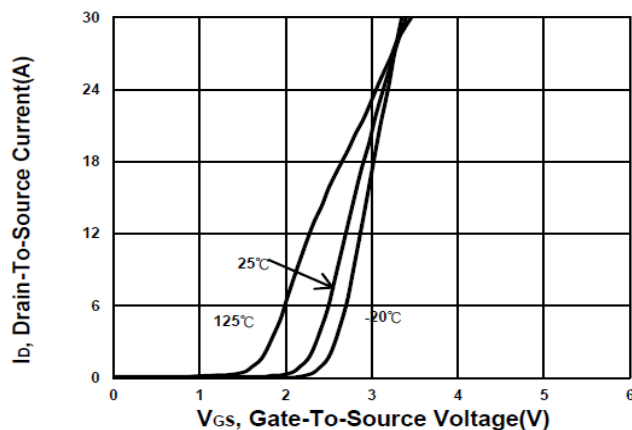
N&P-Channel Enhancement Mode MOSFET

N-CHANNEL

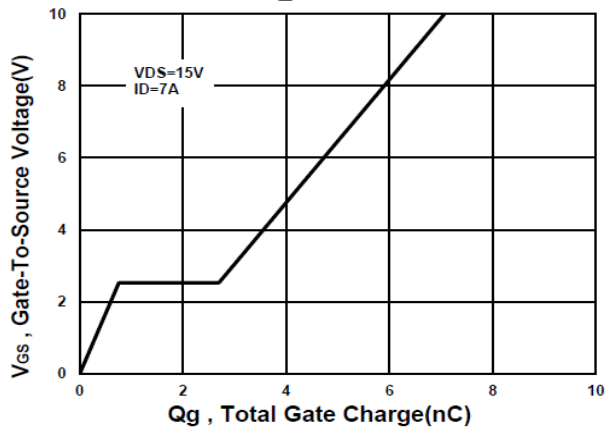
Output Characteristics



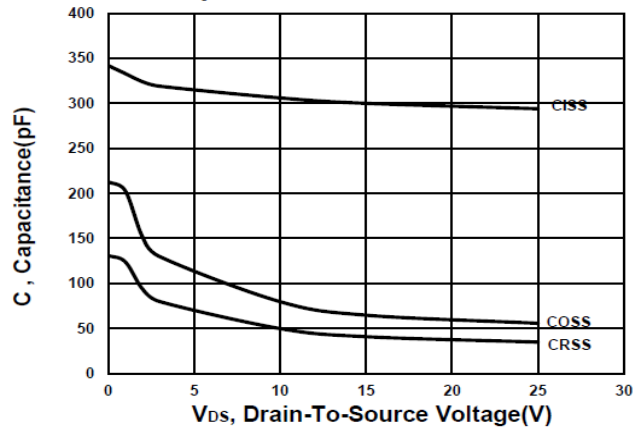
Transfer Characteristics



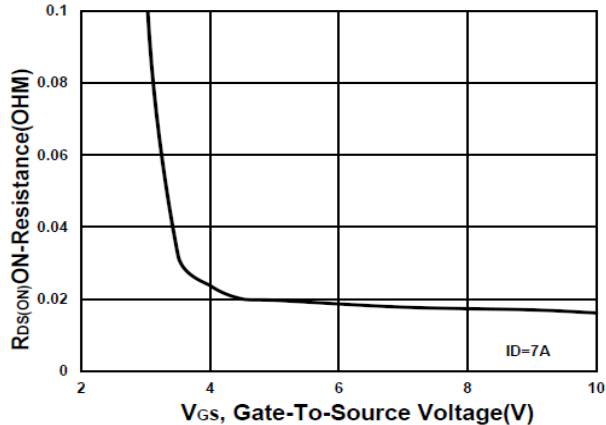
Gate charge Characteristics



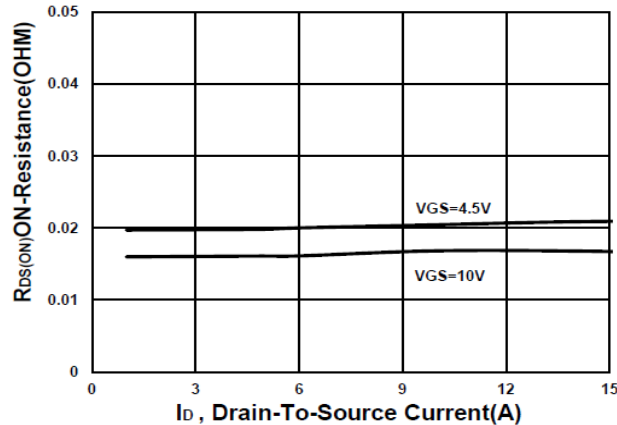
Capacitance Characteristic



On-Resistance VS Gate-To-Source



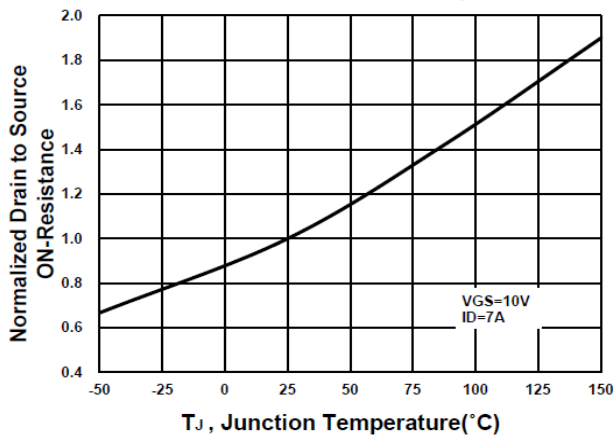
On-Resistance VS Drain Current



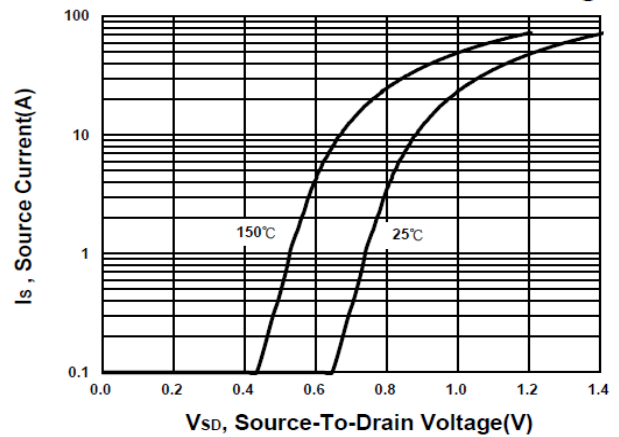
PV601CA

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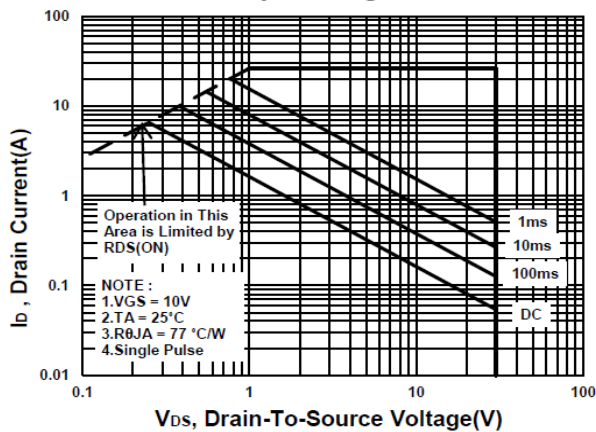
On-Resistance VS Temperature



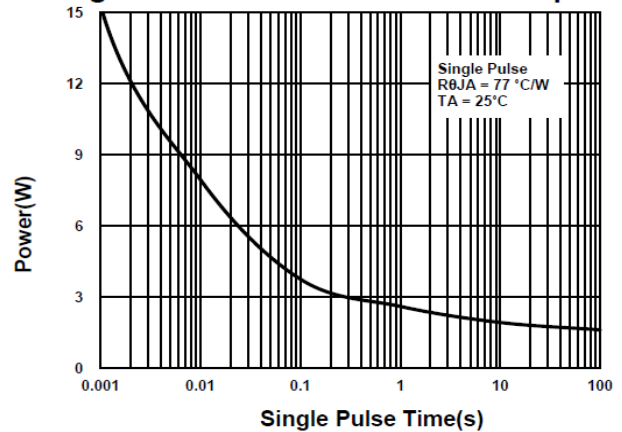
Source-Drain Diode Forward Voltage



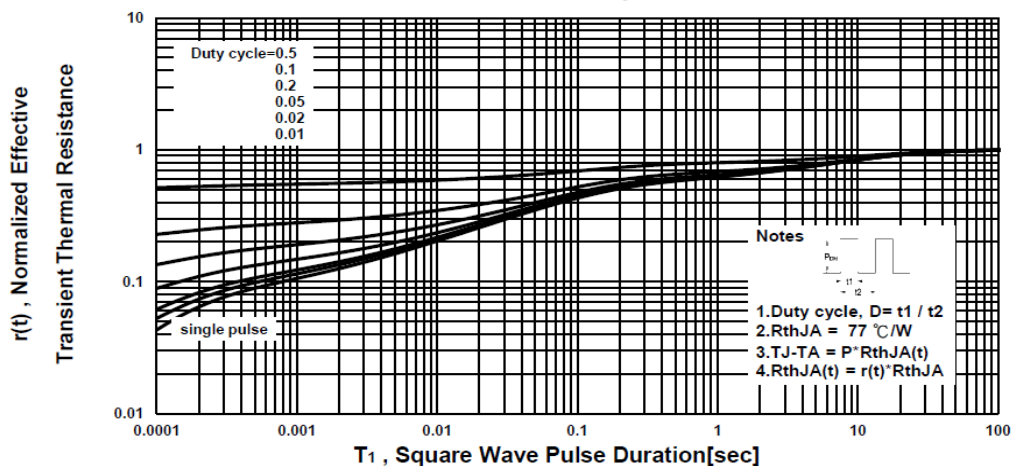
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

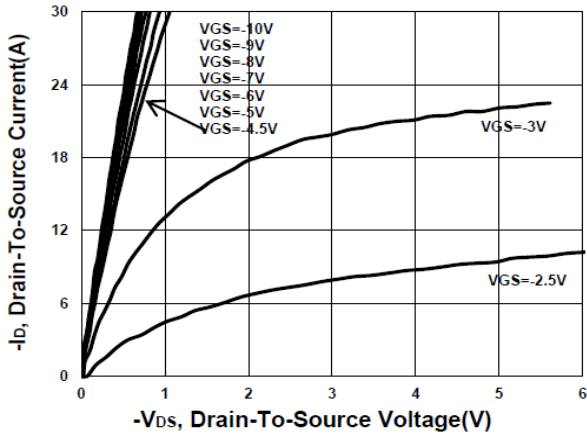


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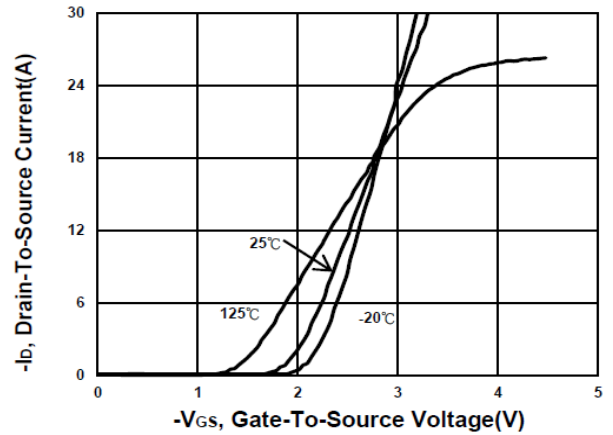
N&P-Channel Enhancement Mode MOSFET

P-CHANNEL

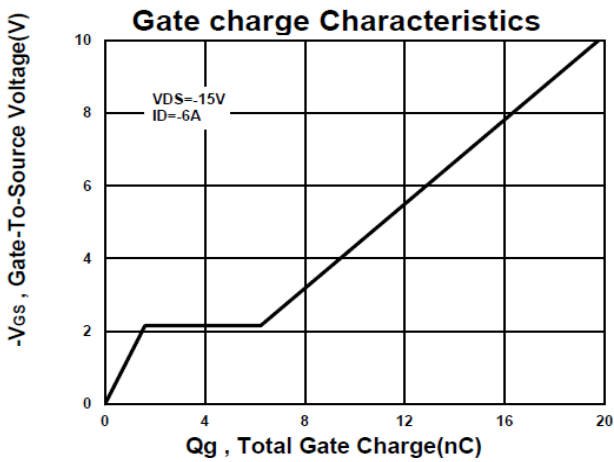
Output Characteristics



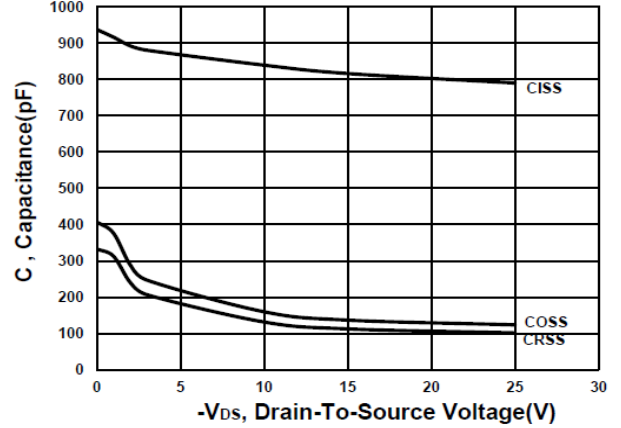
Transfer Characteristics



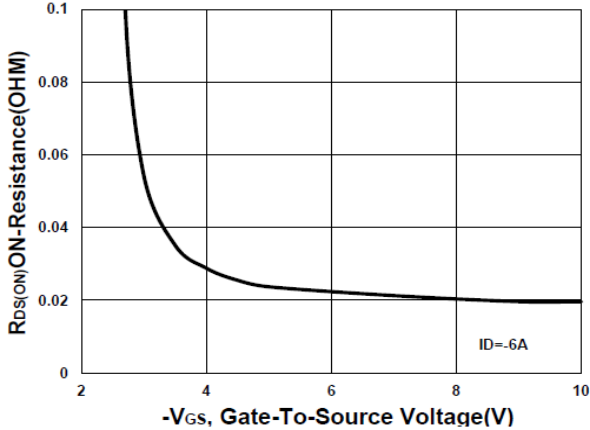
Gate charge Characteristics



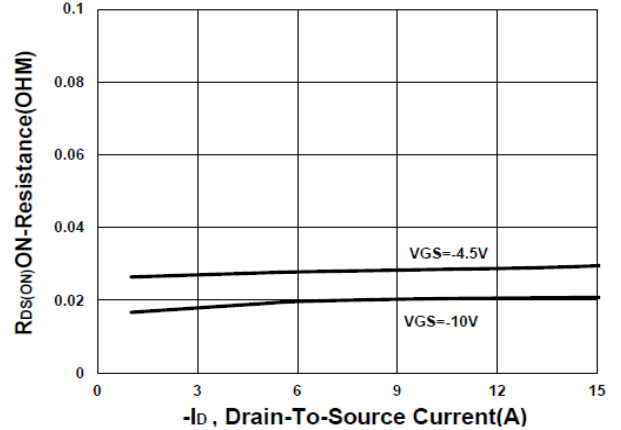
Capacitance Characteristic



On-Resistance VS Gate-To-Source



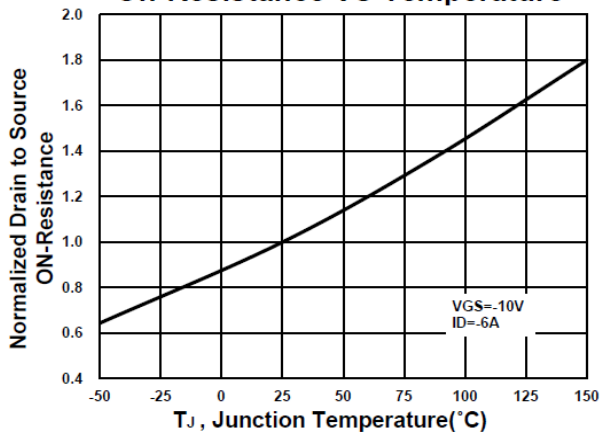
On-Resistance VS Drain Current



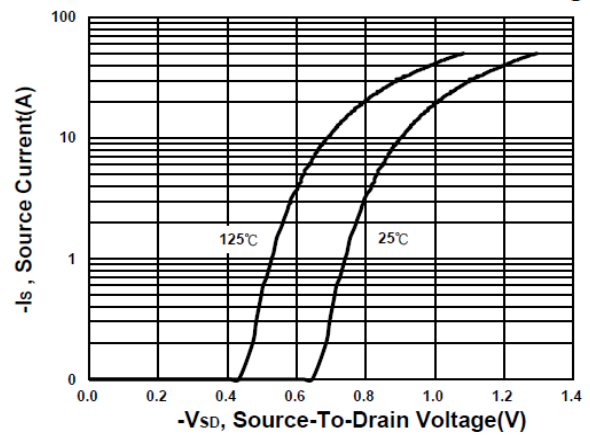
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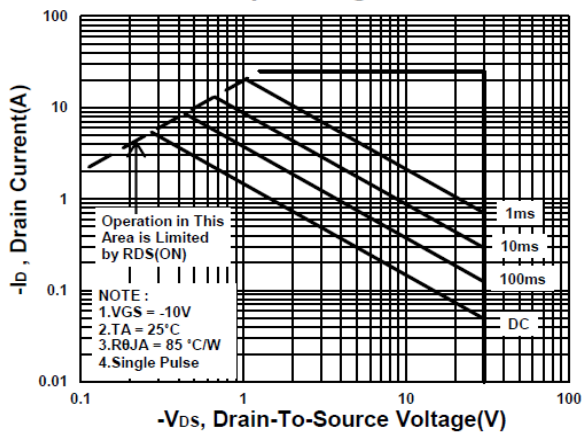
On-Resistance VS Temperature



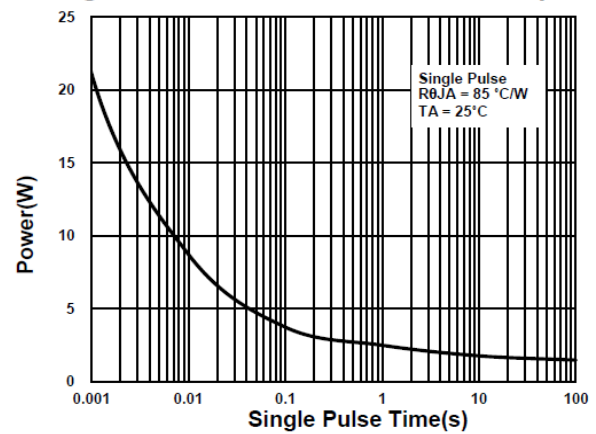
Source-Drain Diode Forward Voltage



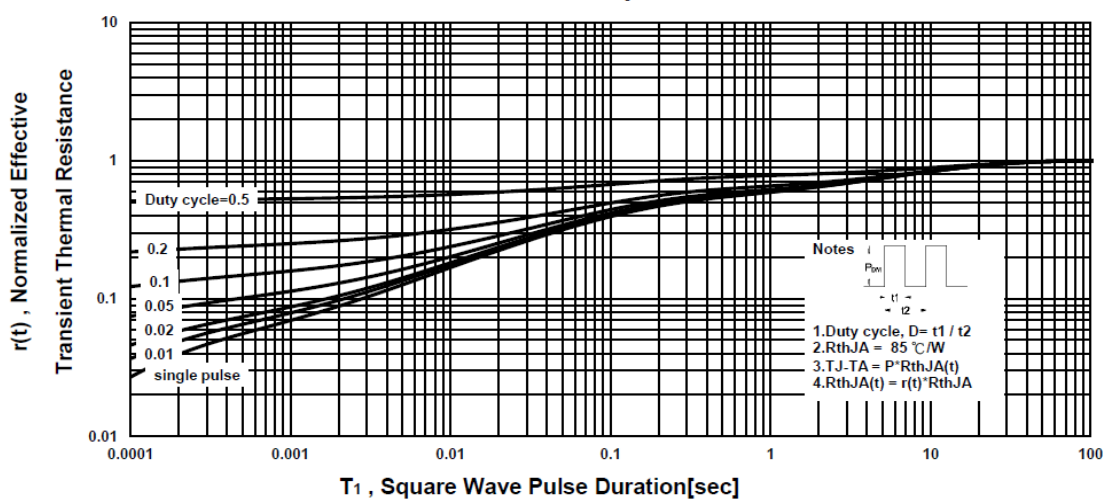
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



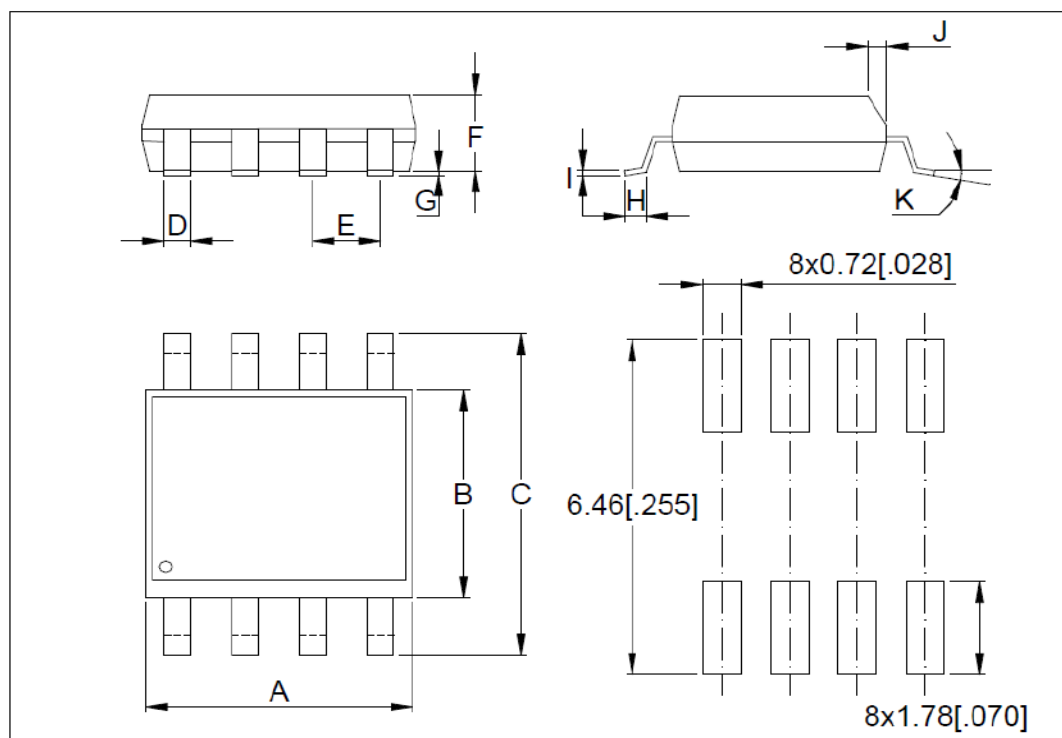
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N&P-Channel Enhancement Mode MOSFET

Package Dimension

SOP-8 MECHANICAL DATA

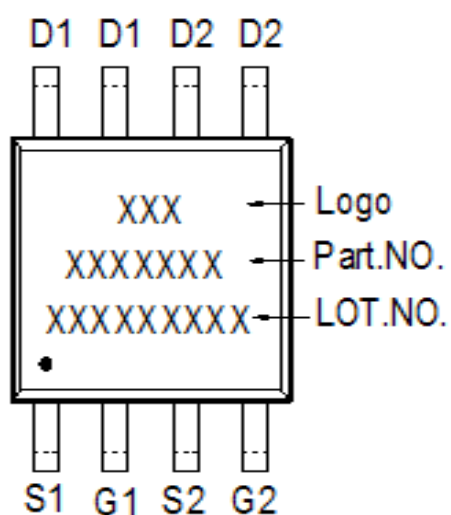
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				



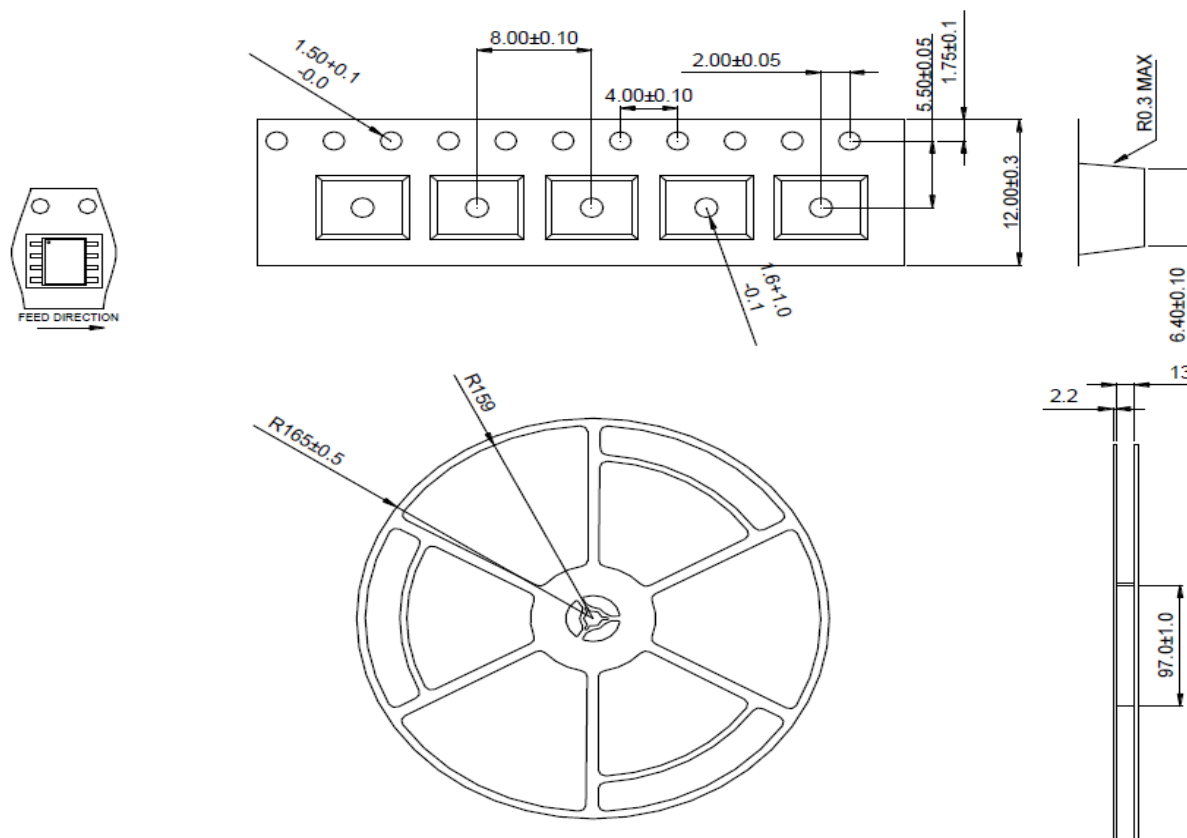
PV601CA

N&P-Channel Enhancement Mode MOSFET

A. Marking Information



B. Tape&Reel Information:2500pcs/Reel



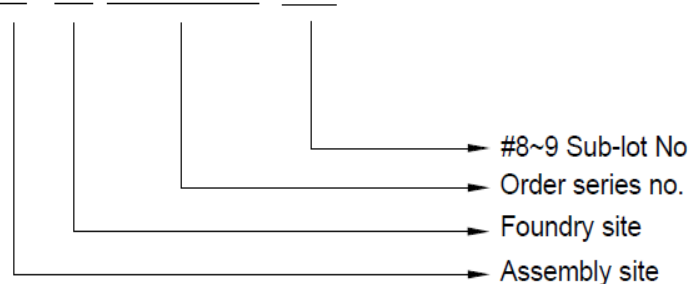
PV601CA

N&P-Channel Enhancement Mode MOSFET

C. Lot.No. & Date Code rule

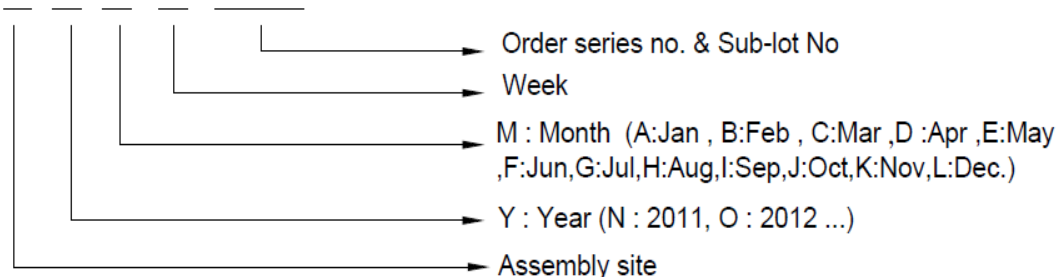
1.LOT.NO.

M N 15M21 03



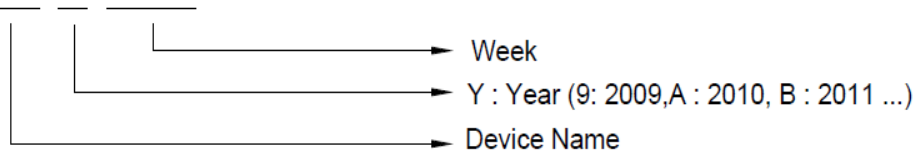
2.Date Code

D Y M X XXX



3.Date Code (for Small package)

XX Y WW



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N&P-Channel Enhancement Mode MOSFET

D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文"0"和数字"0", "G"和"Q"的字型即可)
3	Great Power	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	Pb Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
12	Scan info	Device / Lot / D/C / QTY , Insert " / " between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least