

Version : 0.2

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Http://www.lcdfriends.com

TECHNICAL SPECIFICATION

MODEL NO. : PW070DS1

☐ Customer's Confirmation

Customer Name _____

Date _____

By _____

☐ PVI's Confirmation

Confirmed By _____

Prepared By _____

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Date : Feb. 02,2001

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TECHNICAL SPECIFICATION

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1. Application

This technical specification applies to 7.0" color TFT-LCD module, PW070DS1. The applications of the panel are car TV, portable DVD, GPS, multimedia applications and others AV system.

2. Features

- . Compatible with NTSC & PAL system
- . Pixel in stripe configuration
- . Slim and compact
- . High Brightness
- . Image Reversion : Up/Down and Left/Right
- . Multi Video Display Mode

3. Mechanical Specifications

Parameter	Specifications	Unit
Screen Size	7.0 (16:9 diagonal)	inch
Display Format	1440 (H) × 234(V)	dot
Active Area	154.08 (H)× 87.05 (V)	mm
Dot Pitch	0.107(H)× 0.372 (V)	mm
Pixel Configuration	Stripe	
Outline Dimension	167.1 (W)× 102.0 (H)× 12.0 (D)(typ.)	mm
Surface Treatment	Anti-Glare and Hard Coating	
Weight	205±10	g

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5. Input / Output Terminals

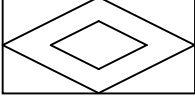
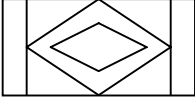
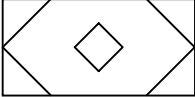
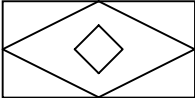
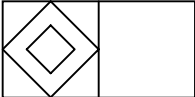
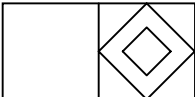
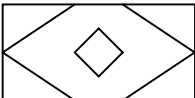
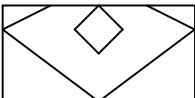
5-1) TFT-LCD Panel Driving

The interface connector is 6210-30P Series manufactured by ELCO or equivalent.
(0.5mm pitch 30 pin)

The connector interface pin assignments are listed in the Table below.

Pin No	Symbol	I/O	Description	Remark
1	PSI	O	Synchronize Pulse for Decoder	
2	VR	I	Red Video Input	
3	VG	I	Green Video Input	
4	VB	I	Blue Video Input	
5	GND	I	Ground	
6	CSY	I	Composite Sync Input	
7	NC		No Connection	
8	NC		No Connection	
9	NC		No Connection	
10	NC		No Connection	
11	MODE 1	I	Display mode selection pin 1 (refer to 5.2)	
12	MODE 2	I	Display mode selection pin 2 (refer to 5.2)	
13	MODE 3	I	Display mode selection pin 3 (refer to 5.2)	
14	GND	I	Ground	
15	BLK	O	Video Signal Mask Timing Output	
16	HSY	O	Horizontal Sync Output	
17	VSX	O	Vertical Sync Output	
18	VDD	I	Input Voltage For Logic/Logic Voltage For Source Driver	
19	NC		No Connection	
20	FRP	O	Video Polarity Control Signal	
21	GND	I	Ground	
22	U/D	I	Scanning Direction : (Low : Normal , High : Reverse)	
23	L/R	I	Scanning Direction : (Low : Normal , High : Reverse)	
24	N/P	I	NTSC/PAL Selection Signal(Low : PAL , High : NTSC)	
25	NC		No Connection	
26	NC		No Connection	
27	GND	I	Ground	
28	VCC	I	Analog Voltage For Source Driver/Logic Voltage For Gate Driver	
29	VGH	I	Positive Voltage For Gate Driver	
30	VGL	I	Negative Voltage For Gate Driver	

5.2)Display Mode

Display Mode	Display (Input Signal of 4:3 Aspect Ratio)	NO:11	NO:12	NO:13	Note
		Mode 1	Mode 2	Mode 3	
Full		Low	Low	Low	Input Video Signals Are Displayed In Full Screen.
Normal Center		High	Low	Low	Input Video Signals Are Displayed In The Center Screen.(4:3 Aspect Ratio)
Zoom 1		High	High	Low	Input Video Signal Of Central 176 Lines Are Display In Full Screen.(Vertically Extension)
Wide		Low	High	Low	Input Video Signals Are Displayed In Full Screen. (Horizontal Modification)
Normal Left		High	Low	High	Input Video Signals Are Display In the Left Screen. (4:3 Aspect Ratio)
Normal Right		Low	High	High	Input Video Signals Are Display In the Right Screen. (4:3 Aspect Ratio)
Zoom 2		Low	Low	High	Input Video Signal Of Central 204 Lines Are Display In Full Screen.(Vertically Extension And Horizontal Modification)
Zoom 3		High	High	High	Same As Zoom 2 Mode Vertically Offset Centered

6 Absolute Maximum Ratings:

The followings are maximum values , which if exceeded, may cause faulty operation or damage to the unit.

Parameter		Symbol	MIN.	MAX.	Unit	Remark
Supply Voltage For Source Driver		V _{CC}	-0.5	7	V	
		V _{DD}	-0.5	7	V	
Supply Voltage For Gate Driver		V _{GH} - V _{GL}	-0.3	40	V	
	H Level	V _{GH}	0	40	V	
	L Level	V _{GL}	-20	0	V	
Analog Signal Input Level		V _R ,V _G ,V _B	-0.3	7.0	V	Note 6-1
Digital Input Signals			-0.3	5.5	V	
Digital Output Signals			-0.3	5.5	V	
Storage Temperature			-30	+80	°C	
Operation Temperature			-20	+70	°C	Note 6-2

Notes:6-1 Analog Input Voltage means V_R,V_G,V_B.

Notes:6-2 Operating Temperature define that contrast, response time, other display optical character are Ta=+25.

7 Electrical Characteristics

7-1) Recommended Driving condition for TFT-LCD panel

Parameter		Symbol	MIN.	Typ	MAX	Unit	Remark
Supply Voltage For Source Driver	Analog	V _{CC}	4.5	5.0	5.5	V	
	Logic	V _{DD}	4.5	5.0	5.5	V	
Supply Voltage For Gate Driver	H level	V _{GH}	+15	+17	+19	V	
	L level	V _{GL}	-16	-15	-14	V	
Supply Voltage For controller		V _{DD}	4.5	5.0	5.5	V	
Analog Signal input Level	Amplitude		0.3		V _{CC} -0.3	V	
Digital input voltage	H level	V _{IN}	0.7 V _{DD}	-	V _{DD}	V	
	L level	V _{IL}	-0.3	-	0.3 V _{DD}	V	
Digital output voltage	H level	V _{OH}	0.7 V _{DD}	-	V _{DD}	V	
	L level	V _{OL}	-0.3	-	0.3 V _{DD}	V	

7-2) Backlight driving & Power Consumption

Pin No	Symbol	Description	Remark
1	VL1	Input terminal (Hi voltage side)	Wire color: pink
2	NC	No Connection	
3	VL2	Input terminal (Low voltage side)	Wire color: white Note 7-1

Note 7-1 : Low voltage side of backlight inverter connects with Ground of inverter circuits.

Recommended Driving condition for backlight

Ta= 25 °C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Lamp voltage	V _L		810		Vrms	I _L =6mA
Lamp current	I _L	4.5	6	7	mA	
Lamp frequency	P _L	35		60	KHz	Note 7-2
Kick-off voltage(25°C)	Vs	TBD	TBD	1095	Vrms	Note 7-3
Kick-off voltage(0°C)	Vs	TBD	TBD	1340	Vrms	Note 7-3

Note 7-2 : The waveform of lamp driving voltage should be as closed to a perfect sine wave as possible.

Note 7-3 : The Kick-off times ≥ 1 sec.

Power Consumption

Ta= 25 °C

Parameter	Symbol	Conditions	TYP.	MAX	Unit	Remark
Supply current for Gate Driver (Hi level)	I _{GH}	V _{GH} = +17V	0.15	0.2	mA	
Supply current for Gate Driver (Low level)	I _{GL}	V _{GL} = -15V	20	50	mA	
Supply current for Source Driver	I _{CC}	V _{CC} = +5V	20	30	mA	
Supply current for controller	I _{DD}	V _{DD} = +5V	20	30	mA	
LCD Panel Power Consumption			0.50		W	Note 7-4
Backlight Lamp Power Consumption			4.86		W	Note 7-5

Note 7-4 : The power consumption for backlight is not included.

Note 7-5 : Backlight lamp power consumption is calculated by I_L × V_L.

7-4) Input / Output Connector

A) LCD Module Connector

The interface connector is 6210-30p Series manufactured by Elco or equivalent (0.5mm pitch 30pin)

B) Backlight Connector, JST BHR-03VS-1. Pin No. : 3, Pitch : 4 mm

7-5) Pixel Arrangement and Output Interface Pin

	1	2	3	4	5	6		1438	1439	1440
1	R	G	B	R	G	B		R	G	B
2	R	G	B	R	G	B		R	G	B
3	R	G	B	R	G	B		R	G	B
233	R	G	B	R	G	B		R	G	B
234	R	G	B	R	G	B		R	G	B

7-6)Signal Timing Waveforms
Timing Specification
[Horizontal]

Parameter	Symbol	Condition	NTSC	PAL	Unit	Notes
Horizontal Start Position	HPOS	-	11.35	11.54	usec	Note7-6
Horizontal Display Area	HDIS	-	50.01	50.36	usec	

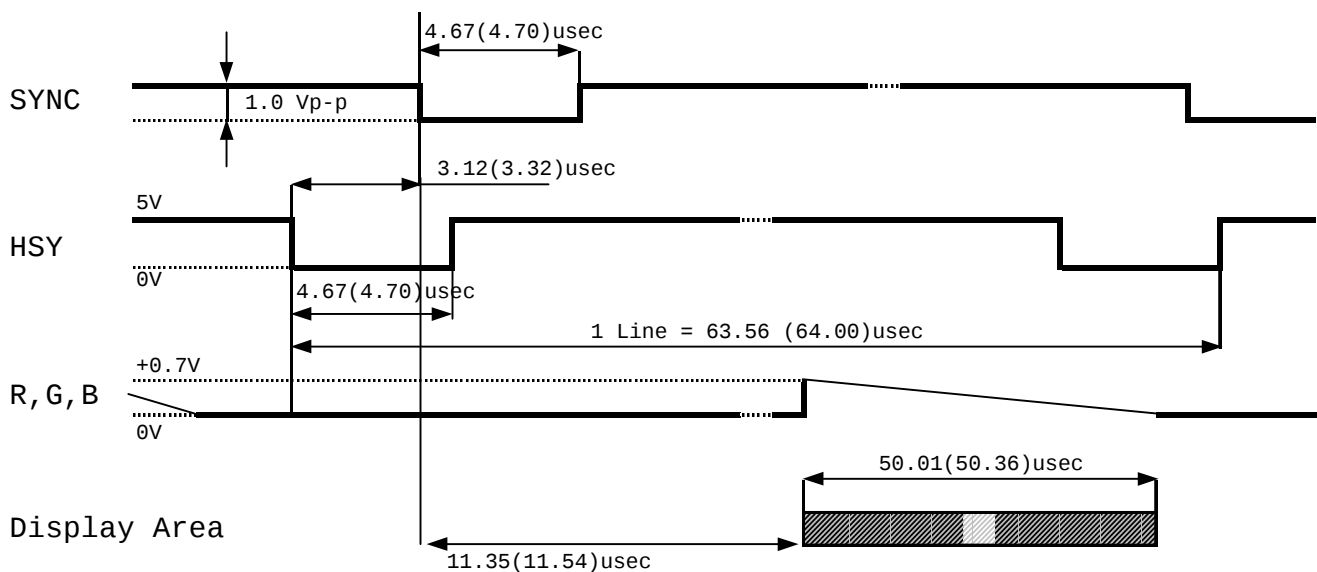
Notes :7-6

1. Sampling start based on the composite sync falling edge.
2. This value is default , if you want to charge ,please contact PVI.

[Vertical]

Parameter	Symbol	Condition		Display Mode					Unit	Notes
				Full Normal	Wide	Zoom 1	Zoom 2	Zoom 3		
Vertical Start Position	VPOS	NTSC	ODD	22	22	52	38	53	Line	Note 7-7
			EVEN	286	286	315	301	315		
		PAL	ODD	26	26	62	50	62	Line	Note 7-7
			EVEN	339	363	375	363	375		
Vertical Display Position	VDIS	NTSC		234	234	176	204	204	Line	
		PAL		281	281	210	234	234	Line	

Notes :7-7 Sampling start line number base on the vertical sync pulse (SYNC).



Note: 7-7 Values in brackets correspond to PAL mode ($f_H = 15.734(15.625)$ KHz).

7-7) Display Time Range

A) When sync. signal of NTSC system is applied.

a) Horizontally

11.35 ~ 61.36 μs.

b) Vertical

22 ~ 255 H

B) When sync. signal of PAL system is applied.

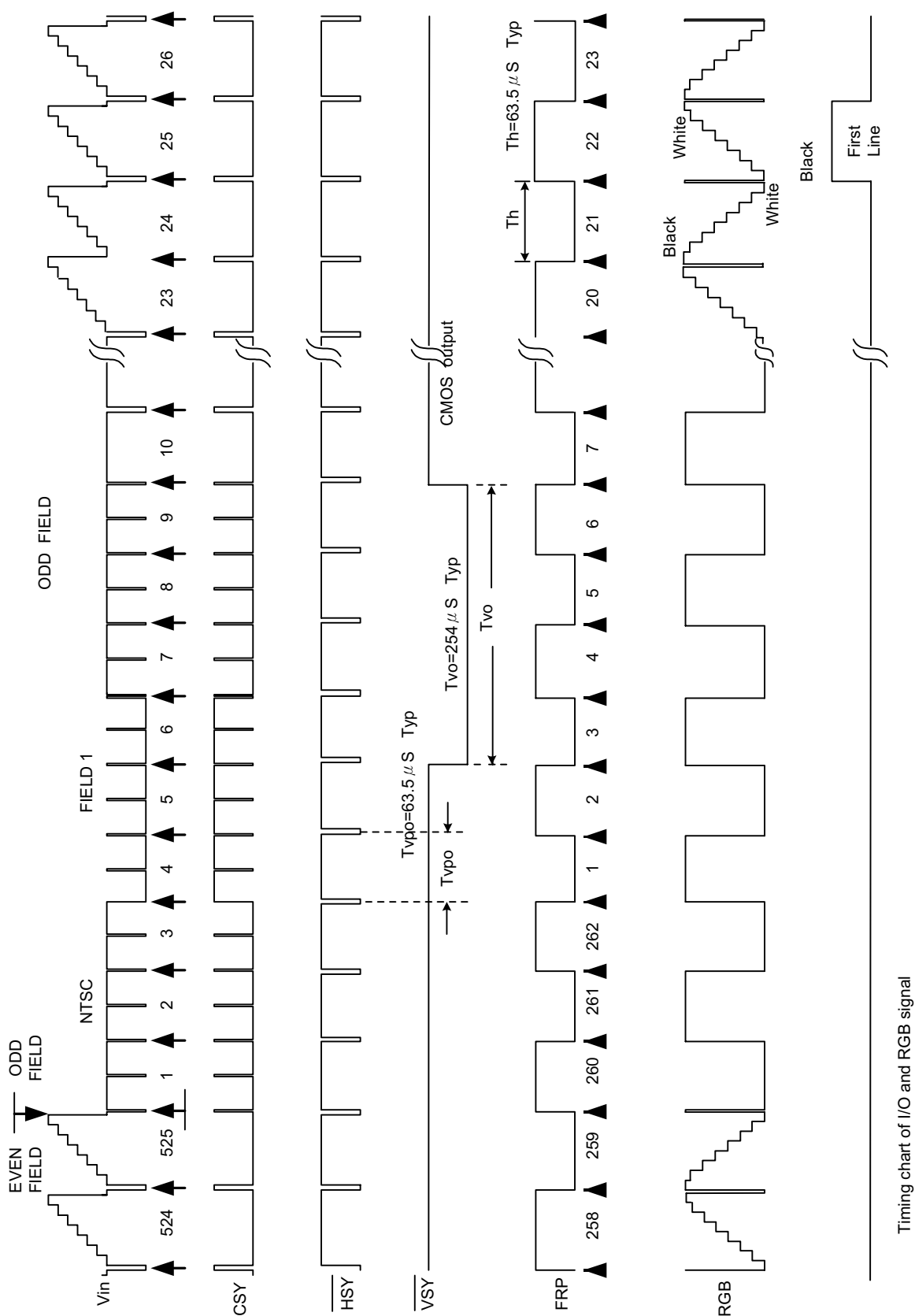
a) Horizontally

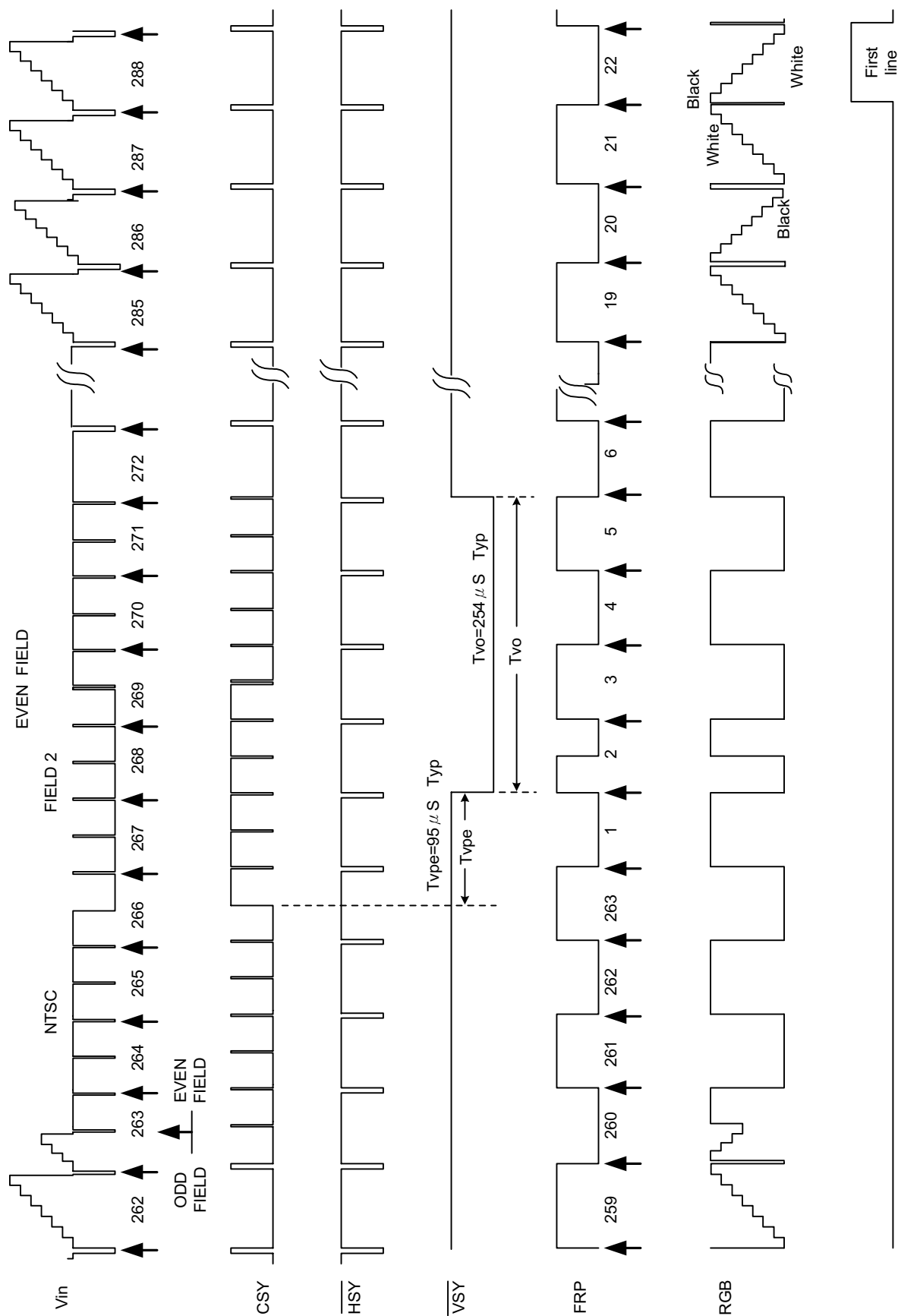
11.54 ~ 61.9 μs

b) Vertical

28 ~ 300 H

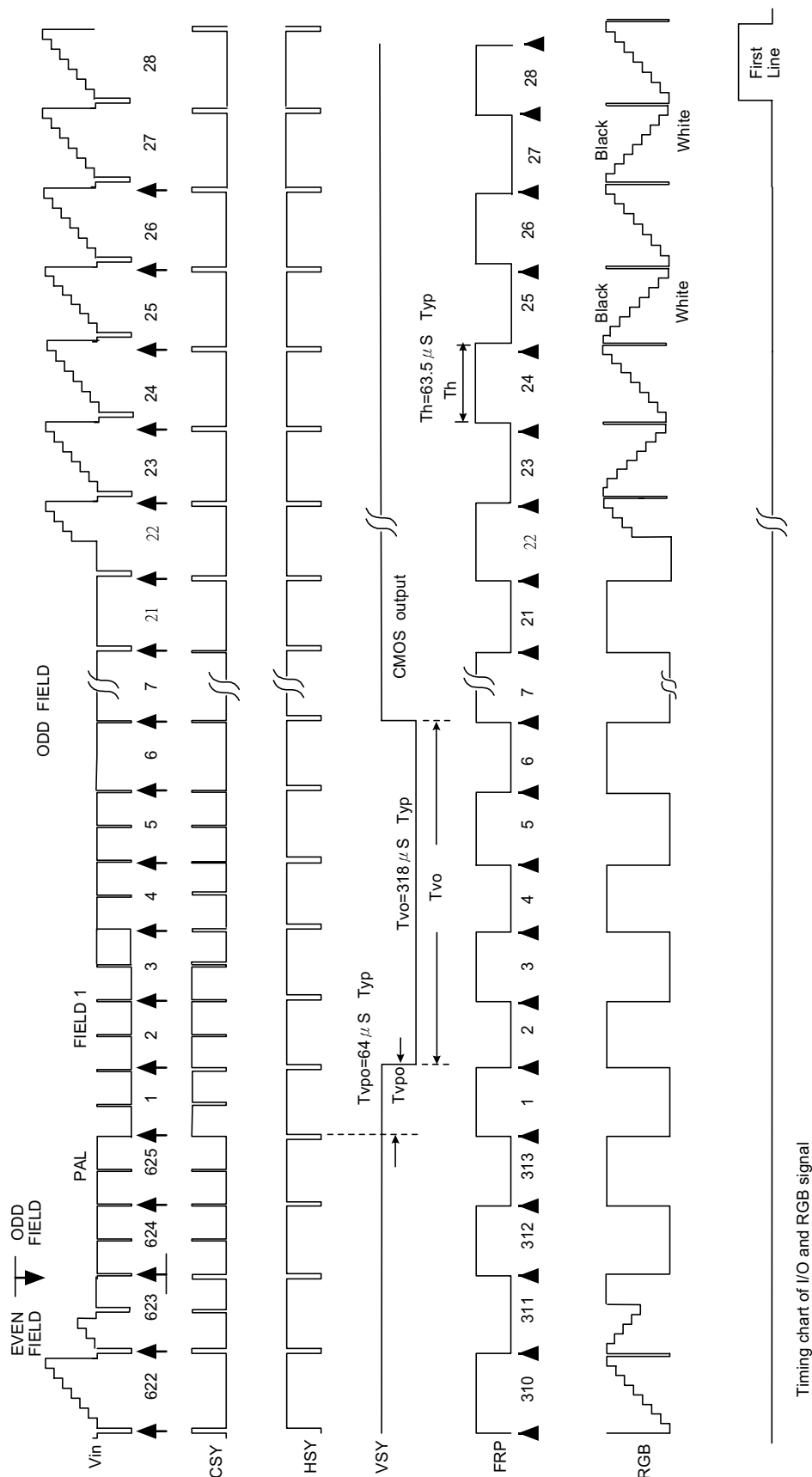
C) NTSC System Timing Reference



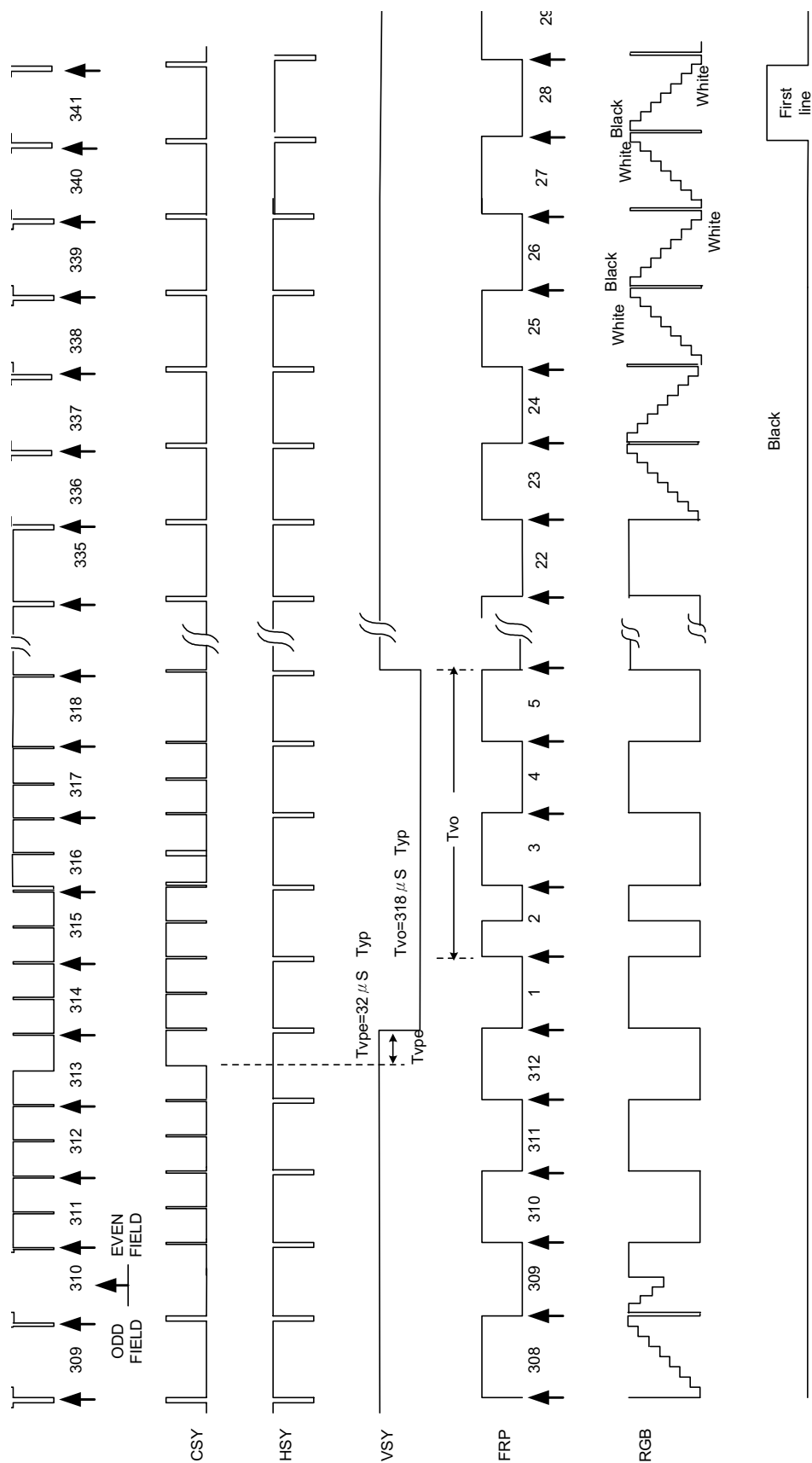


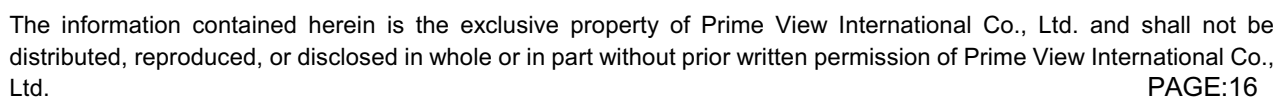
Timing chart of I/O and RGB signal

D) PAL System Timing Reference

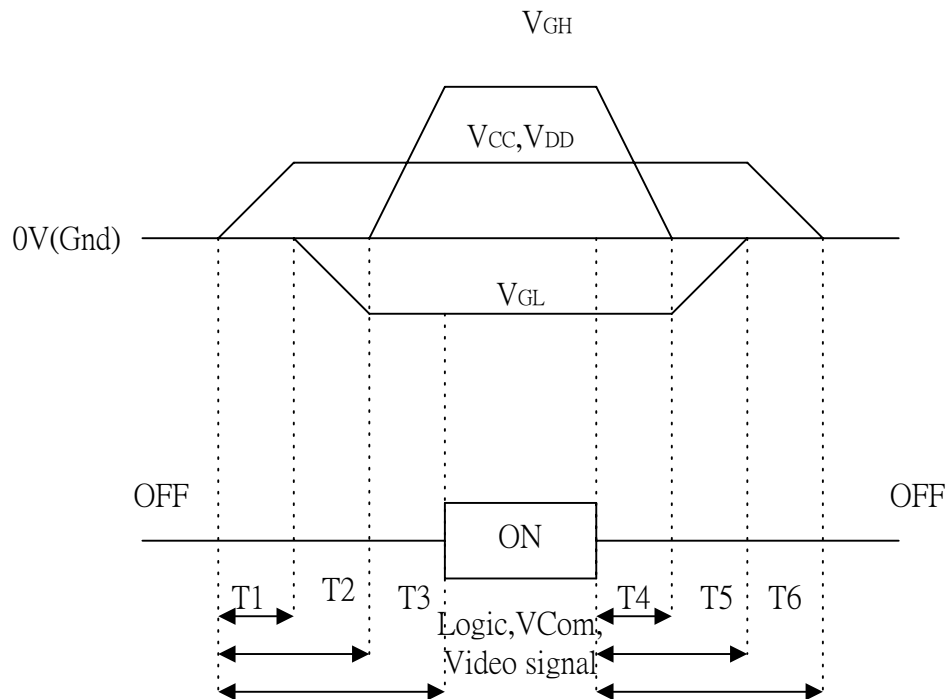


Timing chart of I/O and RGB signal





8.Power Sequence



- 1) $10\text{ms} \leq T1 \leq T2 \leq T3$
- 2) $10\text{ms} \leq T4 \leq T5 \leq T6$

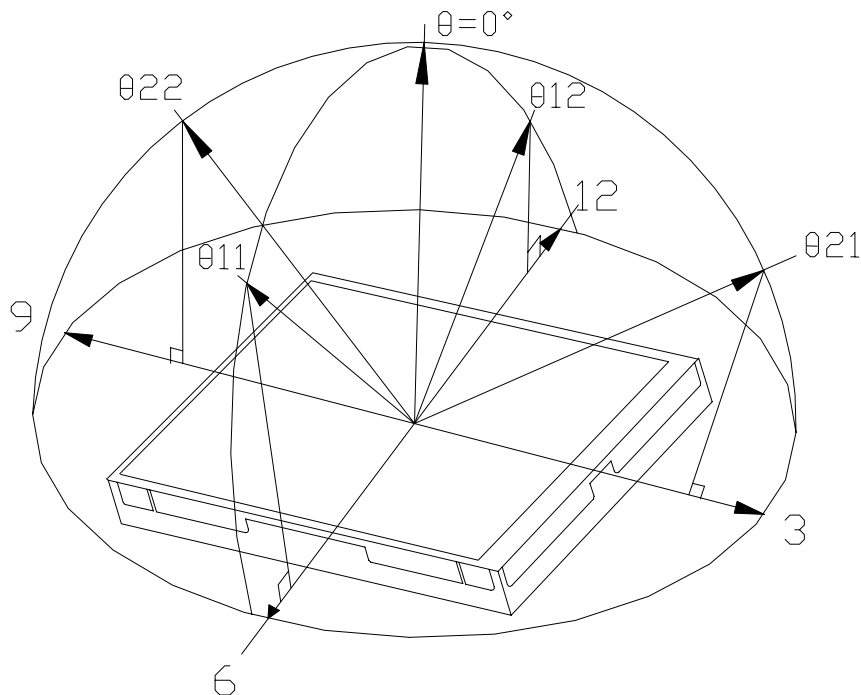
9. Optical Characteristics

9-1) Specification:

$T_a = 25^{\circ}\text{C}$

Parameter		Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remarks
Viewing Angle	Horizontal	θ_{21}, θ_{22}	$CR \geq 10$	45	55		deg	Note 9-1
	Vertical	θ_{12}		10	15		deg	Note 9-1
		θ_{11}		30	35		deg	Note 9-1
Contrast Ratio		CR		70	150			Note 9-2
Response time	Rise	Tr	$\theta = 0^{\circ}$			30	ms	Note 9-4
	Fall	Tf				50	ms	
Brightness				350	400		cd/m ²	Note 9-3
White Chromaticity		x			0.285			Note 9-3
		y			0.295			
Lamp Life Time +25℃				10,000			hr	

Note 9-1: The definitions of viewing angles



Note 9-2 : $CR = \frac{\text{Luminance when Testing point is White}}{\text{Luminance when Testing point is Black}}$

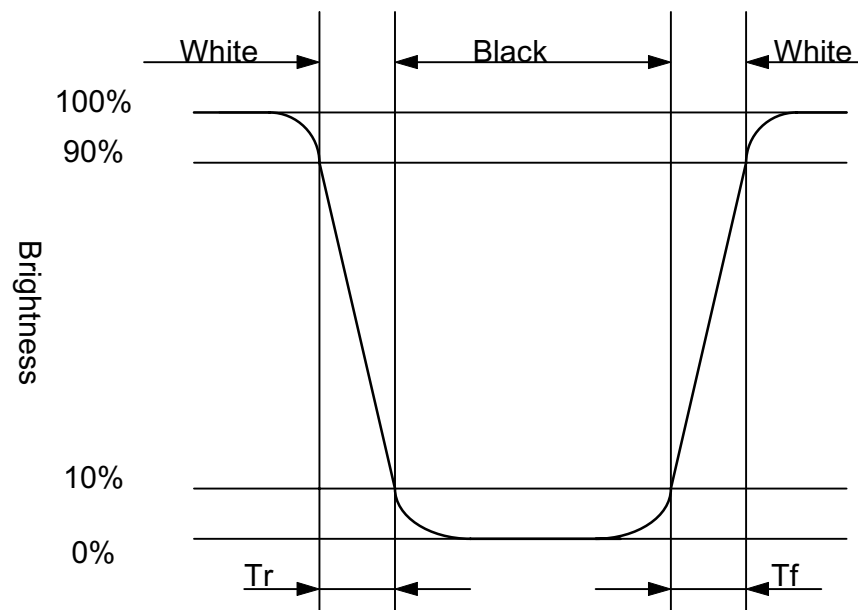
(Testing configuration see 8-2)

Contrast Ratio is measured in optimum common electrode voltage.

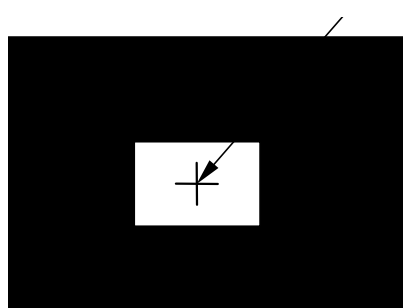
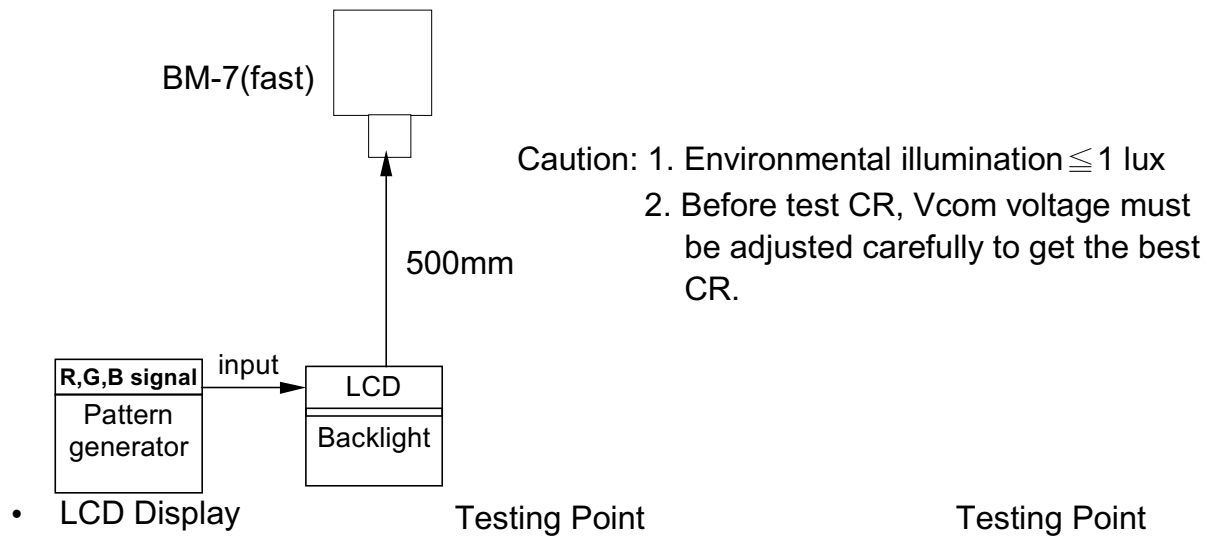
Note 9-3 : Topcon BM-7(fast) luminance meter 2° field of view is used in the testing (after 20~30 minutes operation).

Lamp Current 6mA

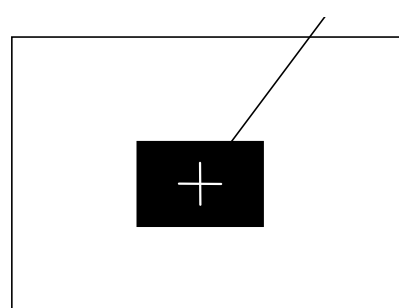
Note 9-4: The definition of response time:



9-2) Testing configuration

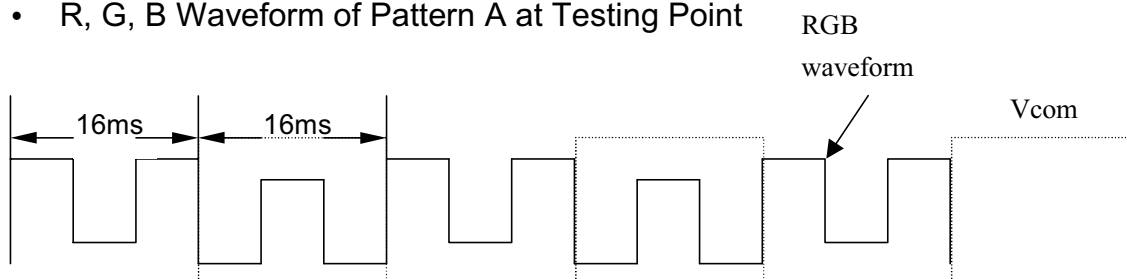


Pattern A

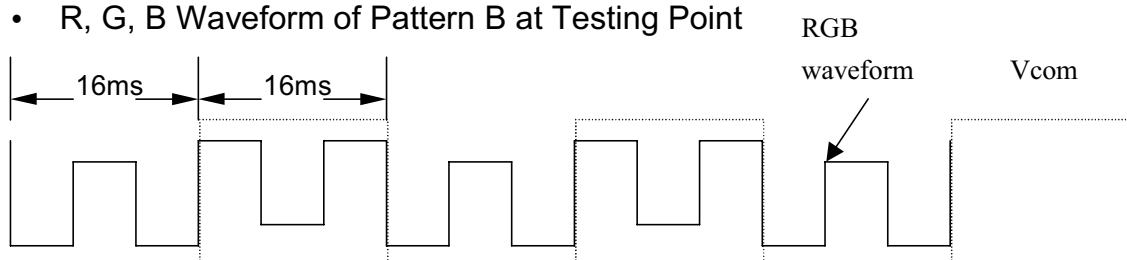


Pattern B

- R, G, B Waveform of Pattern A at Testing Point



- R, G, B Waveform of Pattern B at Testing Point



10. Reliability Test

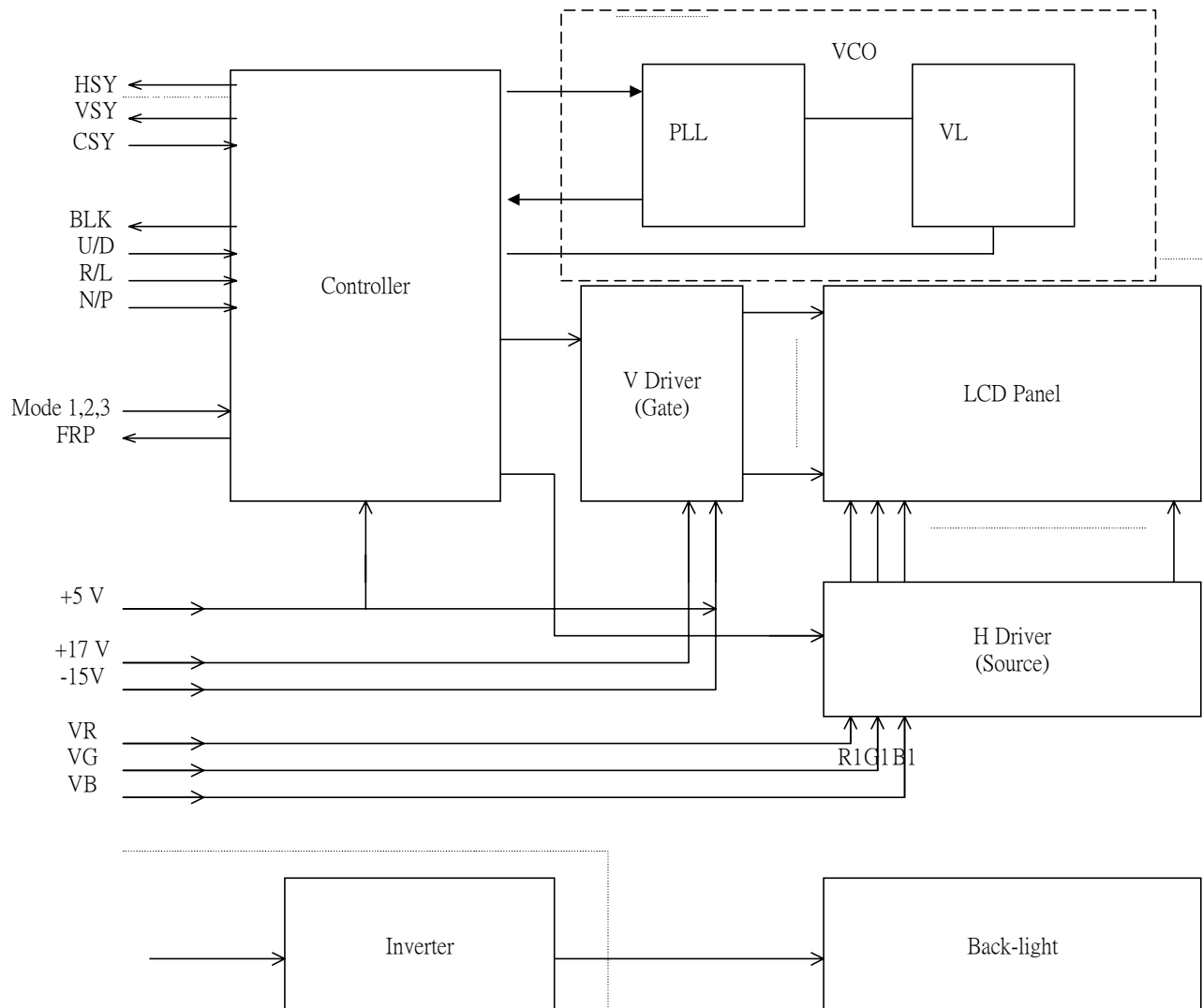
No	Test Item	Test Condition
1	High Temperature Storage Test	Ta = +80 °C, 240 hrs
2	Low Temperature Storage Test	Ta = -30°C, 240 hrs
3	High Temperature Operation Test	Ta = +70 °C, 240 hrs
4	Low Temperature Operation Test	Ta = -20 °C, 240 hrs
5	High Temperature & High Humidity Operation Test	Ta = +60°C, 95%RH, 240 hrs
6	Thermal Cycling Test (non-operating)	-25°C → +25°C → +70°C, 200 Cycles 30 min 5min 30 min
7	Vibration Test (non-operating)	Frequency : 10 ~ 55 Hz Amplitude : 1.5 mm Sweep time: 11 mins Test Period: 6 Cycles for each direction of X, Y, Z
8	Shock Test (non-operating)	100G, 6ms Direction: ± X, ± Y, ± Z Cycle: 3 times
9	Electrostatic Discharge Test (non-operating)	150pF, 330Ω Air: ±15KV; Contact: ±8KV 10 times/point, 9 points/panel face

Ta: ambient temperature

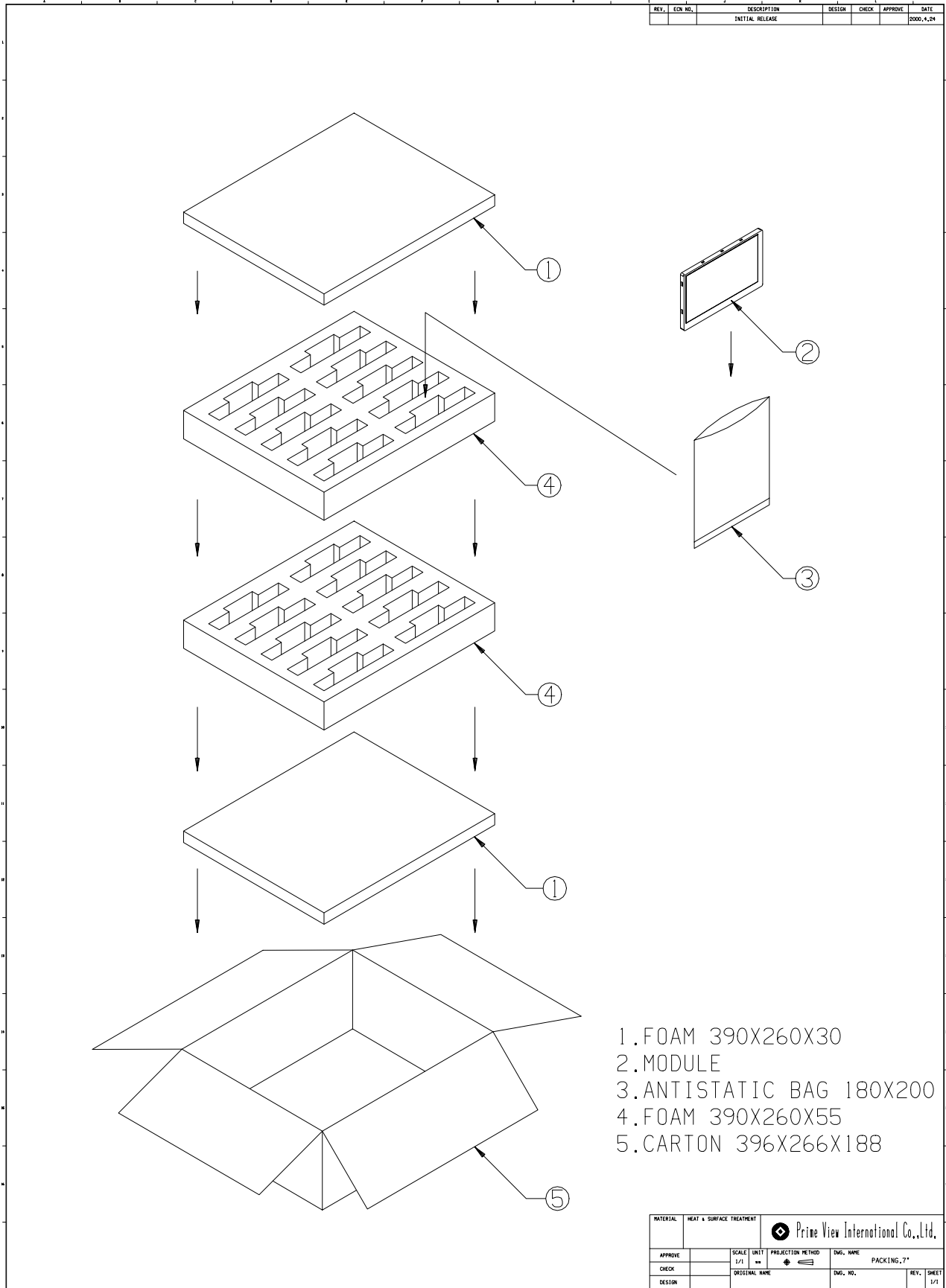
[Criteria]

Under the display quality test conditions with normal operation state, there should be no change which may affect practical display function.

11. Block Diagram



12. Packing



Revision History

Rev.	Issued Date	Revised Contents
0.1	SEP. 06,2000	NEW
0.2	FEB.02.2001	Modify 5-2 Display Mode (add 3 mode)