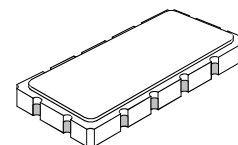




- **Designed for TDMA IS-54 / CDPD IF Applications**
- **Low Insertion Loss**
- **Excellent Selectivity**
- **Hermetic 13.3 X 6.5 mm Surface-Mount Case**
- **Unbalanced Input and Output**
- **Complies with Directive 2002/95/EC (RoHS)**

**PX1004****82.2 MHz
SAW Filter****SM13365-12****Absolute Maximum Ratings**

Rating	Value	Units
Maximum Incident Power in Passband	+10	dBm
Max. DC voltage between any 2 terminals	30	VDC
Storage Temperature Range	-40 to +85	°C
Suitable for lead-free soldering - Max Soldering Profile	260°C for 30 s	

Electrical Characteristics

Characteristic	Sym	Notes	Min	Typ	Max	Units
Nominal Center Frequency	f_c	1	82.200			MHz
Passband Insertion Loss at f_c	IL	1, 2		3	4.0	dB
3 dB Passband	BW ₃		±15	±25		kHz
Amplitude Ripple over f_c ±15 kHz					1.0	dB _{p-p}
Group Delay Variation over f_c ±10 kHz	GDV			2.5	6.0	µs _{p-p}
Third-Order Intermod. for -20 dBm tones at f_c ±60 & 120 kHz					-95	dBm
Rejection f_c ±60 kHz		1, 2, 3	10	16		dB
f_c -880 kHz to f_c -940 kHz			65	68		
Ultimate				65		
Operating Temperature Range	T _A	1	-20		+70	°C

Impedance Matching to 50 Ω unbalanced	External L-C
Case Style	SM13365-12 13.3 X 6.5 mm Nominal Footprint
Lid Symbolization (YY=year, WW=week) See note 4	RFM PX1004 YYWW

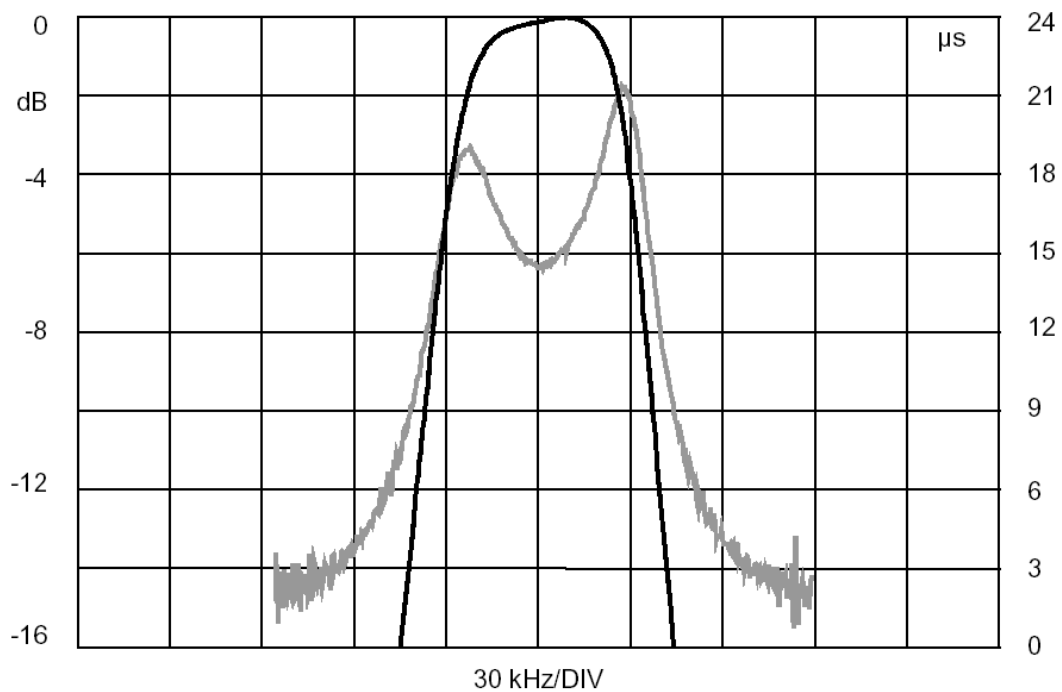
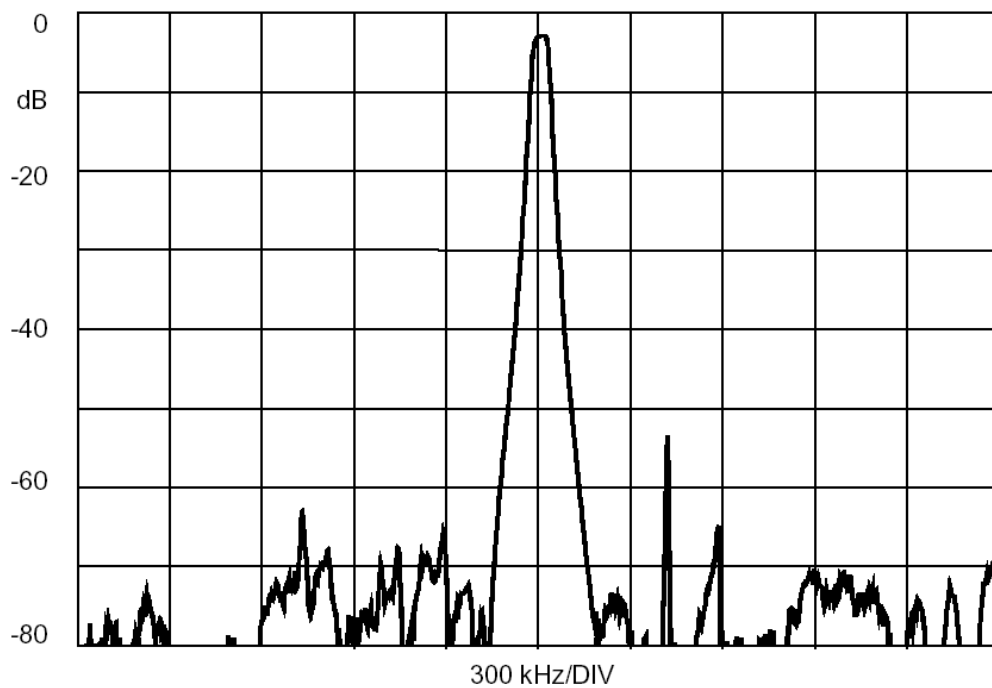
Electrical Connections

Connection	Terminals
Port 1 Hot	2
Port 1 Gnd Return	3
Port 2 Hot	8
Port 2 Gnd Return	9
Case Ground	All Others

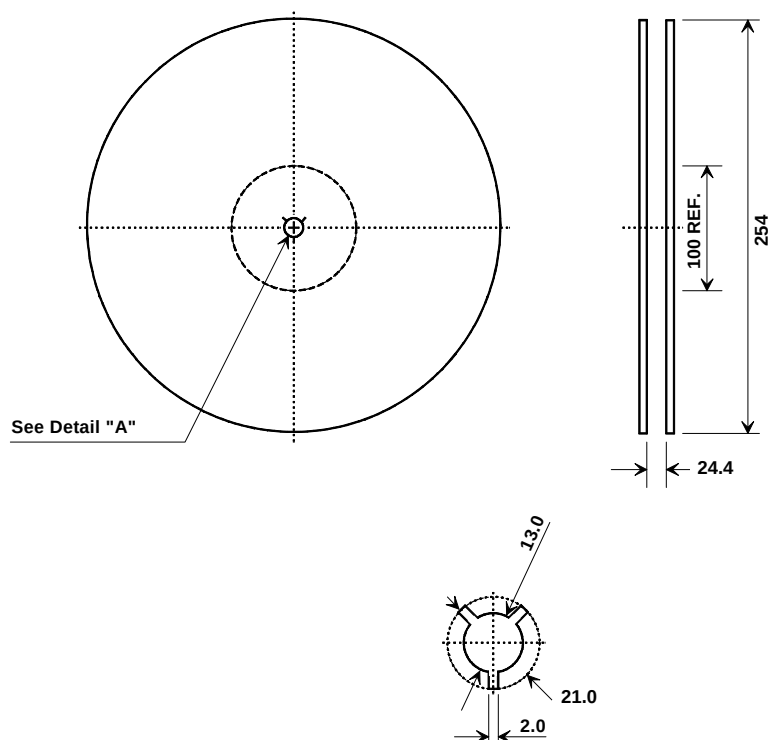
Notes:

1. Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to 50 Ω and measured with 50 Ω network analyzer.
2. Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, f_c .
3. Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details.
4. "LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."
5. The design, manufacturing process, and specifications of this filter are subject to change.
6. Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
7. US and international patents may apply.
8. RFM, stylized RFM logo, and RF Monolithics, Inc. are registered trademarks of RF Monolithics, Inc.
9. ©Copyright 1999, RF Monolithics Inc.
10. Electrostatic Sensitive Device. Observe precautions for handling.





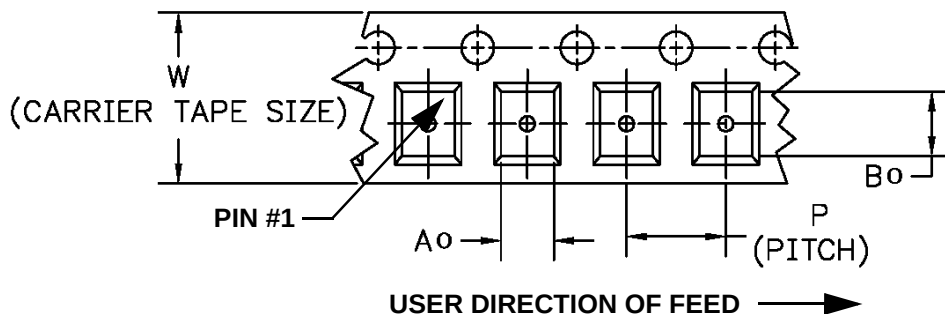
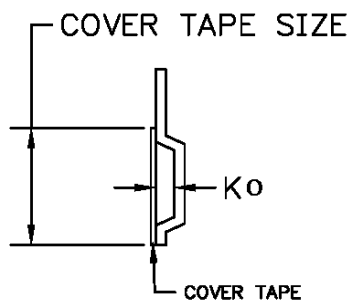
Tape and Reel Specifications



Quantity Per Reel
100 Min
1000 Max

COMPONENT ORIENTATION and DIMENSIONS

Carrier Tape Dimensions	
Ao	7.0 mm
Bo	13.8 mm
Ko	2.0 mm
Pitch	12.0 mm
W	24.0 mm

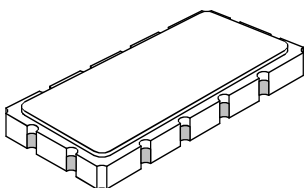


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SM13365-12 Case

12-Terminal Ceramic Surface-Mount Case

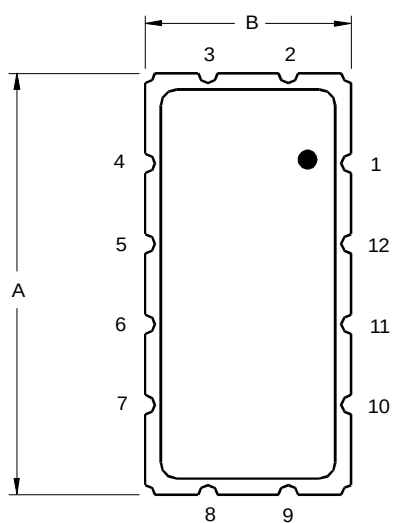
13.3 x 6.5 mm Nominal Footprint



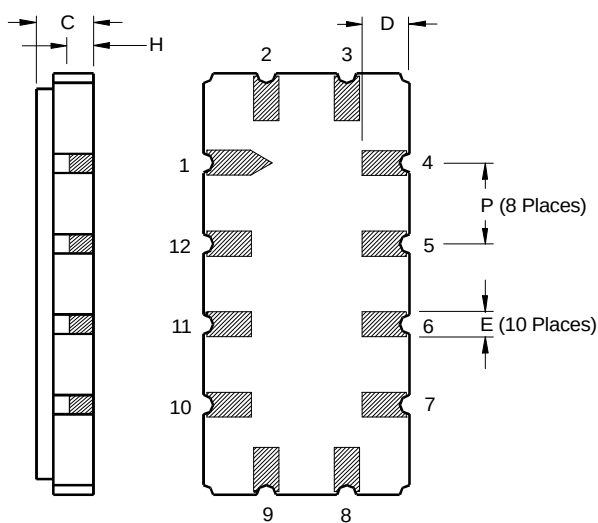
Case Dimensions						
Dimension	mm			Inches		
	Min	Nom	Max	Min	Nom	Max
A	13.08	13.31	13.60	0.515	0.524	0.535
B	6.27	6.50	6.80	0.247	0.256	0.268
C		1.91	2.00		0.075	0.079
D		1.50			0.059	
E		0.79			0.031	
H		1.0			0.039	
P		2.54			0.100	

Materials	
Solder Pad Termination	Au plating 30 - 60 ulnches (76.2-152 uM) over 80-200 ulnches (203-508 uM) Ni.
Lid	Fe-Ni-Co Alloy Electroless Nickel Plate (8-11% Phosphorus) 100-200 ulnches Thick
Body	Al ₂ O ₃ Ceramic
Pb Free	

Electrical Connections		
Connection		Terminals
Port 1	Input or Return	2
	Return or Input	3
Port 2	Output or Return	8
	Return or Output	9
Ground		All others
Single Ended Operation		Return is ground
Differential Operation		Return is hot



TOP VIEW



BOTTOM VIEW

NOTES:

REV	ECN NO.	DESCRIPTION	APP/DATE
B	3403	CHANGE PCB/VAR CAPS	VB
C	3465	REP 2pF CAPS W/TRIMMER	FR
D	4632	UPDATE	
E	10225	REVISED PIN NUMBERING	04oct01

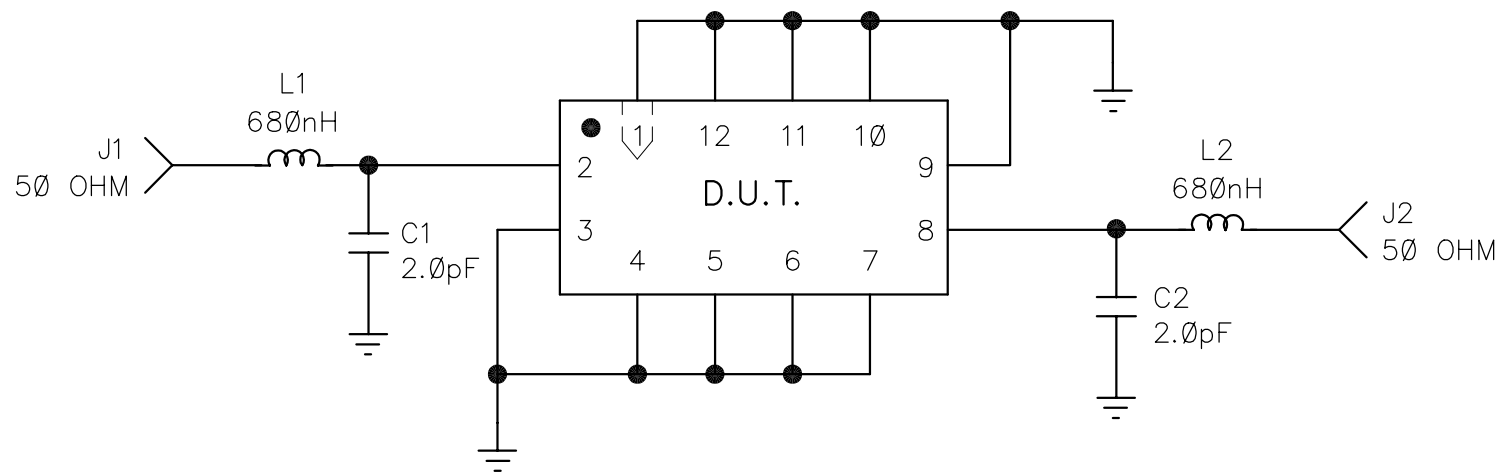
BILL OF MATERIALS

SEQ	QTY	RFM P/N	DESCRIPTION	REF DES	REFERENCE/COMMENTS
1	1	400-0735-001	PCB (REV X3)	PCB1	
2	2	500-0003-020	CAPACITOR, 2.0pF	C1,2	±.25pF
3	2	N/A	CHIP IND. 680nH	L1,2	± 10%
4	2	500-0248-001	CONN, COAX FLANGE MT. JACK	J1,2	
5	1	400-0533-001	SHIELD, BRASS	SHLD1	

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DRAWN BY/DATE: D. GAY 03/08/94			TITLE: DEMO PCB, PX1004				
RF Monolithics, Inc. DALLAS, TEXAS 75244	CHECKED/APPROVED	SIZE	CODE IDENT	DWG. NO.	PX1004(DEMO)	REV	SHEET
		A	2U874			E	1/6

SCHEMATIC, PX1004 (DEMO)



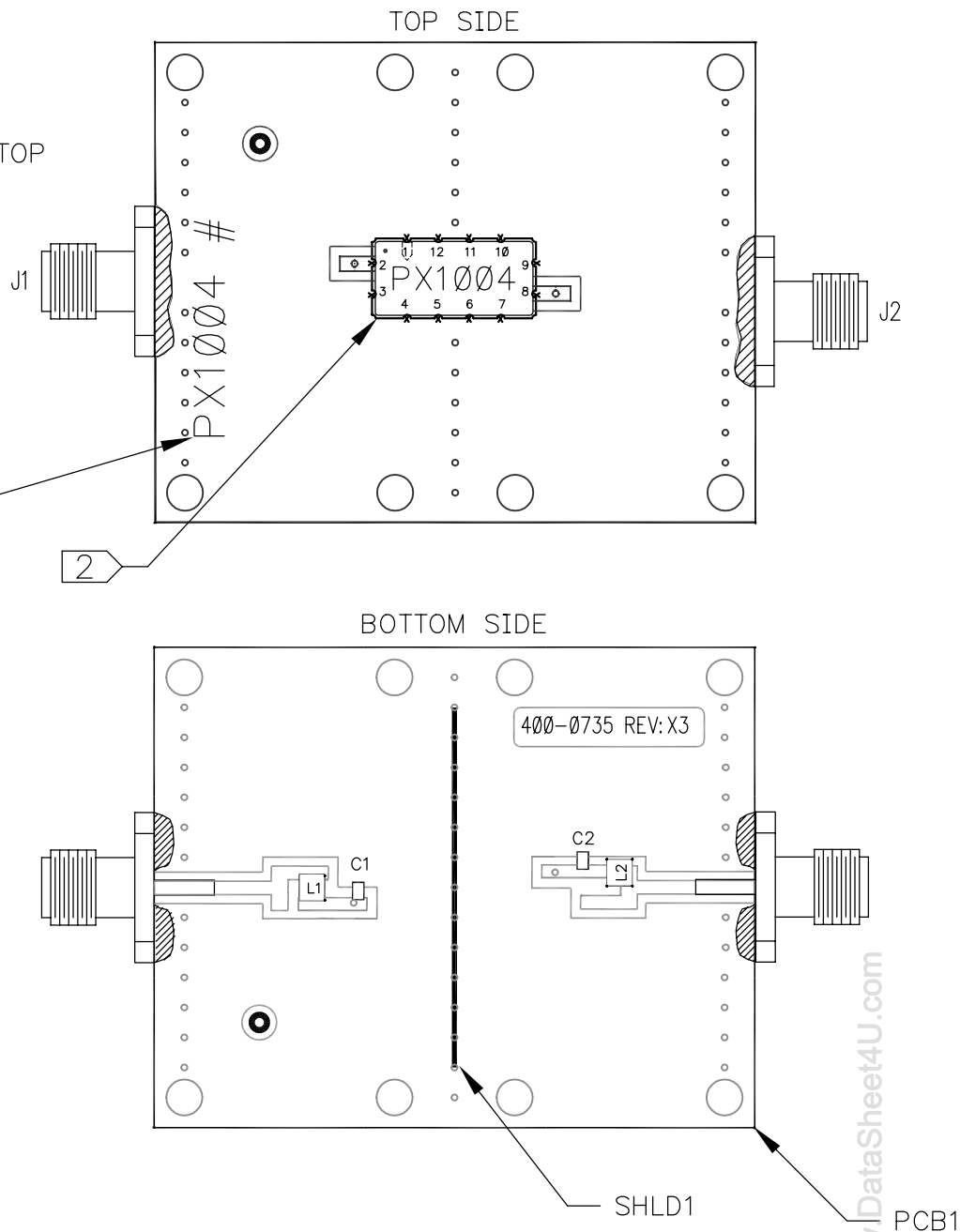
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NOTES:

1 NOTE PROPER ORIENTATION OF L1,2. THEY SHOULD BE POSITIONED AT 90° TO EACH OTHER.

2 SOLDER SURFACE MOUNT PACKAGE, PX1004, TO TOP SIDE OF PCB. SOLDER IN 12 PLACES MARKED "X" AS SHOWN.

MARK DEVICE TYPE WITH ELECTRONIC LABEL MACHINE.

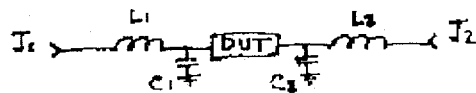


TUNING:

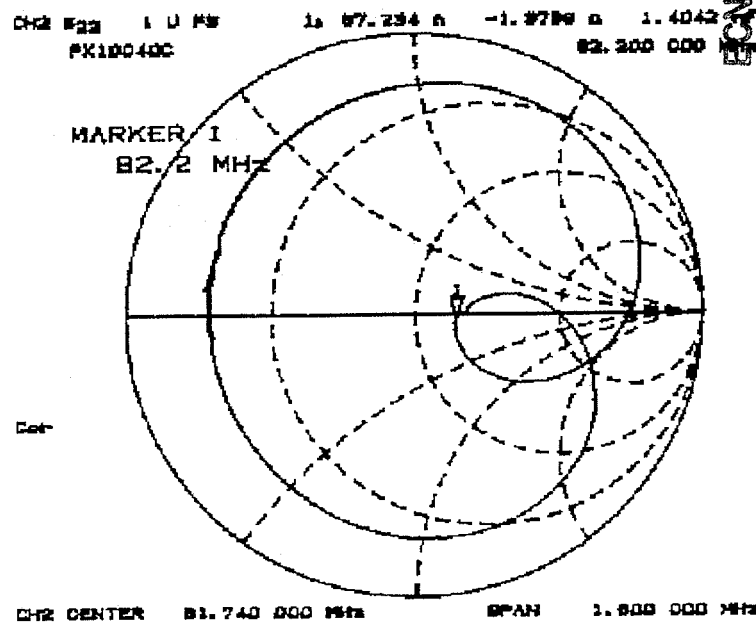
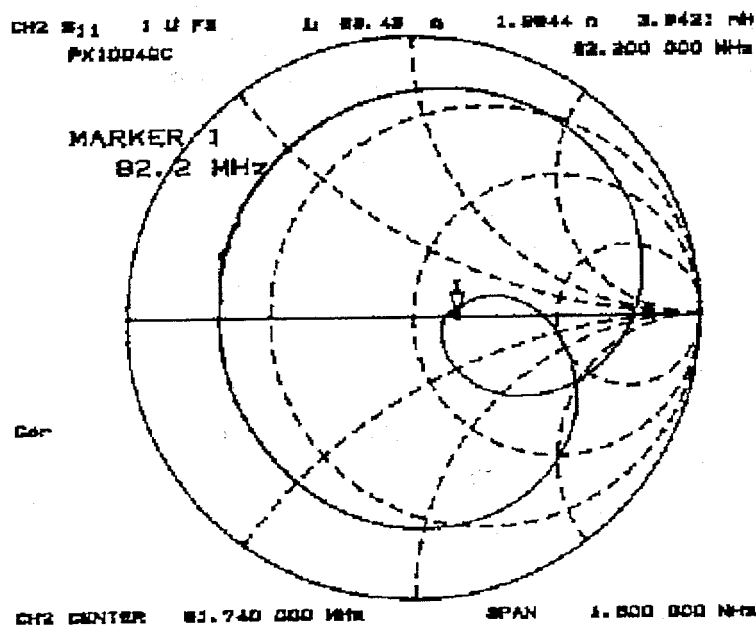
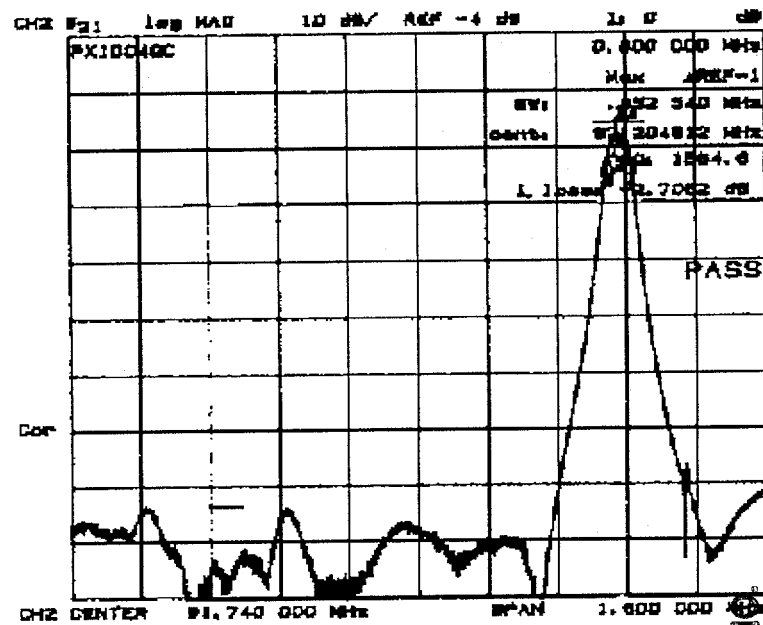
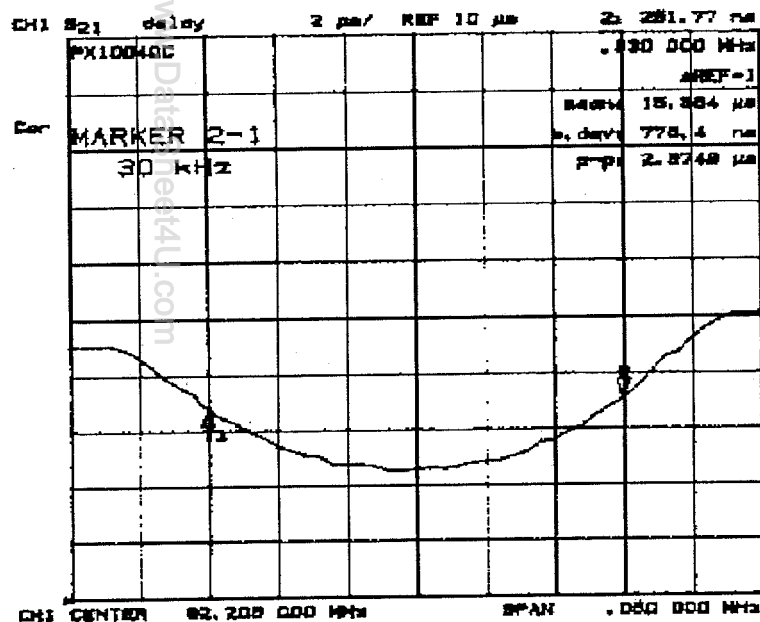
Plot A shows typical tuning respose S21 and smith chart. Plot B is to be delivered with each demo. The tuning component values may vary in order to achieve proper tuning due to component tolerances. Note component values and tolerances on each plot.

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RF Monolithics, Inc. DALLAS, TEXAS 75244		SIZE A	CODE IDENT 2U874	DWG. NO. PX1004(DEMO)	REV E	SHEET 4
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PX1004, Plot A Rev E



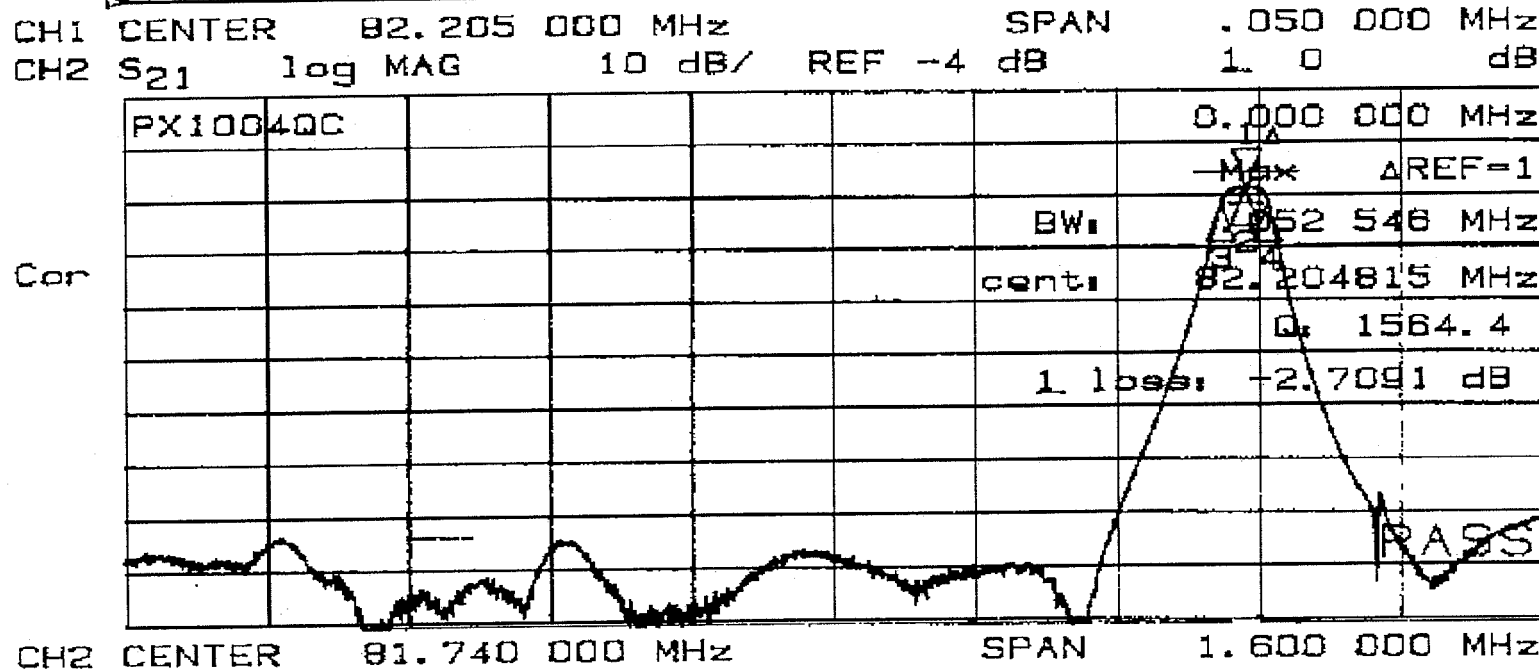
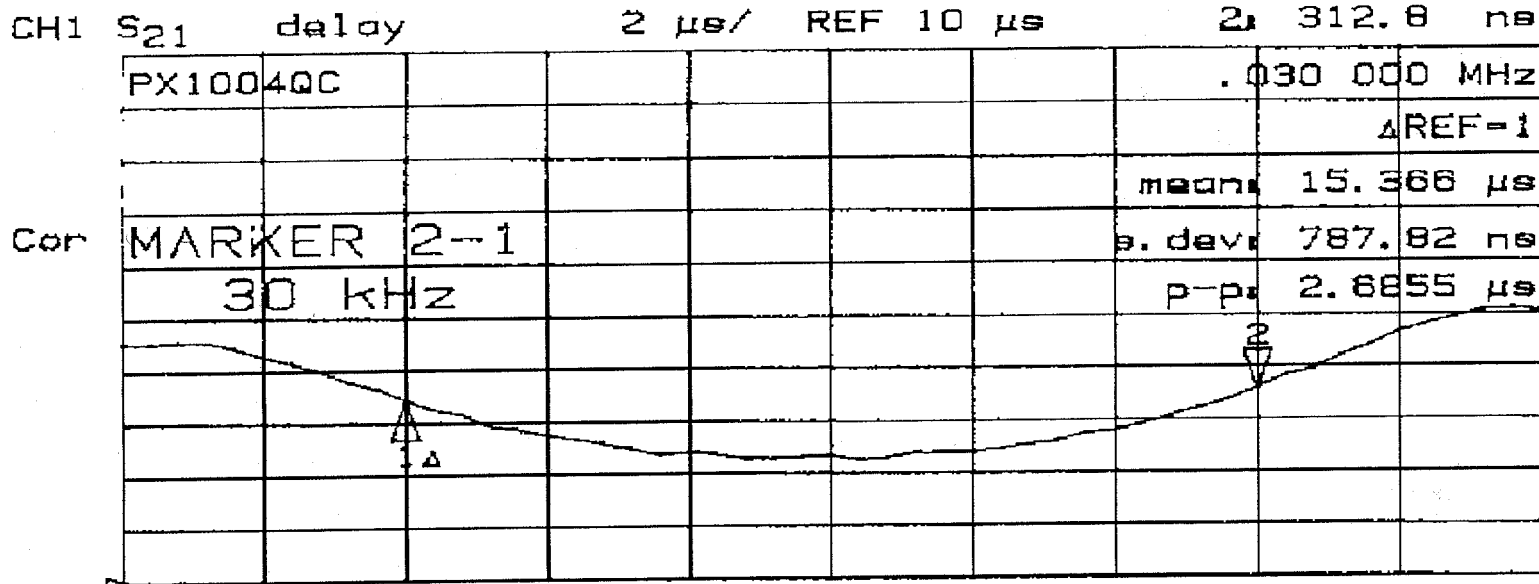
10225

SHEET

Sheet 5 of 6

PX1004, Plot B

Rev E



PX1004
Demo #
dev. #
d/c 5521F
204-56
ECNNO
S.

10.12.25
17

SHEET 15

L1, L2 680 nH $\pm$ 10%
C1, C2 20 pF $\pm$.25 pF

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Sheet 6 of 6