

**PXC2081**  
**480CH 8bit TFT LCD Source Driver**  
**with mini-LVDS interface**

**Product Description :**

PXC2081 is a 8 bits, 480CH TFT-LCD source driver with mini-LVDS interface. The device consists of mini-LVDS interface circuit, a gamma-corrected resistor chain, a 80 bit bi-directional shift register and 480 slices, each consisting of a double stage data latch, an 8 bit DAC and an analog output buffer. With the shift token outputs EIO1 and EIO2 the device may easily be cascaded for bigger displays. The maximum output dynamic range of 16.3V make it possible for this device to be applicable to TV application.

**Product Features :**

- Source driver LSI for TFT-LCD
- Dot by dot and n-line inversion available
- mini-LVDS interface(6 pairs serial data input and 1 pair serial clock)
- Serial clock frequency
  - Typical : 162MHz
  - Maximum : 172MHz
- 480 LCD driving outputs
- Internal 8bit DAC (16.77 million colors)
- 8 gamma correction reference voltages for each polarity
- Selectable data shift direction for flexible mounting
- Supports cascading of source drivers for large displays
- Logic supply voltage range : 2.3V ~ 3.6V
- Display supply voltage range : 10.0V ~ 16.5V
- Package : TCP/COF

**Applications :**

- This chip can be used SXGA, UXGA, HDTV resolution TFT-LCD panel for both monitor and TV Application owing to its wide operating voltage range

**Specifications :**

- |                         |                              |
|-------------------------|------------------------------|
| •Channel                | → 480 channel                |
| •Output Dynamic Rang    | → 10.0V ~ 16.5V              |
| •Serial clock Frequency | → Typ : 162Mhz, Max : 172Mhz |
| •Gray Scale             | → 8Bit : 256 Gray Scale      |
| •Inversion Type         | → Dot & n line inversion     |
| •Interface              | → Mini LVDS Interface        |
| •VDD for Logic Part     | → 2.3V ~ 3.6V                |
| •Package                | → TCP/COF                    |