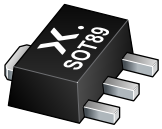


PXTA92

300 V, 100 mA PNP high-voltage transistor

8 October 2024 **Product data sheet**

1. General description

PNP high-voltage transistor in a medium power and flat lead SOT89 (SC-62) Surface-Mounted Device (SMD) plastic package.

NPN complement: PXTA42

2. Features and benefits

- High breakdown voltage
- Medium power and flat lead SMD plastic package

3. Applications

- Electronic ballast for fluorescent lighting
- LED driver for LED chain module
- High Intensity Discharge (HID) front lighting
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

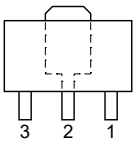
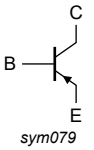
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-300	V
I_C	collector current			-	-	-100	mA
I_{CM}	peak collector current			-	-	-200	mA
h_{FE}	DC current gain	V_{CE} = -10 V; I_C = -10 mA; T_{amb} = 25 °C		40	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 SOT89	 sym079
2	C	collector		
3	B	base		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PXTA92	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PXTA92	%2D

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-300	V
V_{CEO}	collector-emitter voltage	open base		-	-300	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current			-	-100	mA
I_{CM}	peak collector current			-	-200	mA
I_{BM}	peak base current			-	-100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	1.3	W
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 6 cm^2.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	96	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm^2.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	V_{CB} = -200 V; I_E = 0 A; T_{amb} = 25 °C		-	-	-250	nA
I_{EBO}	emitter-base cut-off current	V_{EB} = -3 V; I_C = 0 A; T_{amb} = 25 °C		-	-	-100	nA
h_{FE}	DC current gain	V_{CE} = -10 V; I_C = -1 mA; T_{amb} = 25 °C		25	-	-	
		V_{CE} = -10 V; I_C = -10 mA; T_{amb} = 25 °C		40	-	-	
		V_{CE} = -10 V; I_C = -30 mA; T_{amb} = 25 °C		25	-	-	
V_{CEsat}	collector-emitter saturation voltage	I_C = -20 mA; I_B = -2 mA; T_{amb} = 25 °C		-	-	-500	mV
V_{BEsat}	base-emitter saturation voltage			-	-	-900	mV
C_c	collector capacitance	V_{CB} = -20 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	-	6	pF
f_T	transition frequency	V_{CE} = -20 V; I_C = -10 mA; f = 100 MHz; T_{amb} = 25 °C		50	-	-	MHz

11. Package outline

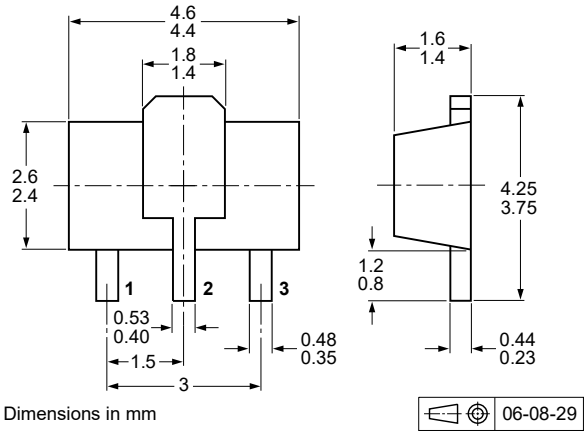


Fig. 1. Package outline SOT89

12. Soldering

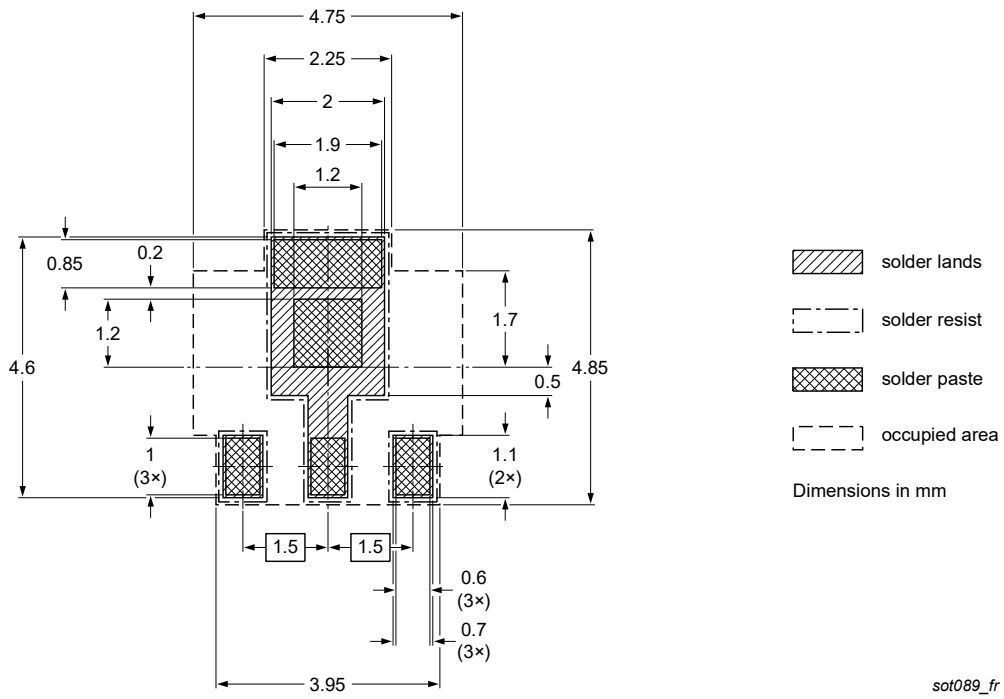


Fig. 2. Reflow soldering footprint for SOT89

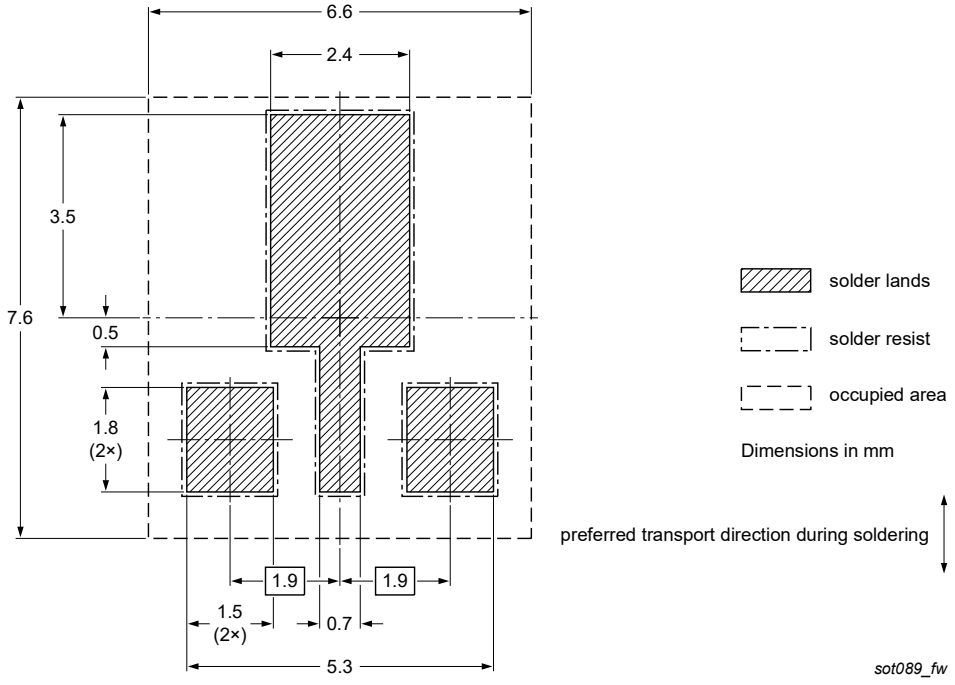


Fig. 3. Wave soldering footprint for SOT89

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PXTA92 v.7	20241008	Product data sheet	-	PXTA92 v.6
Modifications:	• Section "Packing information" removed. • Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
PXTA92 v.6	20110927	Product data sheet	-	PXTA92 v.5
PXTA92 v.5	20110711	Product data sheet	-	PXTA92 v.4
PXTA92 v.4	20041209	Product specification	-	PXTA92 v.3
PXTA92 v.3	19990429	Product specification	-	PXTA92_93_CNV v.2
PXTA92_93_CNV v.2	19970620	Product specification	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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Date of release: 8 October 2024