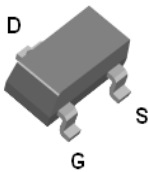


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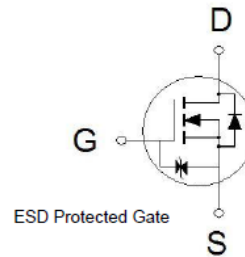
N-Channel Logic Level Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
20V	450m Ω @ $V_{GS} = 4.5V$	1A



SOT-23 (S)



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS	
Drain-Source Voltage	V_{DS}	20	V	
Gate-Source Voltage	V_{GS}	± 8		
Continuous Drain Current	I_D	$T_A = 25\text{ }^\circ\text{C}$	1	A
		$T_A = 70\text{ }^\circ\text{C}$	0.7	
Pulsed Drain Current ¹	I_{DM}	5		
Power Dissipation	P_D	$T_A = 25\text{ }^\circ\text{C}$	0.6	W
		$T_A = 70\text{ }^\circ\text{C}$	0.4	
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		183	$^\circ\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.

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N-Channel Logic Level Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	0.35	0.6	1	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±8V			±30	μA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 16V, V _{GS} = 0V			1	μA
		V _{DS} = 10V, V _{GS} = 0V, T _J = 55 °C			10	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 5V, V _{GS} = 4.5V	5			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 1.8V, I _D = 0.35A		384	850	mΩ
		V _{GS} = 2.5V, I _D = 0.5A		274	765	
		V _{GS} = 4.5V, I _D = 0.6A		213	450	
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 0.6A		2		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 4.5V, f = 1MHz		38		pF
Output Capacitance	C _{oss}			16		
Reverse Transfer Capacitance	C _{rss}			12		
Total Gate Charge ²	Q _g	V _{DS} = 0.5V _{(BR)DSS} , V _{GS} = 4.5V, I _D = 0.6A		1.4		nC
Gate-Source Charge ²	Q _{gs}			0.4		
Gate-Drain Charge ²	Q _{gd}			0.8		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 6V, I _D ≅ 0.6 A, R _{GS} = 6Ω, V _{GS} = 4.5V		6		ns
Rise Time ²	t _r			18		
Turn-Off Delay Time ²	t _{d(off)}			30		
Fall Time ²	t _f			25		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I _S				0.9	A
Forward Voltage ¹	V _{SD}	I _F = 0.15A, V _{GS} = 0V			1.2	V
Reverse Recovery Time	t _{rr}	V _{DS} = 12V,		233		nS
Reverse Recovery Charge	Q _{rr}	I _F = 2A, dI _F /dt = 100 A/μs		630		nC

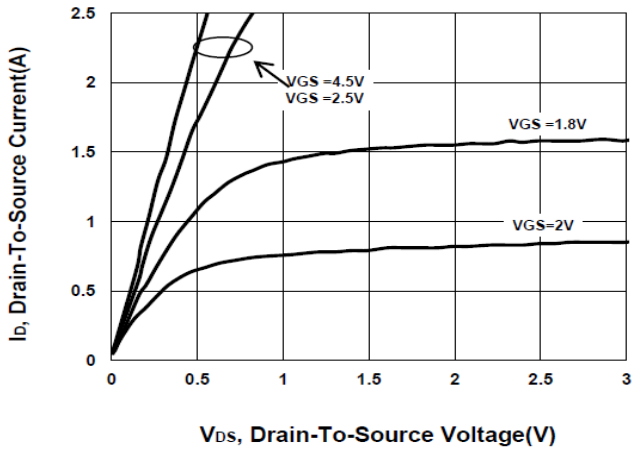
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

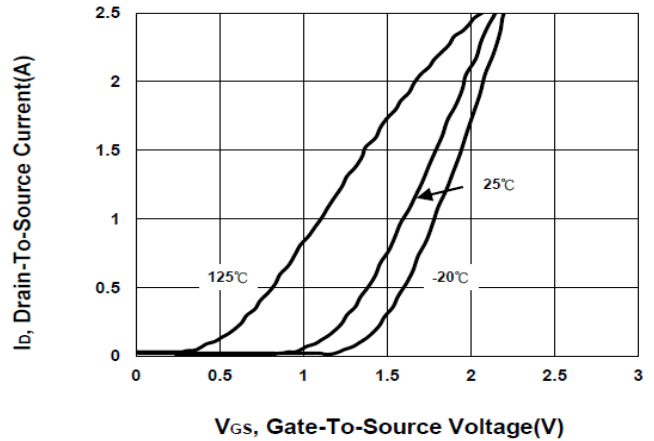
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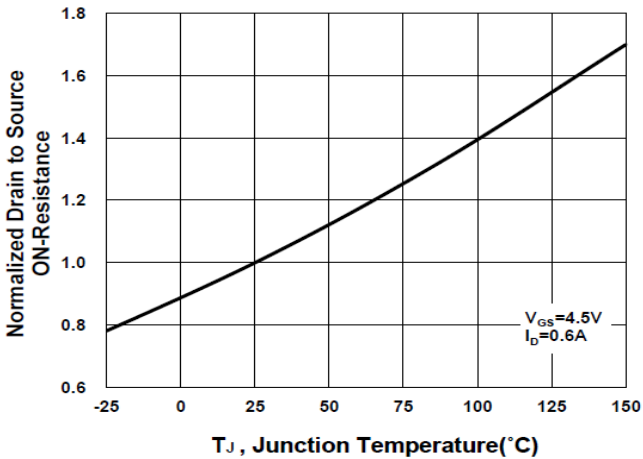
Output Characteristics



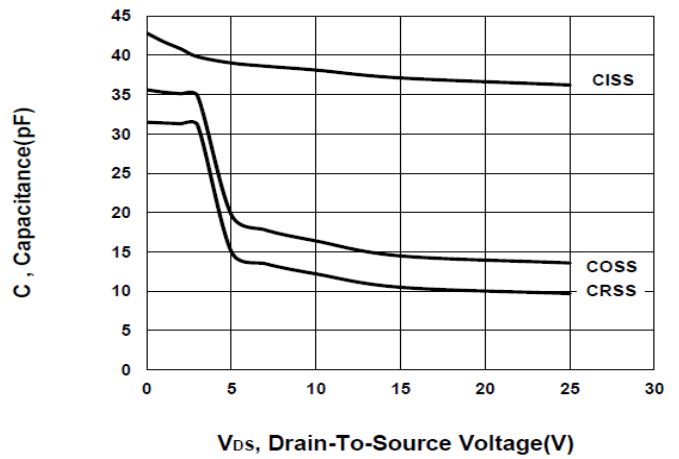
Transfer Characteristics



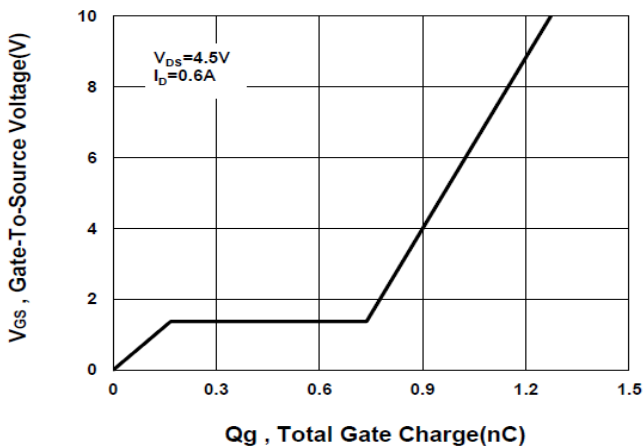
On-Resistance VS Temperature



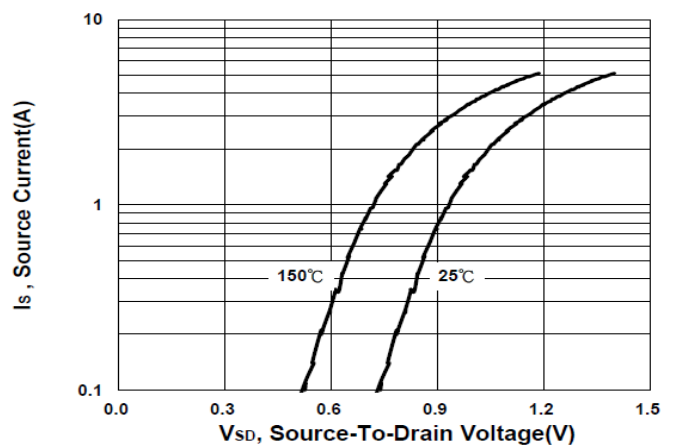
Capacitance Characteristic



Gate charge Characteristics

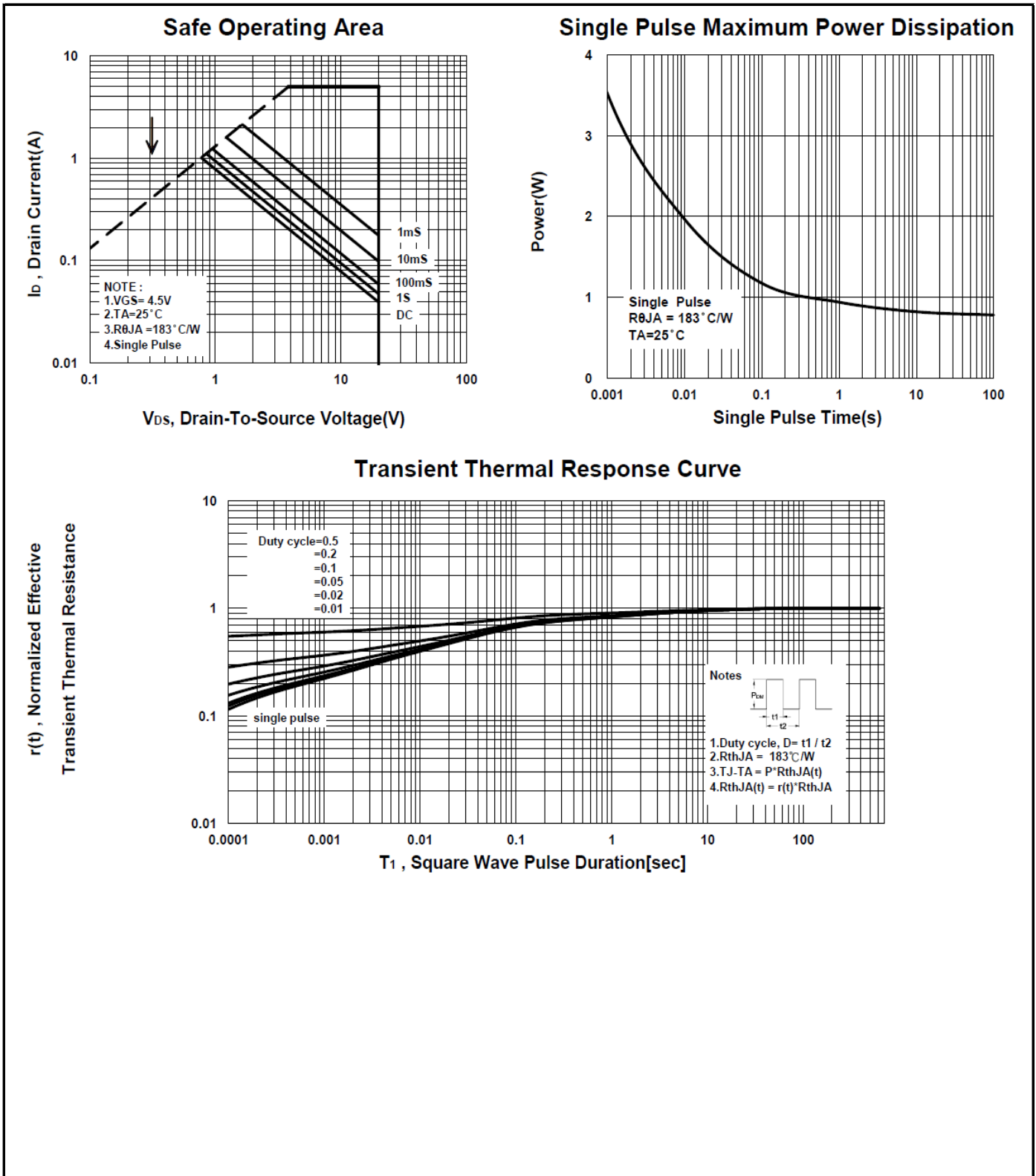


Source-Drain Diode Forward Voltage



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Package Dimension

SOT-23 (S) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.9		1	H	0.08		0.2
B	2.25		2.85	I	0.15		0.6
C	1.2		1.4				
D	2.8		3.04				
E	0.89		1.2				
F	0		0.1				
G	0.3		0.5				

