NIKO-SEM

N-Channel Logic Level Enhancement Mode Field Effect Transistor (Preliminary)

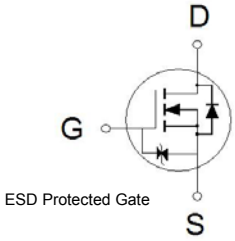
PZD502CY

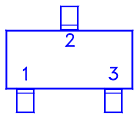
SOT-323

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
20V	450mΩ	0.75A





RoHS Compliant

1. GATE
2. DRAIN
3. SOURCE

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	±8	V
Continuous Drain Current	T_A = 25 °C	I_D	0.75	A
	T_A = 70 °C		0.6	
Pulsed Drain Current[1]		I_{DM}	2	
Power Dissipation	T_A = 25 °C	P_D	0.49	W
	T_A = 70 °C		0.3	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient[2]	$R_{\theta JA}$		255	°C/W

[1]Pulse width limited by maximum junction temperature.
[2]The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The value in any given application depends on the user's specific board design.

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS MIN	LIMITS TYP	LIMITS MAX	UNIT
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	20			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	0.35	0.65	1	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±8V			±30	μA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 16V, V_{GS} = 0V			1	μA
		V_{DS} = 10V, V_{GS} = 0V, T_J = 55 °C			10	
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 1.8V, I_D = 0.35A		464	850	mΩ
		V_{GS} = 2.5V, I_D = 0.5A		307	765	
		V_{GS} = 4.5V, I_D = 0.6A		240	450	
Forward Transconductance[1]	g_{fs}	V_{DS} = 5V, I_D =0.6A		2		S

DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 4.5V, f = 1MHz		38		pF
Output Capacitance	C_{oss}			16		
Reverse Transfer Capacitance	C_{rss}			12		
Total Gate Charge[2]	Q_g	V_{DS} = 10V, V_{GS} = 4.5V, I_D = 0.6A		1.4		nC
Gate-Source Charge[2]	Q_{gs}			0.4		
Gate-Drain Charge[2]	Q_{gd}			0.8		
Turn-On Delay Time[2]	$t_{d(on)}$	V_{DS} =6V, I_D ≅0.6 A, V_{GS} = 4.5V, R_{GS} = 6Ω		6		nS
Rise Time[2]	t_r			18		
Turn-Off Delay Time[2]	$t_{d(off)}$			30		
Fall Time[2]	t_f			25		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I_S				0.75	A
Forward Voltage[1]	V_{SD}	I_F = 0.15A, V_{GS} = 0V			1.2	V
Reverse Recovery Time	t_{rr}	I_F = 2A,dI_F/dt = 100 A/μs		233		nS
Reverse Recovery Charge	Q_{rr}			630		nC

[1]Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.
[2]Independent of operating temperature.