



Quanta Display Inc.

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	Doc. REV.: 01
	Issue Date:05/12/2005
	RoHS compliant

Specification for TFT LCD Module

Model No.
QD15XL13 Rev.:01

□ **Approved By**

Quanta Display Inc.



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Revision History			
REV.	Date	ECN NO.	Change Content
00	04/22/2005	N/A	Specification Initiate
01	05/12/2005	N/A	P.21 drawing revise

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1. Application

This specification applies to a color TFT-LCD module, QD15XL13.

2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a 1024×3×768 dots panel with 16.2M colors by using LVDS (Low Voltage Differential Signaling) to interface and supplying +3.3V DC supply voltage for TFT-LCD panel driving and supply voltage for backlight.

The TFT-LCD panel used for this module has very high aperture ratio. A low-reflection and higher-color-saturation type color filter is also used for this panel. Therefore, high-brightness and high-contrast image, which is suitable for the multimedia use, can be obtained by using this module.

Optimum viewing direction is 6 o'clock.

[Features]

- 1) High aperture panel; high-brightness or low power consumption.
- 2) Brilliant and high contrast image.
- 3) Small footprint and thin shape.
- 4) Light weight.
- 5) Interface 6bit + FRC
- 6) RoHS compliant

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	15" Diagonal	inch
Active area	304.1X228.1	mm
Pixel format	1024 (H)×768 (V)	Pixel
	(1 pixel = R+G+B dots)	
Pixel pitch	0.297 (H) × 0.297 (V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions (typ.)*1	326.5(W)×253.5 (H)×11(D)	mm
Mass	Max. 950	g
Surface treatment	Anti-glare and hard-coating 3H	

*1.Note : excluding backlight cables. Outline dimensions is shown in this specification



4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (1 channel, LVDS signals – NSC/Ti standard and +3.3V DC power supply)

Using connector: DF14H-20P-1.25H (Hirose)

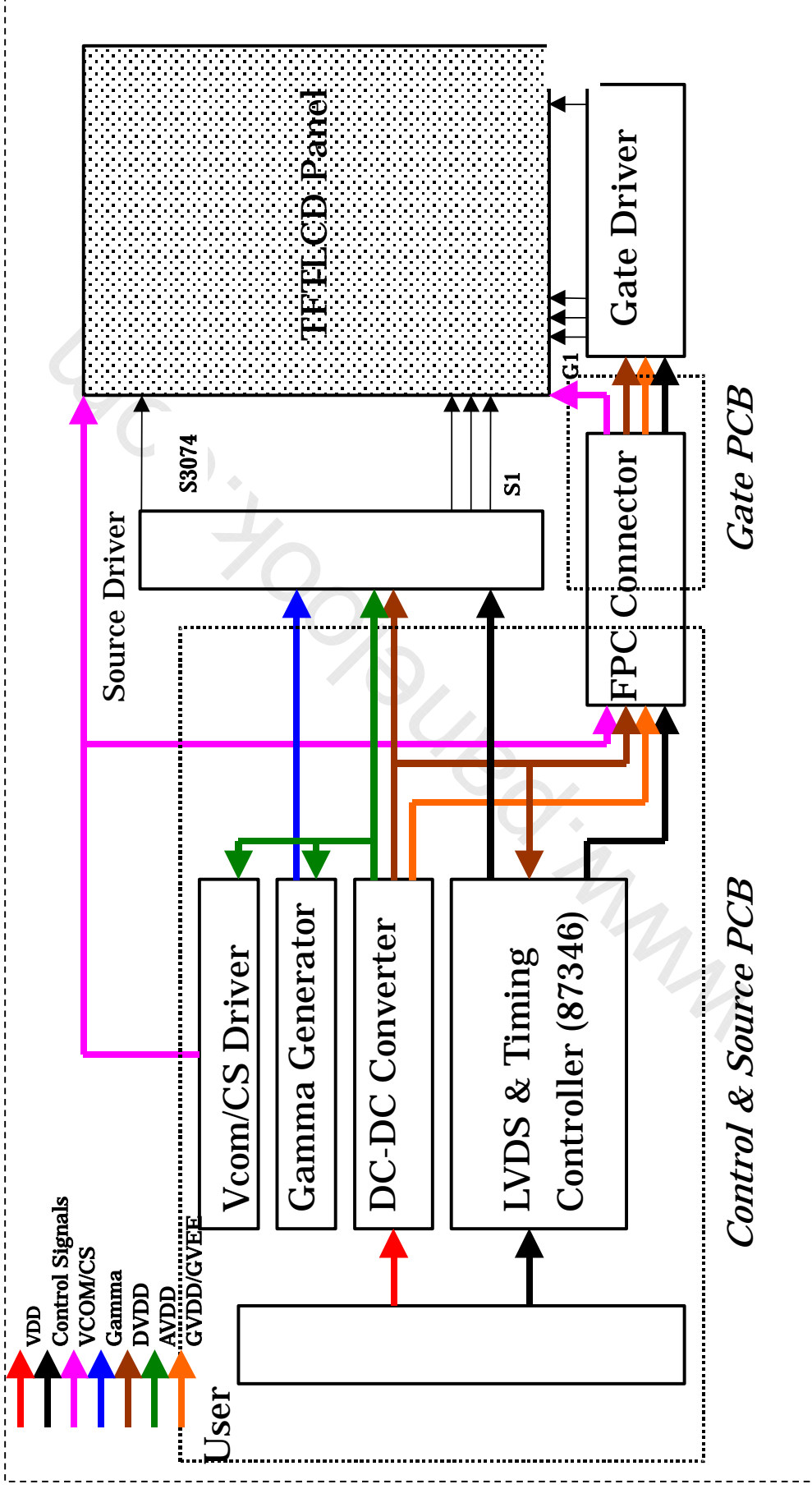
Interface Cable Pin Assignments

PIN NO	. SYMBOL	FUNCTION
1	VDD	Power Supply, 3.3 V (typical)
2	VDD	Power Supply, 3.3 V (typical)
3	GND	Ground
4	GND	Ground
5	RxIN0-	LVDS Receiver IN0- Signal
6	RxIN0+	LVDS Receiver IN0+ Signal
7	GND	Ground
8	RxIN1-	LVDS Receiver IN1- Signal
9	RxIN1+	LVDS Receiver IN1+ Signal
10	GND	Ground
11	RxIN2-	LVDS Receiver IN2- Signal
12	RxIN2+	LVDS Receiver IN2+ Signal
13	GND	Ground
14	RxCLKIN-	LVDS CLOCK - Signal
15	RxCLKIN+	LVDS CLOCK + Signal
16	GND	Ground
17	RxIN3-	LVDS Receiver IN3- Signal
18	RxIN3+	LVDS Receiver IN3+ Signal
19	GND	Ground
20	GND	Ground

【Note 1】 Relation between LVDS signals and actual data shows below section (4-2).

【Note 2】 The shielding case is connected with signal GND.

4-2 Interface block diagram





4-3. Backlight driving

CN2 (connector): BHR-03VS-1(JST)

Mating connector:SM02(8.0) B-BHS-1(JST)

Pin No.	Symbol	Function
1	V _{HIGH}	Power supply for lamp (High voltage side)
2		NC
3	V _{LOW}	Power supply for lamp (Low voltage side)

5. Absolute Maximum Ratings

5-1 LCD module

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	V _I	T _a =25℃	-0.3 ~ V _{CC} +0.3	V	【Note1】
+3.3V supply voltage	V _{CC}	T _a =25℃	0 ~ +4	V	
Storage temperature	T _{stg}	—	-25 ~ +60	℃	【Note2】
Operating temperature (Ambient)	T _{opa}	—	0 ~ +50	℃	

【Note1】 LVDS signals

【Note2】 Humidity : 95%RH Max. at T_a≤40℃.

Maximum wet-bulb temperature at 39℃ or less at T_a>40℃.

No condensation.



6. Electrical Characteristics

6-1.TFT-LCD panel driving

Ta=25°C

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
VDD	Supply voltage	VDD	+3.0	+3.3	+3.6	V	【Note2】
	Current dissipation	IDD	—	420	800	m A	【Note3】
Permissive input ripple voltage		V _{RP}	—	—	100	mV p-p	VDD=+3.3V
Differential input Threshold voltage	High	V _{TH}	—	—	+100	mV	V _{CM} =+1.2V 【Note1】
	Low	V _{TL}	-100	—	—	mV	
Terminal resistor		R _T	—	100	—	Ω	Differential input
Rush current		I _{RUSH}			1.5	A	Rise time 470uS

【Note1】 V_{CM} : Common mode voltage of LVDS driver.

【Note2】

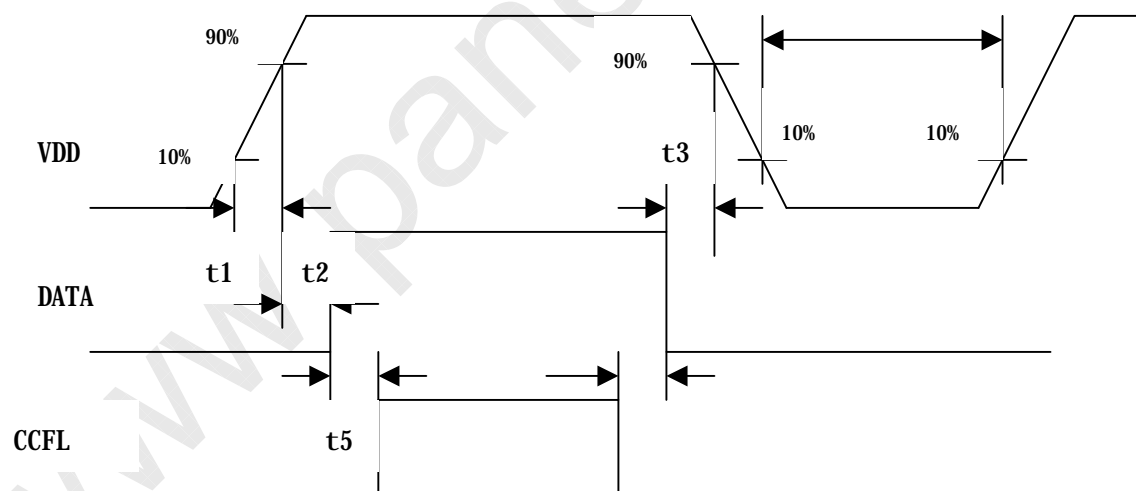
On-off conditions for supply voltage

$$0 < t_1 \leq 10 \text{ ms}$$

$$0 < t_2 \leq 50 \text{ ms}$$

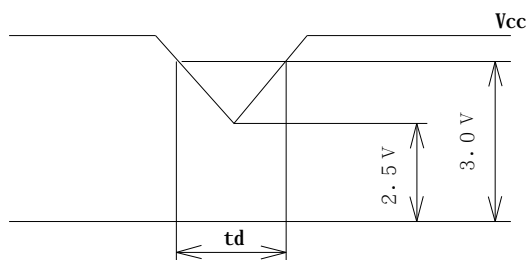
$$0 < t_3 \leq 50 \text{ ms}$$

$$500 \text{ ms} \leq t_4 ; 200 \text{ ms} \leq t_5$$



**VDD-dip conditions**

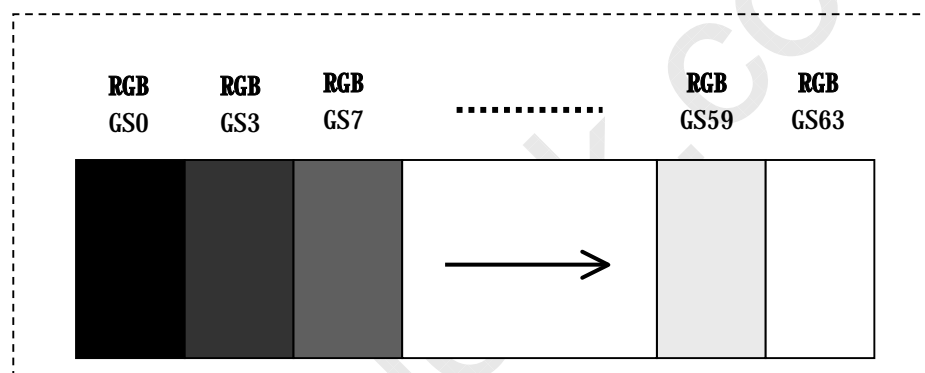
- 1) $2.5\text{ V} \leq \text{VDD} < 3.0\text{ V}$
 $t_d \leq 10\text{ ms}$
- 2) $\text{VDD} < 2.5\text{ V}$



VDD-dip conditions should also follow the On-off conditions for supply voltage

[Note3] Typical current situation : 16-gray-bar pattern.

VDD=+3.3V





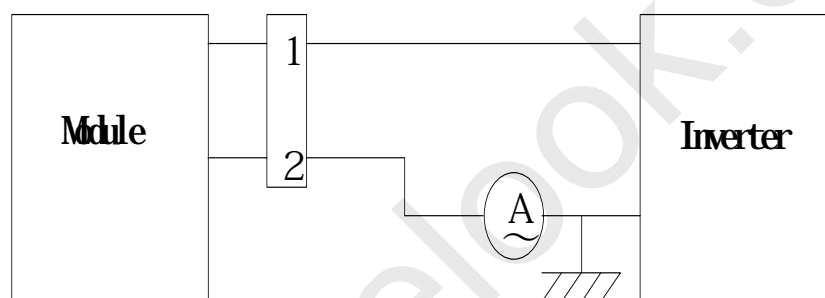
6-2. Backlight driving

The backlight system is an edge-lighting type with two CCFT (Cold Cathode Fluorescent Tube).

The characteristics of each lamp are shown in the following table.

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Lamp current range	I_L	7.5	8	8.5	mArms	【Note1】
Lamp voltage	V_L	526	585	644	Vrms	
Lamp power consumption	P_L	—	4.68	—	W	【Note2】
Lamp frequency	F_L	40		80	kHz	【Note3】
Kick-off voltage	V_s	—	—	1290	Vrms	$T_a=25^{\circ}\text{C}$
		—	—	1400	Vrms	$T_a=0^{\circ}\text{C}$
Lamp life time	L_L	30000	—	—	hour	【Note5】

【Note1】 Lamp current is measured with current meter for high frequency as shown below.



* 2 pin is V_{COM}

【Note2】 Calculated Value for reference ($I_L \times V_L$) $I_L=8\text{mA}$

【Note3】 Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display. Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.

【Note4】 The voltage above this value should be applied to the lamp for more than 1 second to start-up. Otherwise the lamp may not be turned on.

【Note5】 Lamp life time is defined as the time when either ① or ② occurs in the continuous operation under the condition of $T_a = 25^{\circ}\text{C}$ and $I_L = 8\text{mArms}$.

- ① Brightness becomes 50 % of the original value under standard condition.
- ② Kick-off voltage at $T_a = 0^{\circ}\text{C}$ exceeds maximum value.

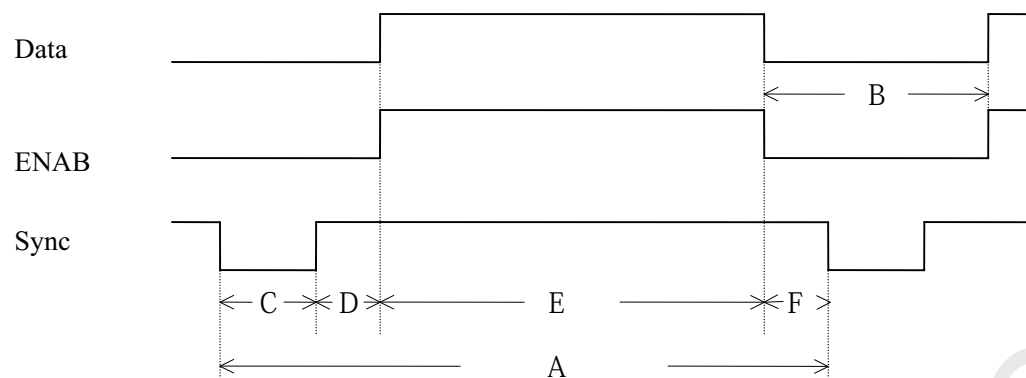
Note) The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.



7. Timing characteristics of LCD module input signals

7-1. Timing characteristics

(This is specified at digital outputs of LVDS driver.)



(Vertical)

Item (symbol)	Min.	Typ.	Max.	Unit	Remark
Vsync cycle (T_{VA})	—	16.667	—	ms	Negative
	796	806	860	line	
Blanking period(T_{VB})	—	38	—	line	
Sync pulse width (T_{VC})	3	6	—	line	
Back porch (T_{VD})	2	29	—	line	
Sync pulse width + Back porch ($T_{VC}+T_{VD}$)	—	35	—	line	
Active display area (T_{VE})	—	768	—	line	
Front porch (T_{VF})	—	3	—	line	

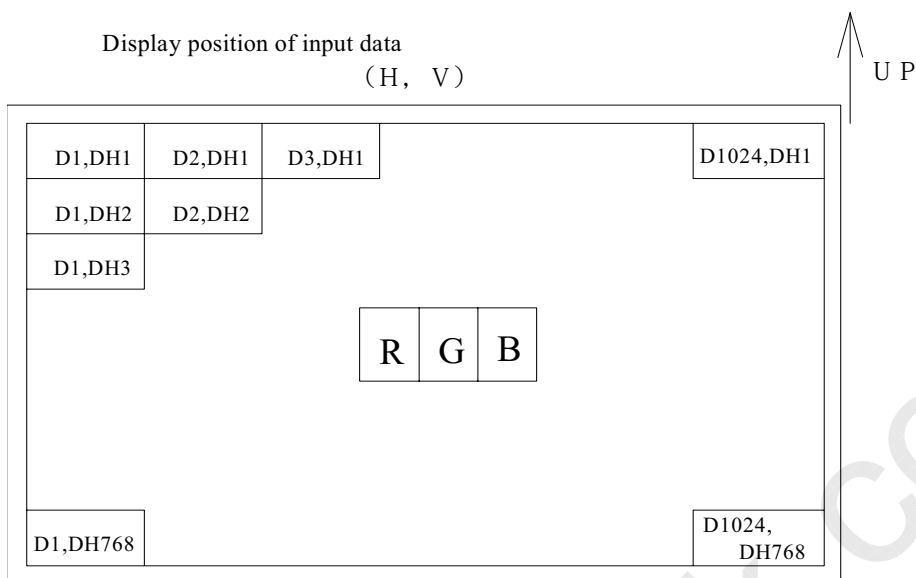
(Horizontal)

Item (symbol)	Min.	Typ.	Max.	Unit	Remark
Hsync cycle (T_{HA})	—	20.677	—	μs	Negative
	1260	1344	1366	clock	
Blanking period (T_{HB})	—	320	—	clock	
Sync pulse width (T_{HC})	16	136	—	clock	
Back porch (T_{HD})	28	160	—	clock	
Sync pulse width + Back porch ($T_{HC} +T_{HD}$)	—	296	—	clock	
Active display area (T_{HE})	—	1024	—	clock	
Front porch (T_{HF})	—	24	—	clock	

(Clock)

Item	Min.	Typ.	Max.	Unit	Remark
Frequency	—	65.0	80	MHz	[Note1]

Note) In case of lower frequency, the deterioration of display quality, flicker etc., may occur.

**7-2. Input Data Signals and Display Position on the screen**

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8. Input Signals, Basic Display Colors and Gray Scale of Each Color

	Colors &	Data Signal																							
	Gray scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑				↓								↓								↓				
	↓				↓								↓								↓				
	Bright	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
↓	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑																								
	↓																								
	Bright	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
↓	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Gray Scale of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	↑																								
	↓																								
	Bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1
↓	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

0 : Low level voltage, 1 : High level voltage

Each basic color can be displayed in 256 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16.2M-color display can be achieved on the screen.



9. Optical Characteristics

Ta=25°C, Vcc=+3.3V

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle Range	Horizontal	θ_{21}, θ_{22}	CR>10	50	60	—	Deg.	【Note1,4】
	Vertical	θ_{11}		35	45	—	Deg.	
		θ_{12}		45	55	—	Deg.	
Viewing Angle Range	Horizontal	θ_{21}, θ_{22}	CR>5	60	70	—	Deg.	
	Vertical	θ_{11}		45	55	—	Deg.	
		θ_{12}		55	65	—	Deg.	
Contrast ratio		C R n	$\theta = 0^\circ$	300	600	—		【Note2,4】
Response Time	Rise	τ_r	$\theta = 0^\circ$	—	3	5	ms	【Note3,4】
	Decay	τ_d		—	13	25	ms	
Chromaticity of White		W_x		0.283	0.313	0.343		【Note4】
		W_y		0.299	0.329	0.359		
Chromaticity of Red		R_x		0.595	0.625	0.655		
		R_y		0.306	0.336	0.366		
Chromaticity of Green		G_x		0.269	0.299	0.329		
		G_y		0.556	0.586	0.616		
Chromaticity of Blue		B_x		0.117	0.147	0.177		
		B_y		0.079	0.109	0.139		
Luminance of white		Y_{L2}	Center	200	270	—	Cd/m ²	IL = 8.0mArms 【Note4】
White Uniformity		δ_w		—	1.25	1.33		【Note5】

※ The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.3.

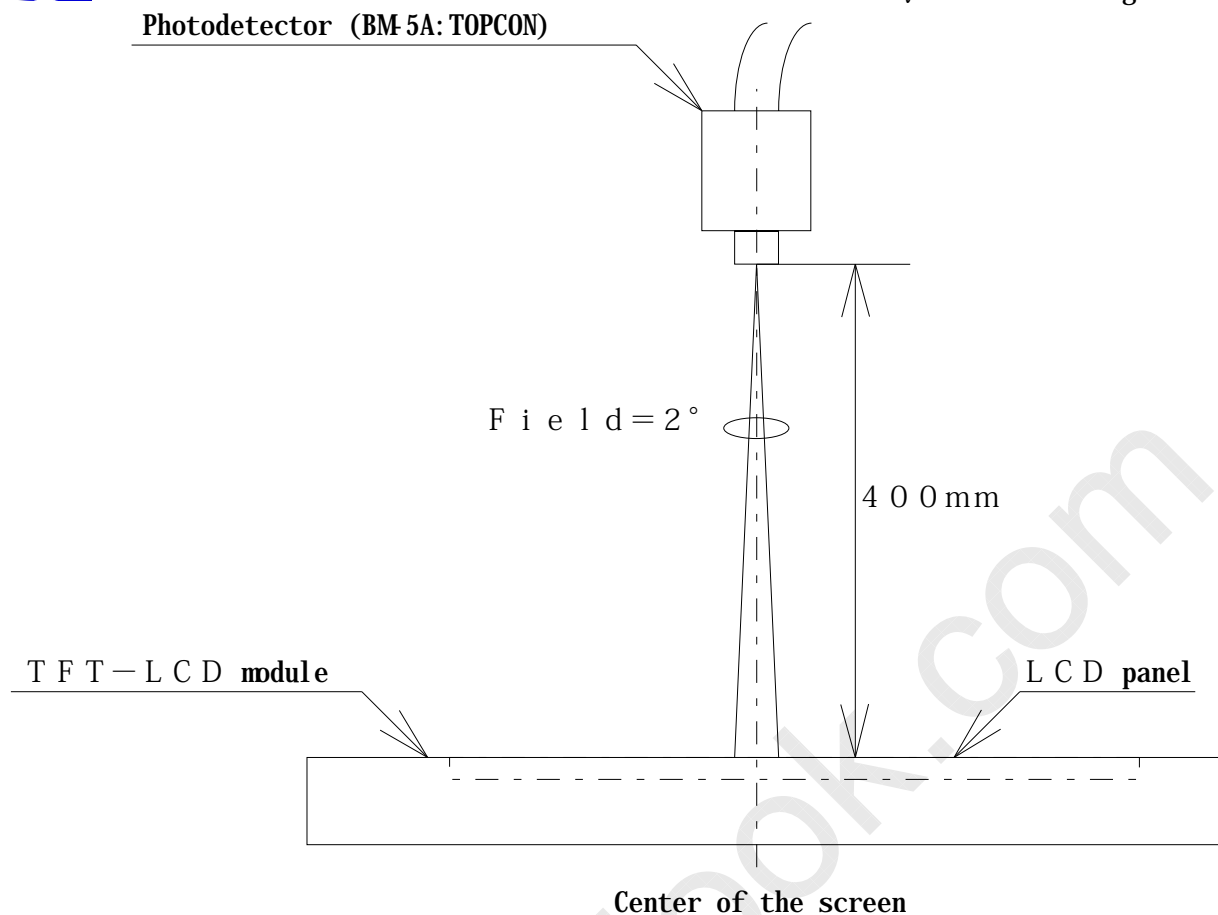
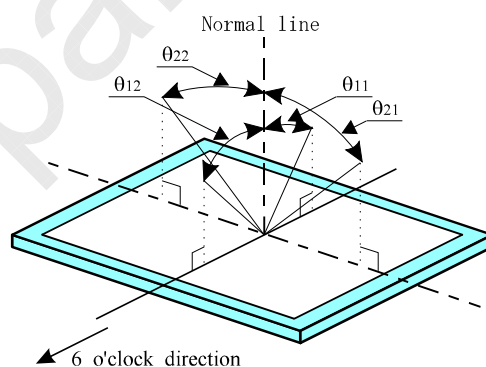


Fig. 3 Optical characteristics measurement method

[Note1] Definitions of viewing angle range:



[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

[Note3] Definition of response time:

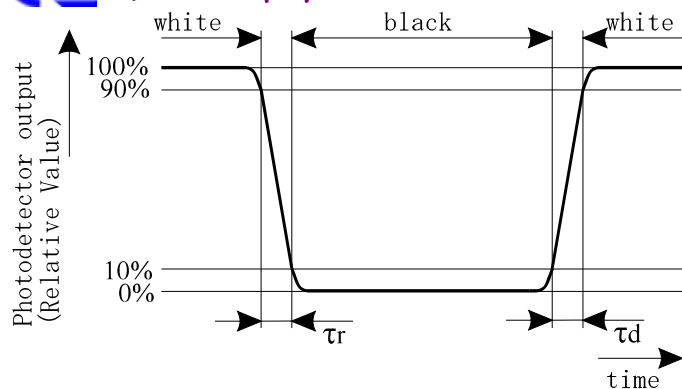
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white" .



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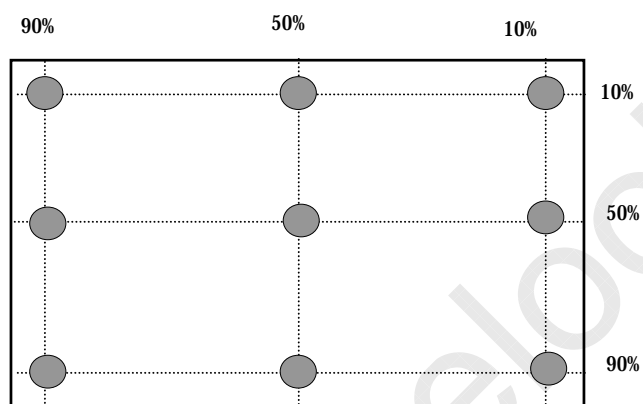
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[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity:

White uniformity is defined as the following with 9 measurements



$$\delta_w = \frac{\text{Maximum Luminance (of 9 points)}}{\text{Minmum Luminance (of 9 points)}}$$



10. Display Quality

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

11. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarizer is easily damaged, pay attention not to scratch it.
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling.
- h) Observe all other precautionary requirements in handling components.
- i) This module has its circuitry PCBs on the rear side and should be handled carefully in order not to be stressed.
- j) Laminated film is attached to the module surface to prevent it from being scratched . Peel the film off slowly just before the use with strict attention to electrostatic charges. Ionized air shall be blown over during the action. Blow off the 'dust' on the polarizer by using an ionized nitrogen gun, etc..
- k) Mounting screw hole can stand torque 1.3~1.5 Kgf-cm.

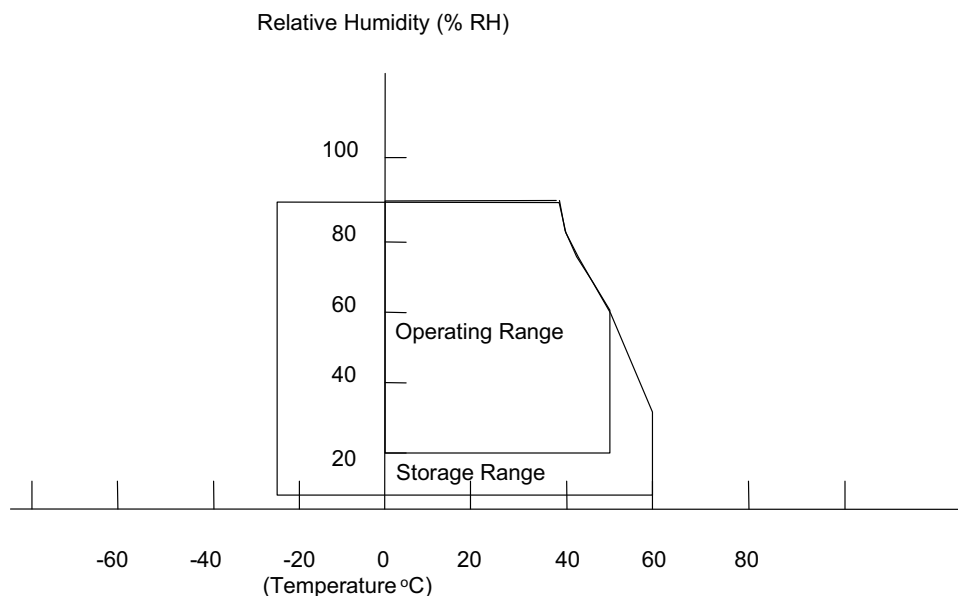


12. Reliability test items

No.	Test item	Conditions
1	High temperature storage test	Ta = 60°C 240h
2	Low temperature storage test	Ta = -25°C 240h
3	High temperature & high humidity operation test	Ta = 40°C ; 90 %RH 240h ; (As remark 3) (No condensation)
4	High temperature operation test	Ta = 50°C 240h (The panel temp. must be less than 60°C)
5	Low temperature operation test	Ta = 0°C 240h
6	Vibration test (non- operating)	Frequency: 10~500Hz, 1G, Test period : 3 hours (1 hour for each direction of X,Y,Z)
7	Shock test (non- operating)	Max. gravity : 100G Pulse width : 2 ms, Half sine wave Direction : ±X,±Y,±Z once for each direction.

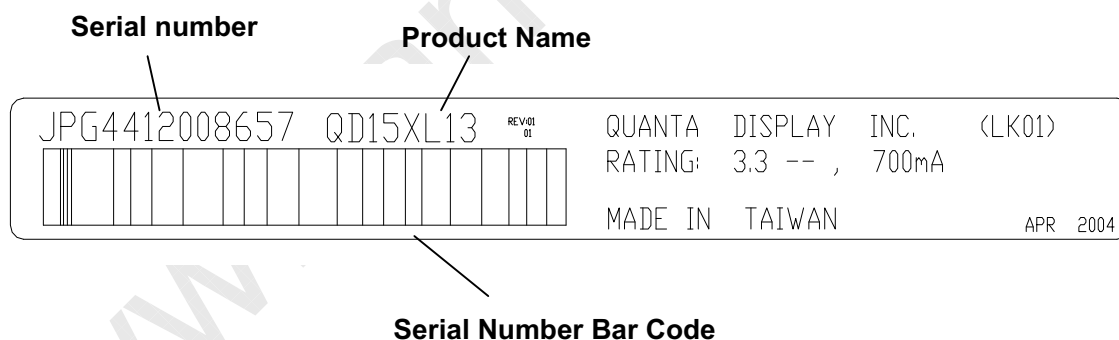
Remark:

- (1) A failure is defined as the appearance of pixel failed on any color layer or the appearance of horizontal or vertical lines, bars etc.
- (2) Low temperature storage “ Panel must return to operating temperature range prior to activation.”
- (3) Hi temperature / Humidity test
Max. wet-bulb temperature is less than 39°C ; At glass temperature high than 40 °C.
Temperature and relative humidity range is shown in the figure below.



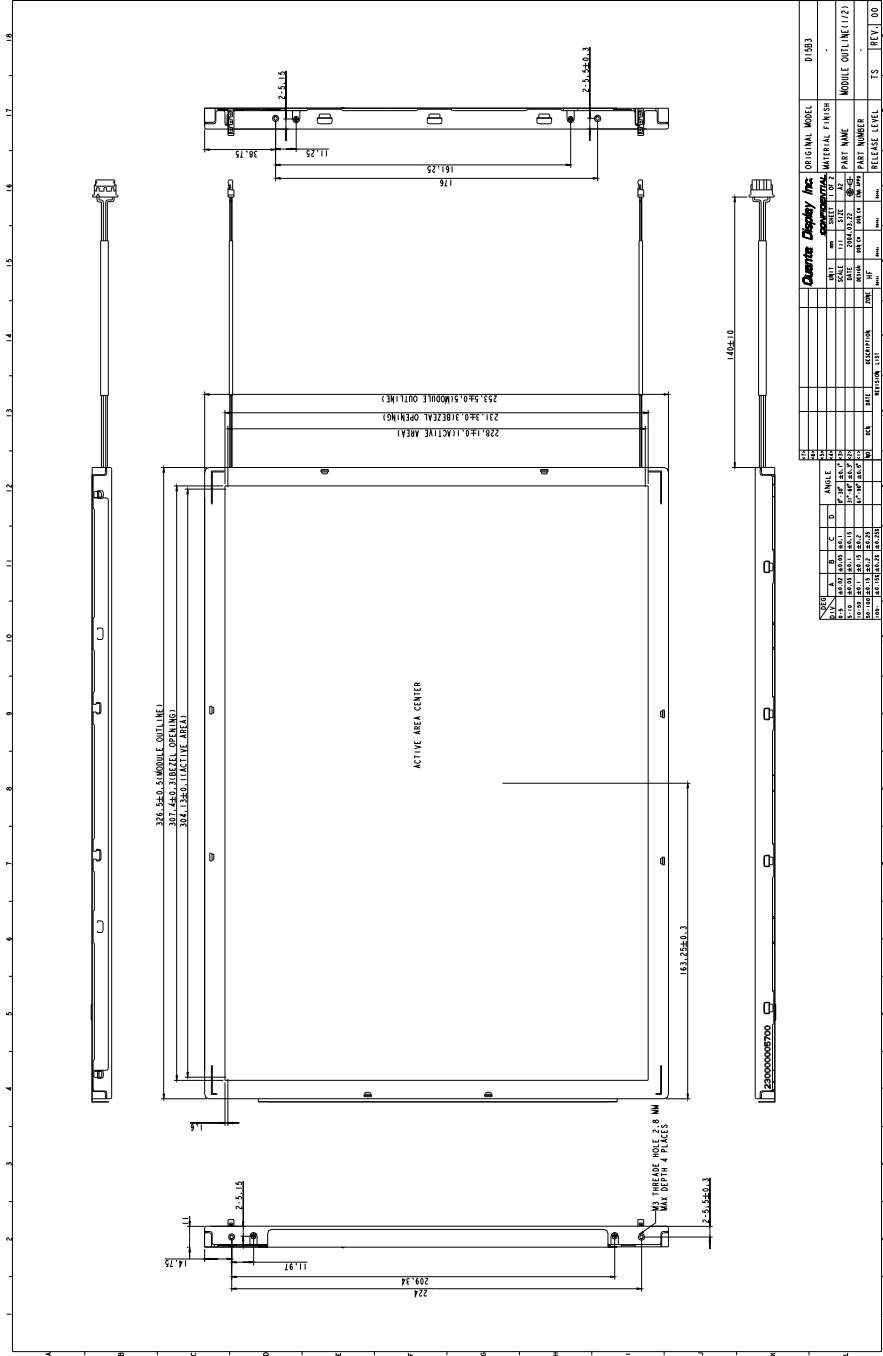
13. Others

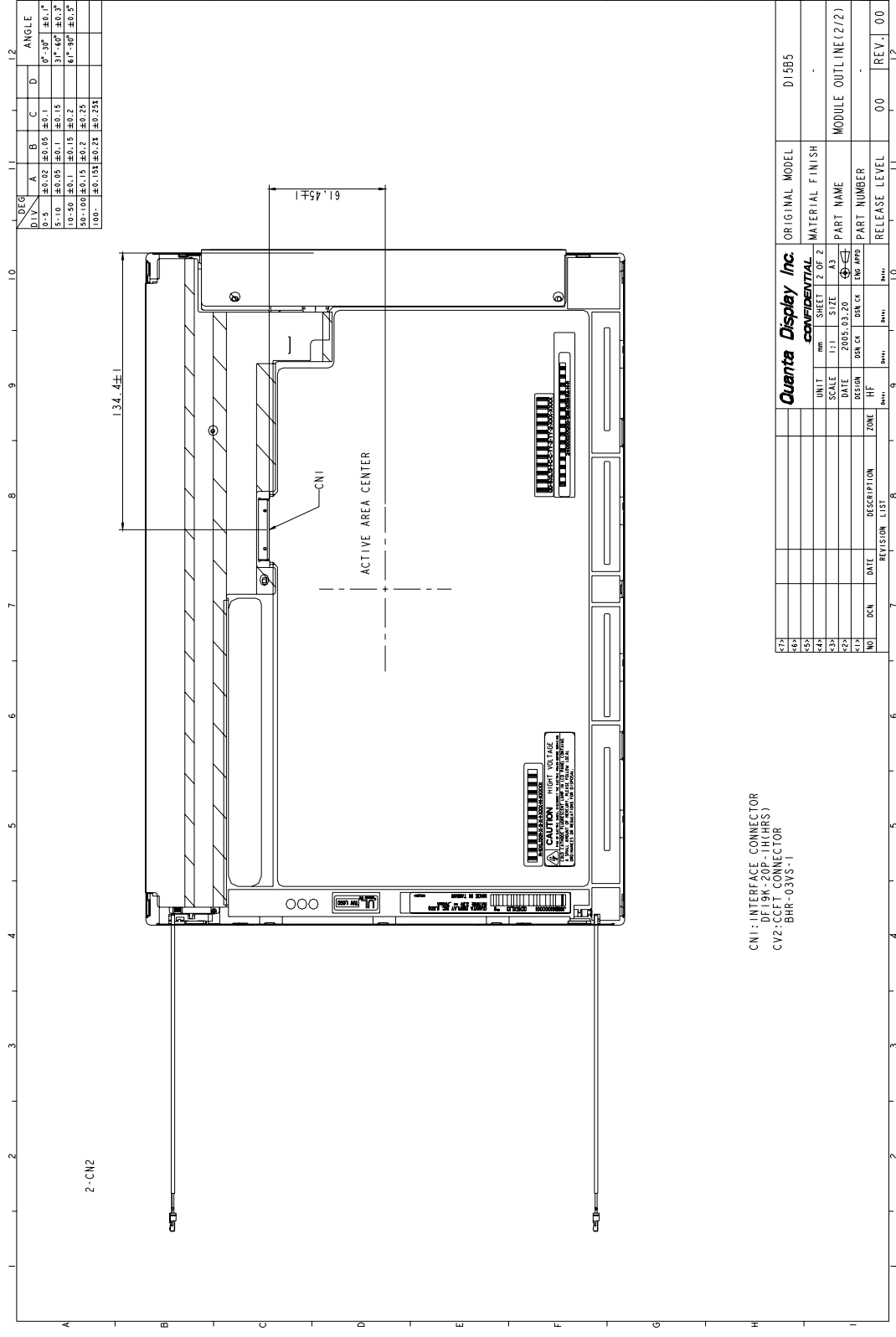
1) Lot No. Label:



- 2) Adjusting volume has been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- 3) Disassembling the module can cause permanent damage and should be strictly avoided.
- 4) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 5) If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.

14. Drawing





CN1: INTERFACE CONNECTOR
 DF19K-20P-1H(HRS)
 CV2: CCFT CONNECTOR
 BHR-03VS-1

UNIT	mm	SIZE	A3	SCALE	1:1	DATE	2005.03.20	DESIGN	DSN CK	ENG APPR	REV. LEVEL	00	REV. NO	1.00
Quanta Display Inc. CONFIDENTIAL SHEET 2 OF 2														
ORIGINAL MODEL: D1585														
MATERIAL FINISH: MODULE OUTLINE(2/2)														
PART NAME: PART NUMBER:														
REVISION LIST:														
NO	DCN	DATE	DESCRIPTION	ZONE										