

N-Ch and P-Ch Fast Switching MOSFETs

General Description

The QM2606C1 is the highest performance trench N-ch and P-ch MOSFETs with extreme high cell density, which provide excellent RDS(on) and gate charge for most of the small power switching and load switch applications.

The QM2606C1 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

Product Summary

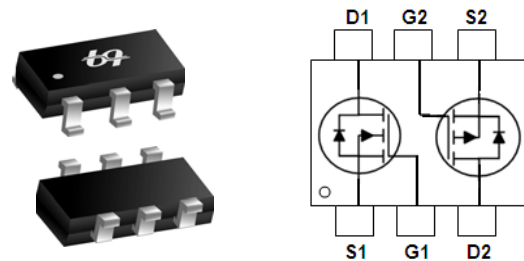


BVDSS	RDS(on)	ID
20V	90mΩ	1.52A
-20V	240mΩ	-1A

Applications

- High Frequency Point-of-Load Synchronous s
- Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

SOT363 (SC-70-6L) Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
V _{DS}	Drain-Source Voltage	20	-20	V
V _{GS}	Gate-Source Voltage	±12	±12	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 4.5V ¹	1.52	-1	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 4.5V ¹	1.22	-0.8	A
I _{DM}	Pulsed Drain Current ²	7.6	-5	A
P _D @T _A =25°C	Total Power Dissipation ³	0.33	0.33	W
T _{STG}	Storage Temperature Range	-55 to 150	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	375	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	240	°C/W

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N-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=1mA$	---	0.022	---	V/ $^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=4.5V$, $I_D=1.5A$	---	70	90	m Ω
		$V_{GS}=2.5V$, $I_D=1A$	---	90	112	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	0.5	0.8	1.2	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-2.2	---	mV/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=16V$, $V_{GS}=0V$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=16V$, $V_{GS}=0V$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 12V$, $V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V$, $I_D=1.5A$	---	6.5	---	S
R_g	Gate Resistance	$V_{DS}=0V$, $V_{GS}=0V$, $f=1MHz$	---	1.7	3.4	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=15V$, $V_{GS}=4.5V$, $I_D=1A$	---	3.64	5.1	nC
Q_{gs}	Gate-Source Charge		---	0.4	0.6	
Q_{gd}	Gate-Drain Charge		---	1.35	1.9	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=10V$, $V_{GS}=4.5V$, $R_G=3.3\Omega$ $I_D=1A$	---	1.4	2.8	ns
T_r	Rise Time		---	26.6	48	
$T_{d(off)}$	Turn-Off Delay Time		---	9.6	19	
T_f	Fall Time		---	6.8	13.6	
C_{iss}	Input Capacitance	$V_{DS}=15V$, $V_{GS}=0V$, $f=1MHz$	---	180	252	pF
C_{oss}	Output Capacitance		---	30	42	
C_{rss}	Reverse Transfer Capacitance		---	25	35	

Drain-Source Body Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source-Drain Diode Current ^{1,4}	$V_G=V_D=0V$, Force Current	---	---	1.52	A
I_{SM}	Pulsed Diode Forward Current ^{2,4}		---	---	7.6	A
V_{SD}	Body Diode Voltage ²	$V_{GS}=0V$, $I_S=1A$, $T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=1A$, $dI/dt=100A/\mu s$, $T_J=25^\circ\text{C}$	---	9	---	nS
Q_{rr}	Reverse Recovery Charge		---	3	---	nC

Note :

1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.

2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$

3.The power dissipation is limited by 150°C junction temperature

4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

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P-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=-250\mu A$	-20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=-1mA$	---	-0.012	---	V/ $^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=-4.5V$, $I_D=-1A$	---	185	240	$m\Omega$
		$V_{GS}=-2.5V$, $I_D=-0.5A$	---	270	350	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu A$	-0.5	-0.8	-1.2	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	2.2	---	mV/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-16V$, $V_{GS}=0V$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-16V$, $V_{GS}=0V$, $T_J=55^\circ\text{C}$	---	---	-5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 12V$, $V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=-5V$, $I_D=-1A$	---	3.1	---	S
Q_g	Total Gate Charge (-4.5V)	$V_{DS}=-15V$, $V_{GS}=-4.5V$, $I_D=-1A$	---	2.9	4.1	nC
Q_{gs}	Gate-Source Charge		---	0.51	0.7	
Q_{gd}	Gate-Drain Charge		---	0.81	1.1	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-10V$, $V_{GS}=-4.5V$, $R_G=3.3\Omega$ $I_D=-1A$	---	2	4.0	ns
T_r	Rise Time		---	8	14.4	
$T_{d(off)}$	Turn-Off Delay Time		---	23	46	
T_f	Fall Time		---	14.8	29.6	
C_{iss}	Input Capacitance	$V_{DS}=-15V$, $V_{GS}=0V$, $f=1MHz$	---	190	266	pF
C_{oss}	Output Capacitance		---	33	46.2	
C_{rss}	Reverse Transfer Capacitance		---	27	37.8	

Drain-Source Body Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source-Drain Diode Current ^{1,4}	$V_G=V_D=0V$, Force Current	---	---	-1	A
I_{SM}	Pulsed Diode Forward Current ^{2,4}		---	---	-5	A
V_{SD}	Body Diode Voltage ²	$V_{GS}=0V$, $I_S=-1A$, $T_J=25^\circ\text{C}$	---	---	-1.2	V
t_{rr}	Reverse Recovery Time	$I_F=-2A$, $dI/dt=100A/\mu s$, $T_J=25^\circ\text{C}$	---	6.2	---	nS
Q_{rr}	Reverse Recovery Charge		---	2	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

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N-Channel Typical Characteristics

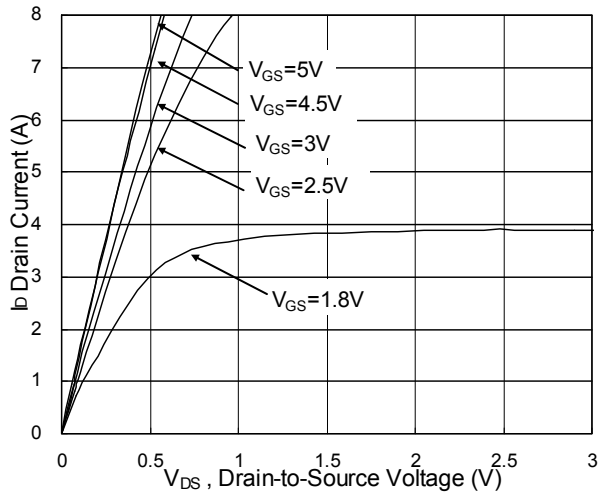


Fig.1 Typical Output Characteristics

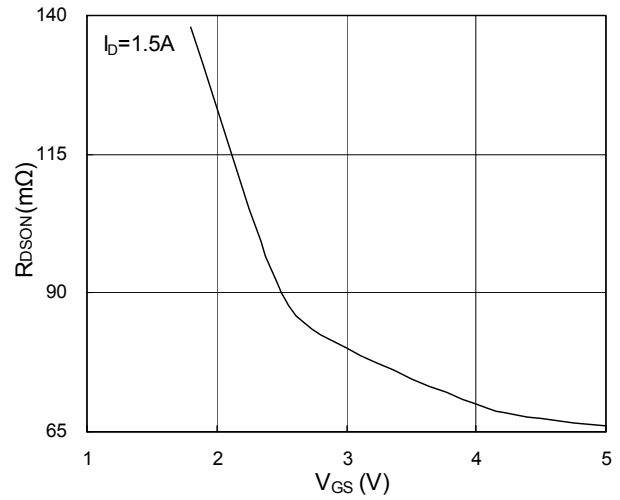


Fig.2 On-Resistance vs. Gate-Source

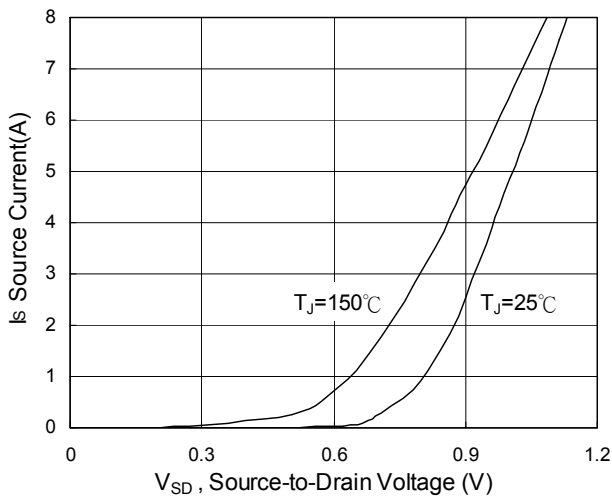


Fig.3 Forward Characteristics Of Reverse

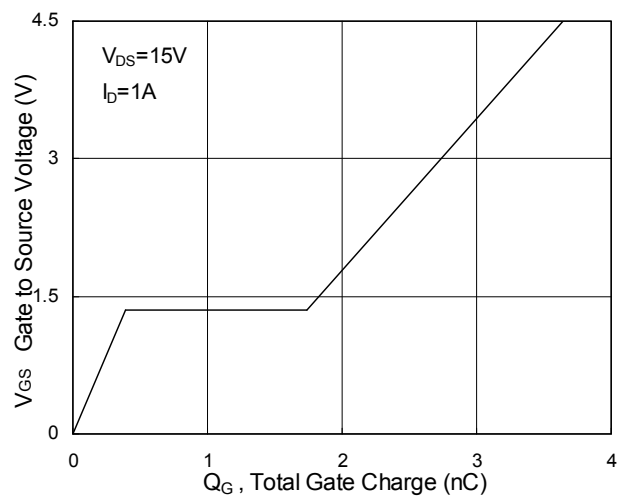


Fig.4 Gate-Charge Characteristics

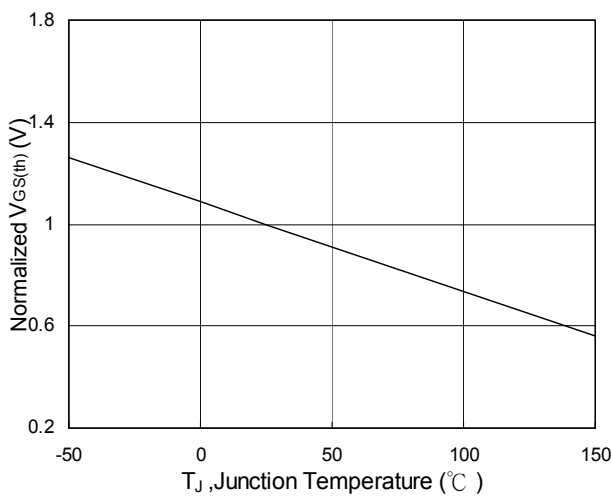


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

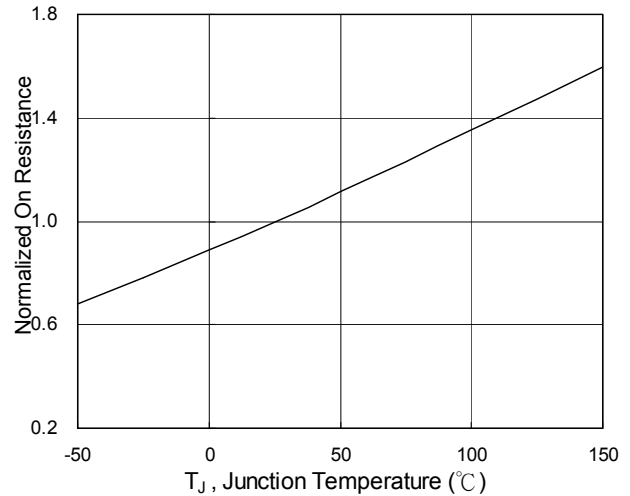


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

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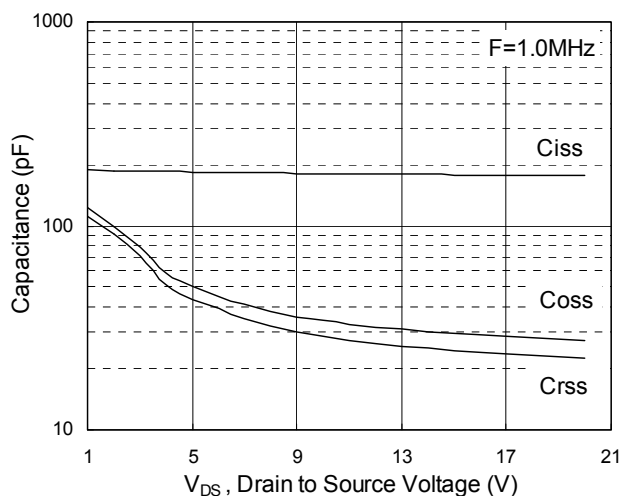


Fig.7 Capacitance

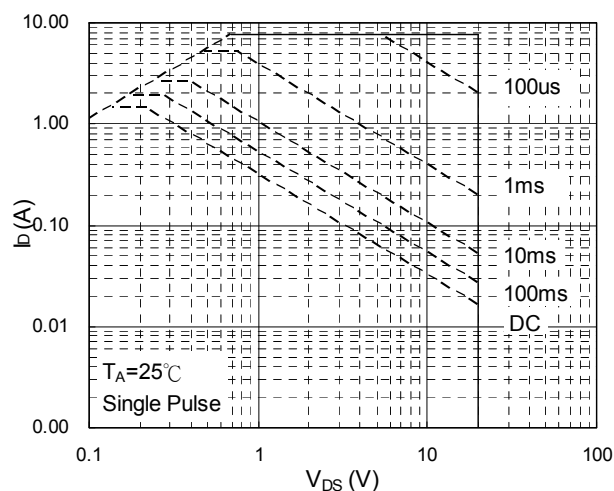


Fig.8 Safe Operating Area

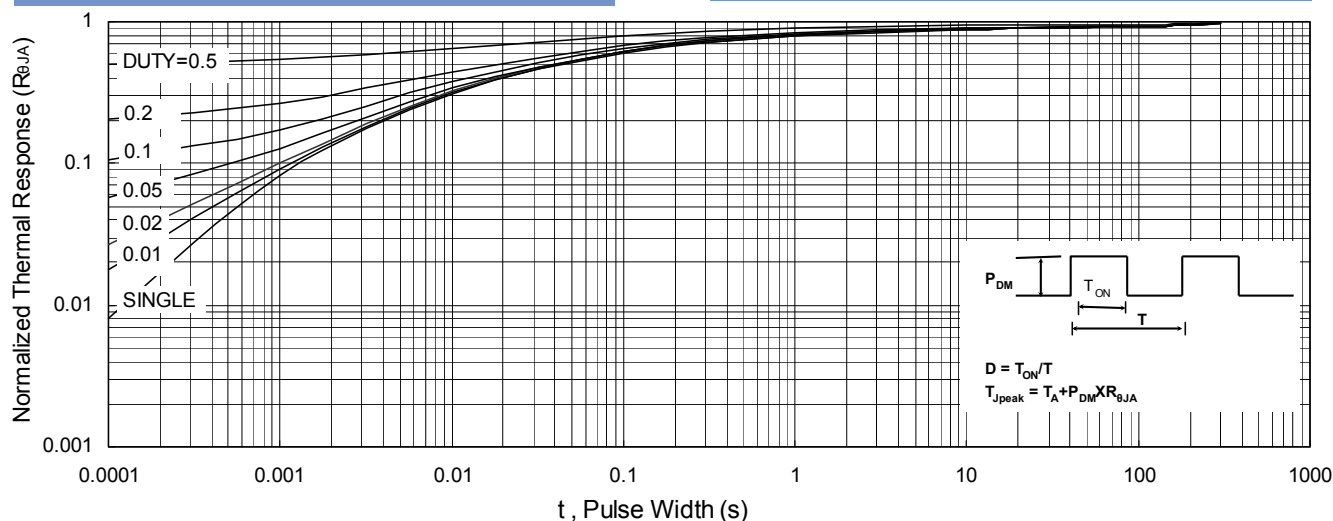


Fig.9 Normalized Maximum Transient Thermal Impedance

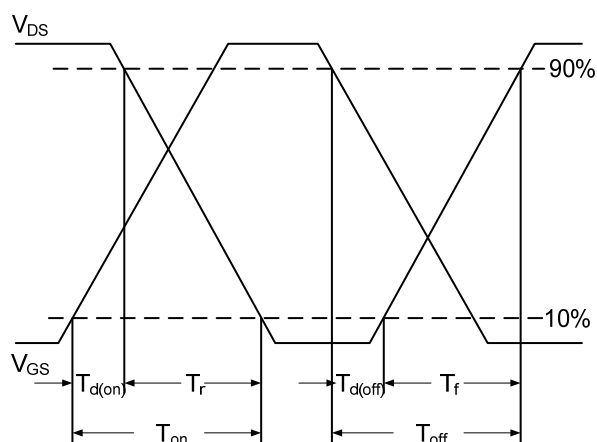


Fig.10 Switching Time Waveform

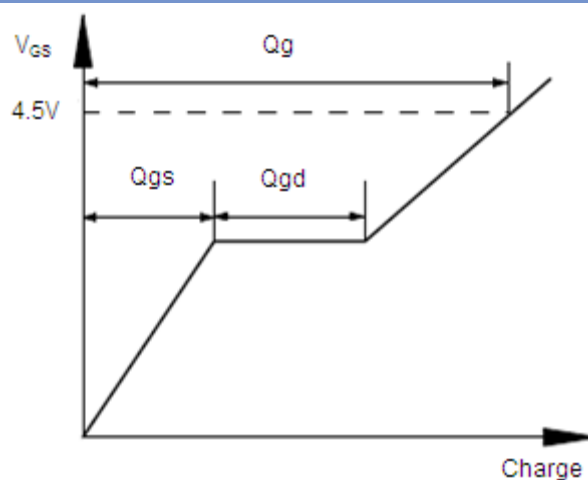


Fig.11 Gate Charge Waveform

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P-Channel Typical Characteristics

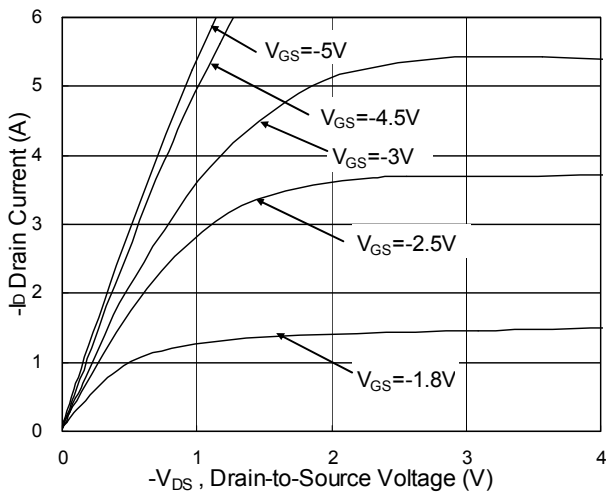


Fig.1 Typical Output Characteristics

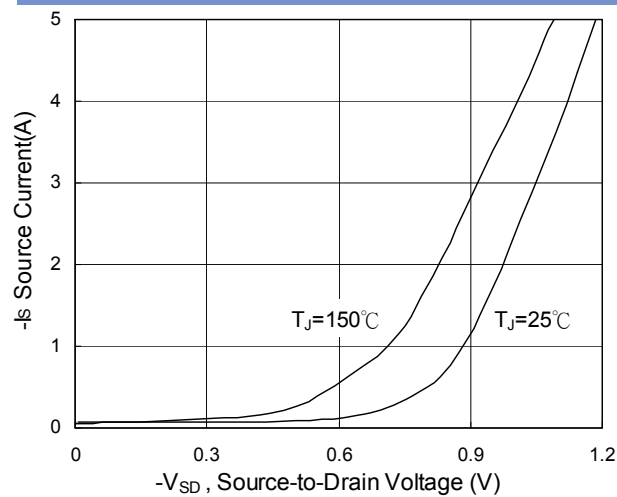


Fig.3 Forward Characteristics Of Reverse

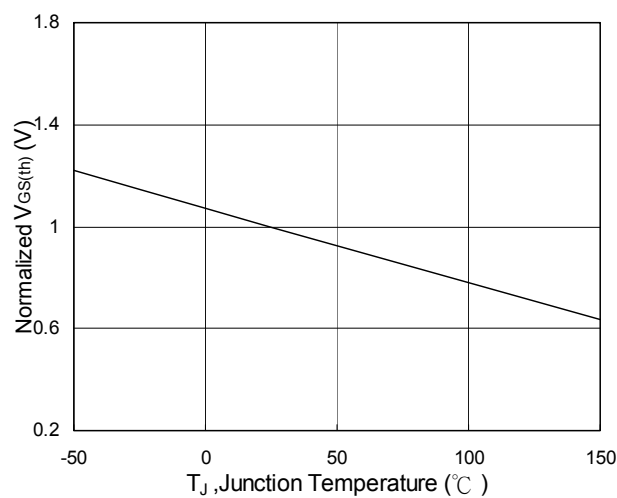


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

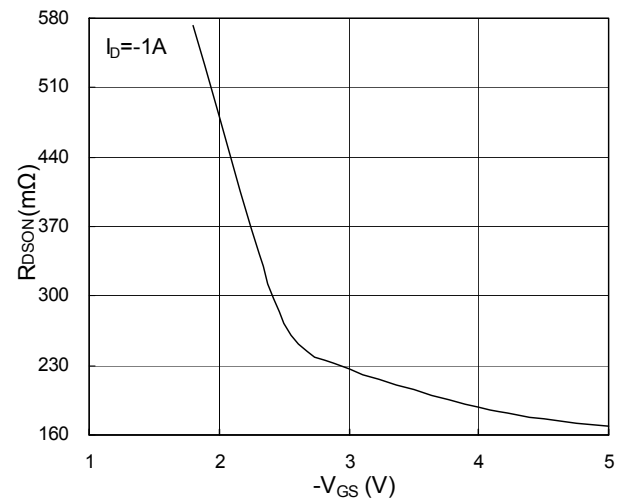


Fig.2 On-Resistance vs. Gate-Source

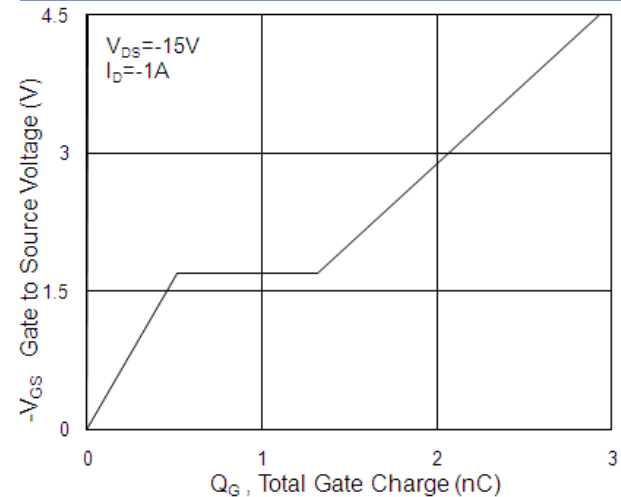


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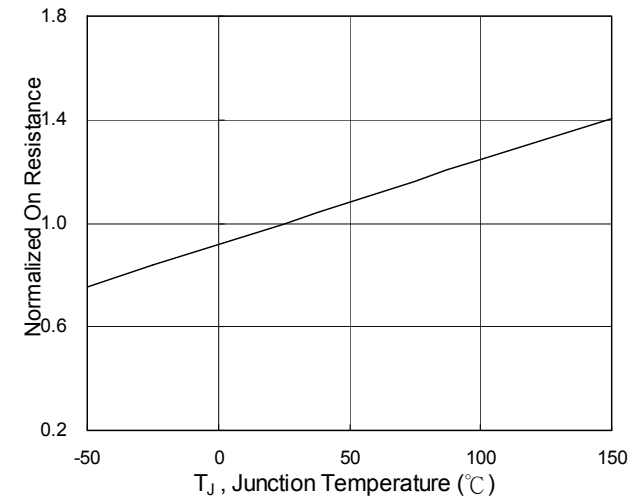


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

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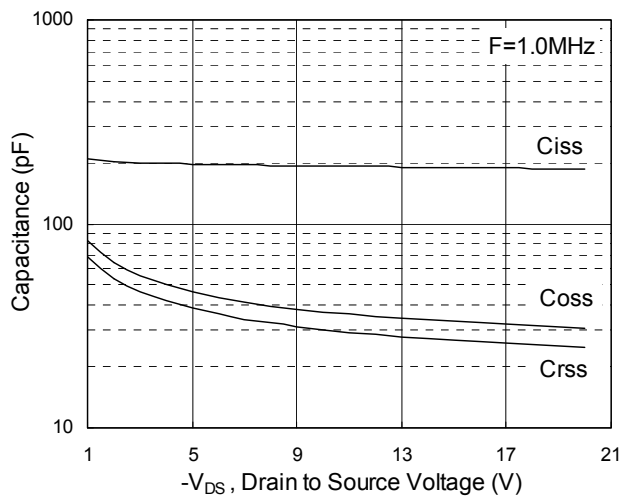


Fig.7 Capacitance

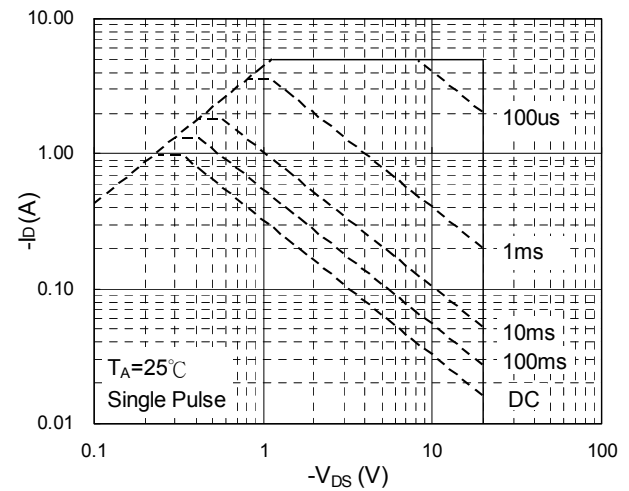


Fig.8 Safe Operating Area

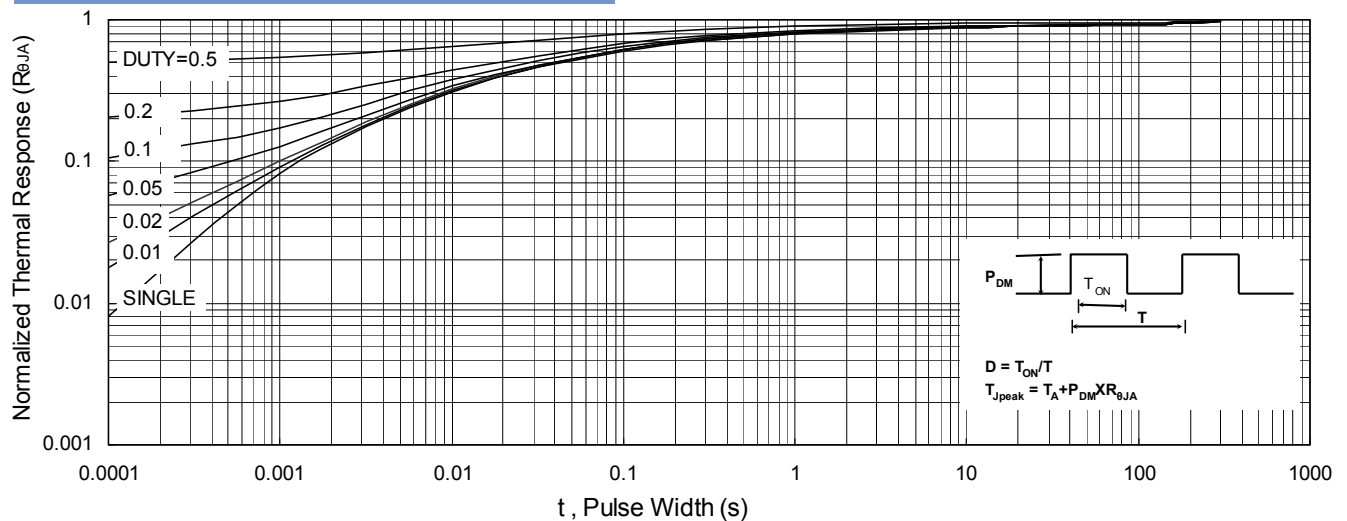


Fig.9 Normalized Maximum Transient Thermal Impedance

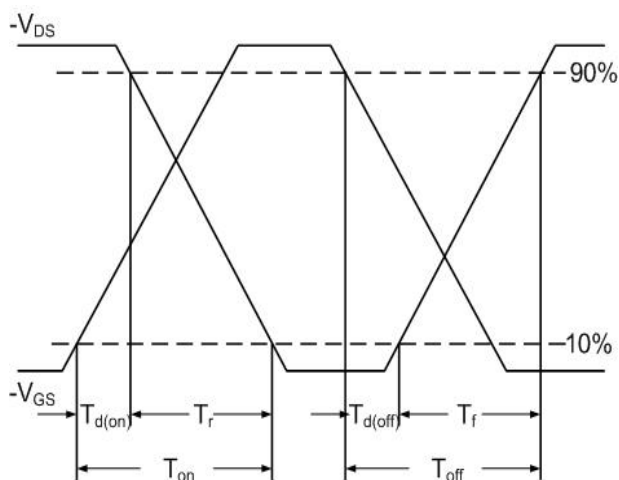


Fig.10 Switching Time Waveform

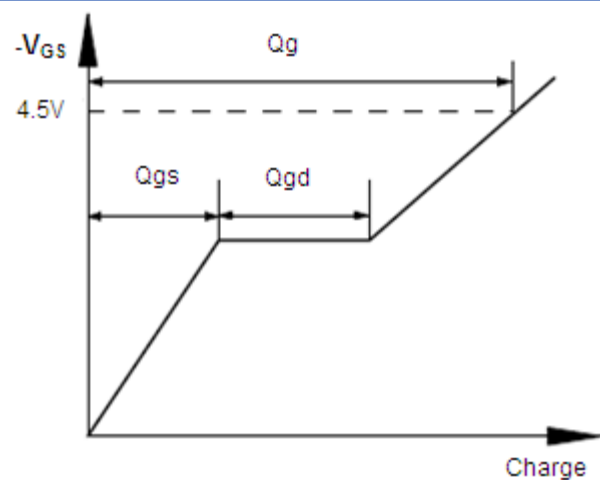


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