

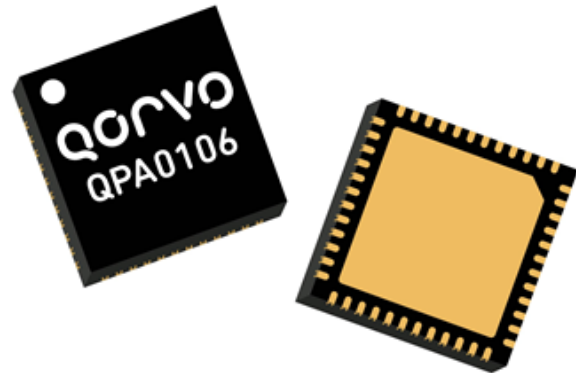
Product Overview

Qorvo’s QPA0106 is a wideband power amplifier fabricated using Qorvo’s QGaN25 0.25 um GaN on SiC production process. Covering 1–6 GHz, the QPA0106 typically provides 42.7 dBm of saturated output power and 24 dB of large-signal gain while achieving greater than 40 % power-added efficiency.

The QPA0106 can support a variety of operating conditions to best support system requirements. With good thermal properties, it can support a range of bias voltages. The QPA0106 is matched to 50 ohms with integrated DC blocking caps on both I/O ports..

The QPA0106 is packaged in a plastic overmolded 7 x 7 mm package. The QPA0106 is 100% DC and RF tested to ensure compliance to electrical specifications.

Lead free and RoHS compliant.

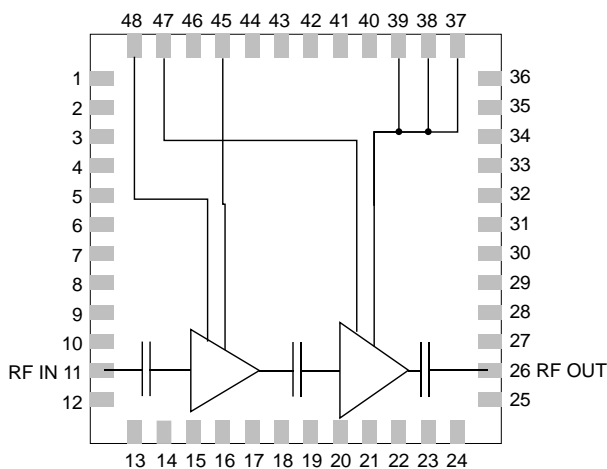


Key Features

- Frequency Range: 1 – 6 GHz
- P_{SAT} : 42.7 dBm (P_{IN} = 19 dBm)
- PAE: > 40% (P_{IN} = 19 dBm)
- Power Gain: 24 dB (P_{IN} = 19 dBm)
- Small Signal Gain: > 31 dB
- Bias: V_D = 22 V, I_{DQ} = 1022 mA
- Package Dimensions: 7.00 x 7.00 x 0.85 mm

Performance is typical across frequency. Please reference electrical specification table and data plots for more details.

Functional Block Diagram



Top View

Applications

- Electronic Warfare
- Radar
- Test Instrumentation
- Communications

Ordering Information

Part No.	Description
QPA0106	1 -6 GHz GaN Power Amplifier
QPA0106TR7	250 pcs. on 7 inch reel
QPA0106EVB	Evaluation Board for QPA0106

Absolute Maximum Ratings

Parameter	Value / Range
Drain Voltage (V_D)	40 V
Gate Voltage Range (V_G)	-5 V to 0 V
Drain Current (I_D)	2.865 A
Gate Current (I_G)	See plot page 21
P_{DISS} (under drive), 85 °C	56 W
Input Power, 50 Ω , $V_D=22$ V, $I_{DQ}=1022$ mA, CW, 85 °C	30 dBm
Input Power, 3:1 VSWR, $V_D=22$ V, $I_{DQ}=1022$ mA, CW, 85 °C	30 dBm
Soldering Temperature	260 °C
Storage Temperature	-55 to +125 °C

Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.

Recommended Operating Conditions

Parameter	Min	Typ	Max	Unit
Drain Voltage (V_D)	12	22	22	V
Drain Current (I_{DQ})		1022		mA
Operating Temperature	-40	25	85	°C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

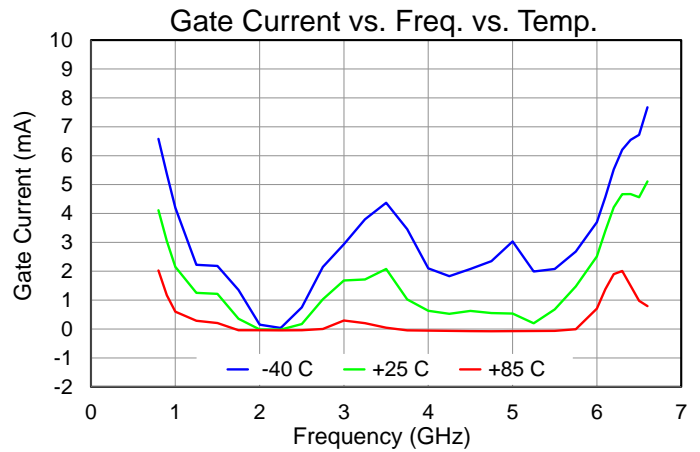
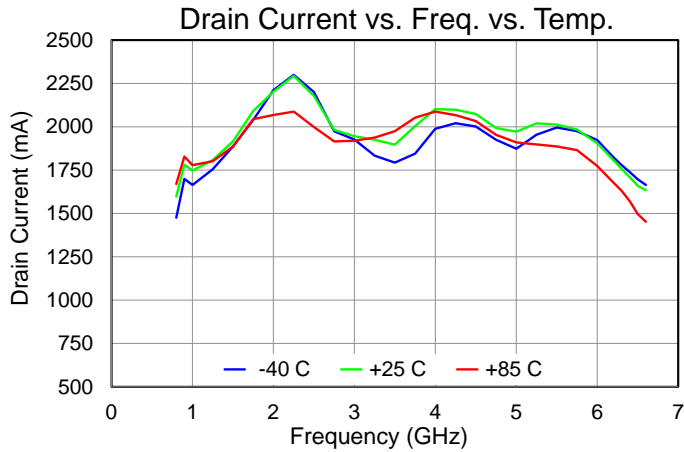
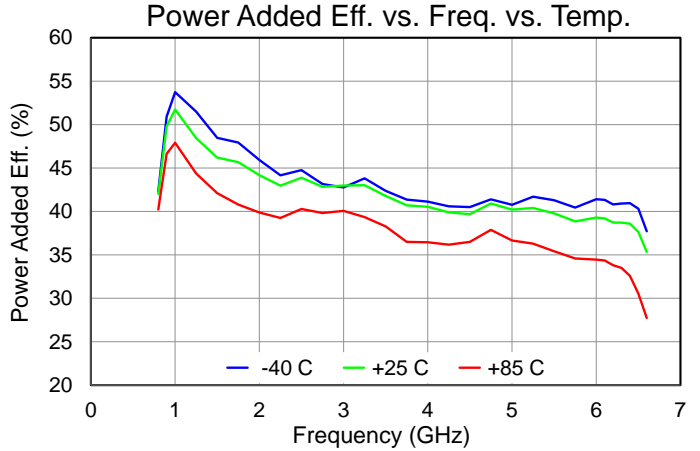
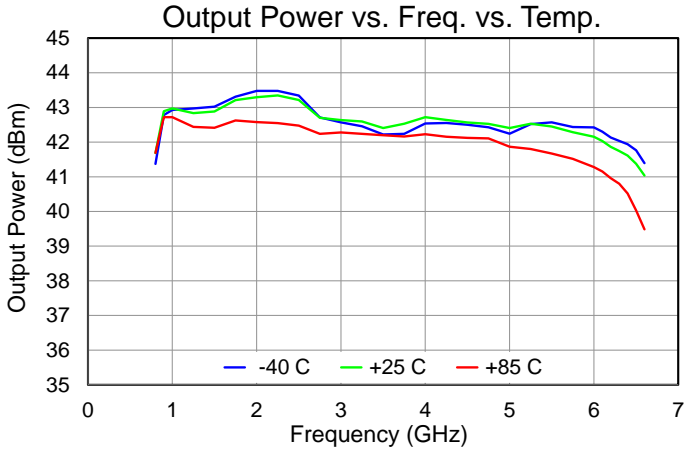
Electrical Specifications

Parameter		Min	Typ	Max	Units
Operational Frequency		1		6	GHz
Output Power ($P_{IN}=19$ dBm)	1 GHz		43.0		dBm
	2 GHz		43.3		dBm
	4 GHz		42.7		dBm
	6 GHz		42.2		dBm
PAE ($P_{IN}=19$ dBm)	1 GHz		51.7		%
	2 GHz		44.3		%
	4 GHz		40.7		%
	6 GHz		39.5		%
Small Signal Gain	1 GHz		33.7		dB
	2 GHz		33.0		dB
	4 GHz		32.9		dB
	6 GHz		31.1		dB
Input Return Loss	1 GHz		13		dB
	2 GHz		16		dB
	4 GHz		19		dB
	6 GHz		17		dB
Output Return Loss	1 GHz		14		dB
	2 GHz		18		dB
	4 GHz		15		dB
	6 GHz		22		dB
Second Harmonic Level ($P_{IN}=19$ dBm)	1 GHz		-26		dBc
	2 GHz		-17		dBc
	4 GHz		-33		dBc
	6 GHz		-38		dBc
Third Harmonic Level ($P_{IN}=19$ dBm)	1 GHz		-14		dBc
	2 GHz		-12		dBc
	4 GHz		-34		dBc
	6 GHz		-62		dBc
Third Order IM Distortion ($P_{OUT}/\text{tone} = 30$ dBm)	1 GHz		-31		dBc
	2 GHz		-28		dBc
	4 GHz		-27		dBc
	6 GHz		-29		dBc
P_{OUT} Temp. Coeff. (85 °C to -40 °C, $P_{IN} = 19$ dBm)			-0.004		dB/°C
Sm. Sig. Gain Temp. Coefficient (85 °C to -40 °C)			-0.035		dB/°C
Gate Leakage Current ($V_D = +10$ V, $V_G = -3.7$ V)		-11.3			mA

Test conditions, unless otherwise noted: T = 25 °C, $V_D = 22$ V, $I_{DQ} = 1022$ mA

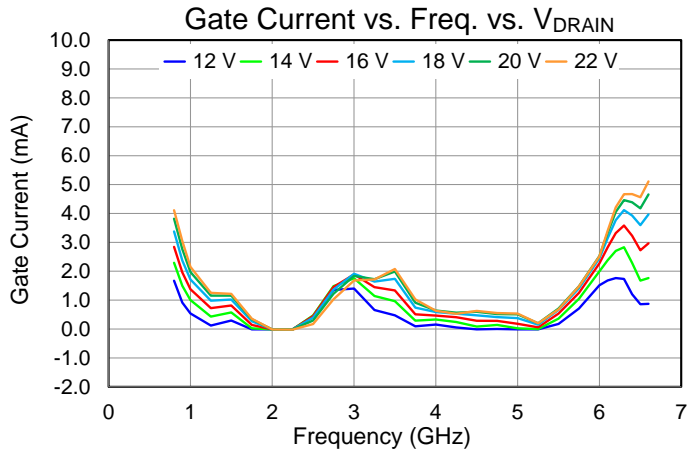
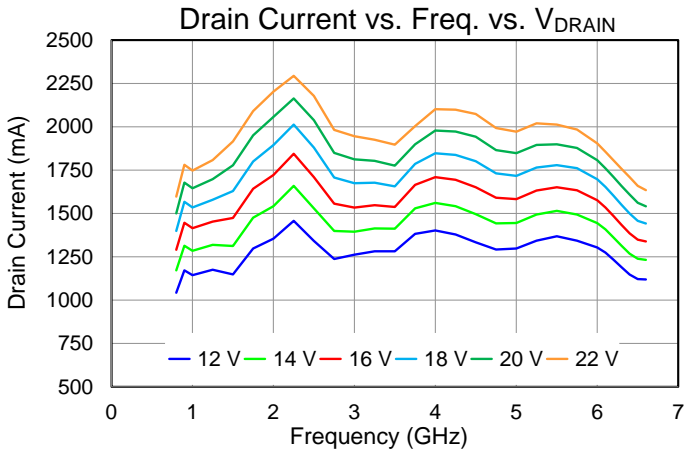
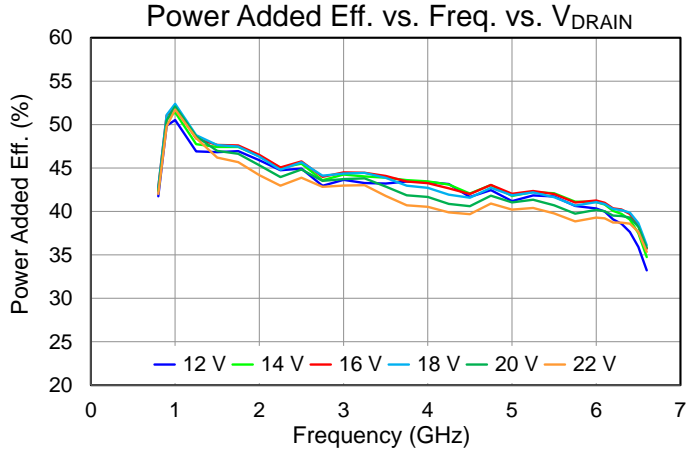
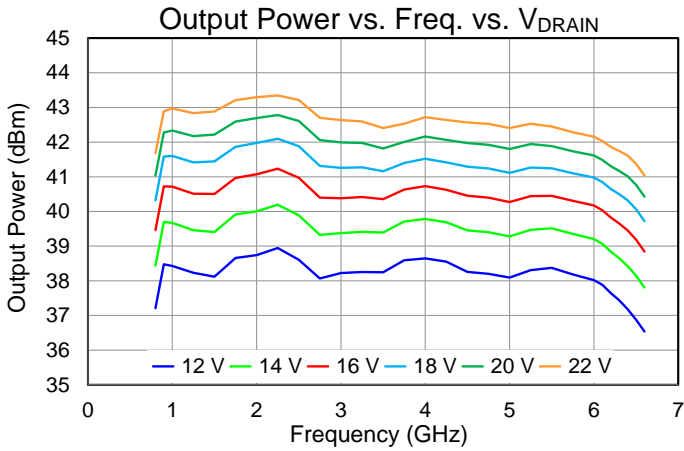
Performance Plots – Large Signal

Test conditions, unless otherwise noted: $V_D = 22\text{ V}$, $I_{DQ} = 1022\text{ mA}$, $T = +25\text{ }^\circ\text{C}$, $P_{IN} = 19\text{ dBm}$, CW



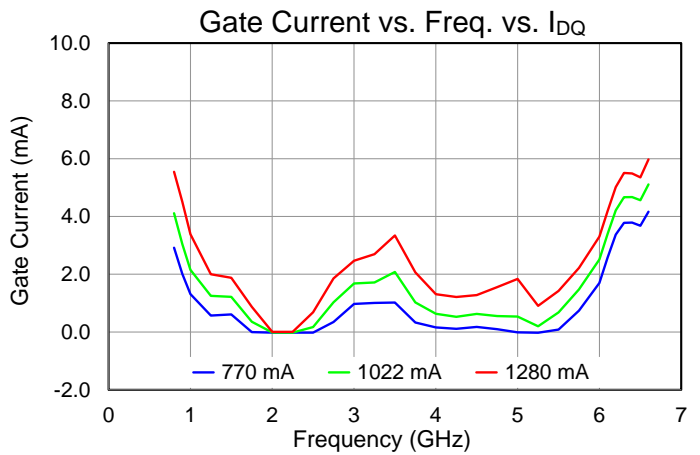
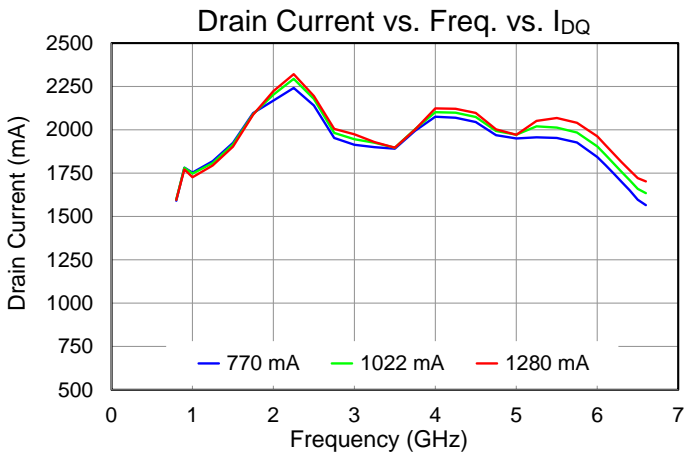
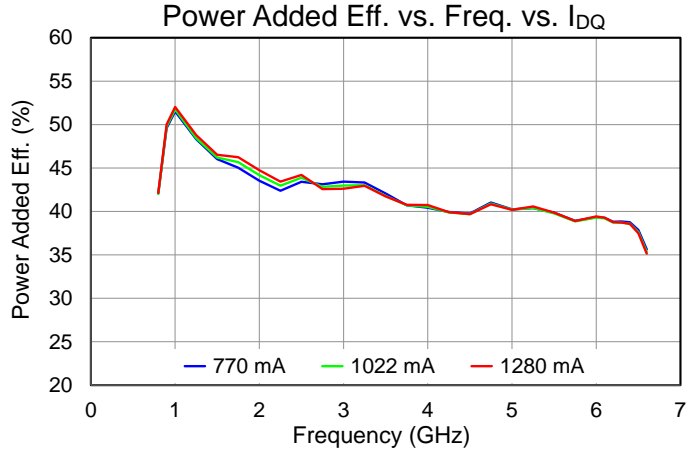
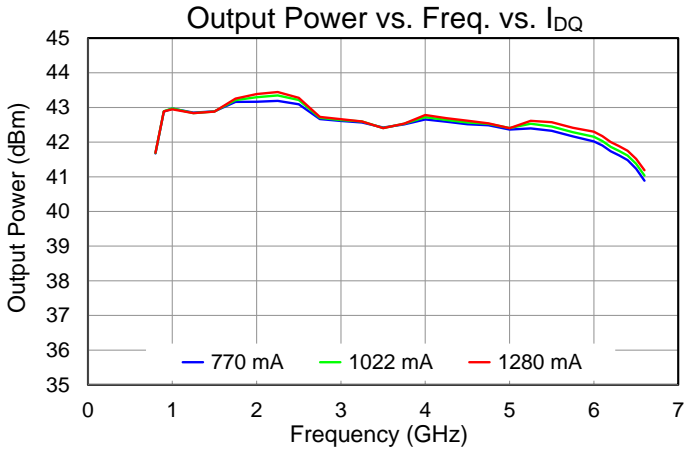
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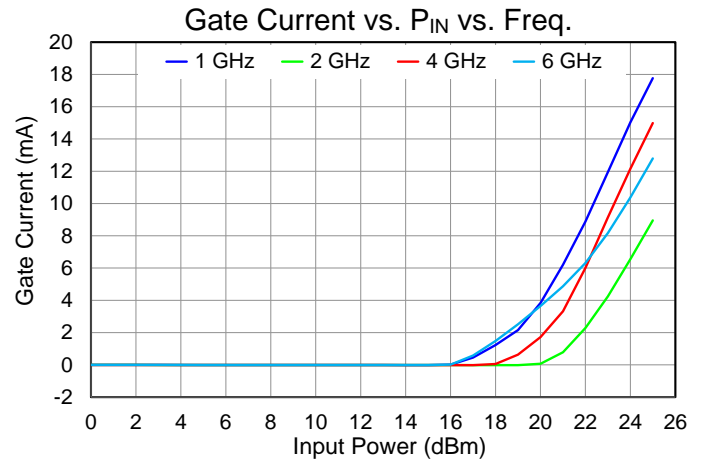
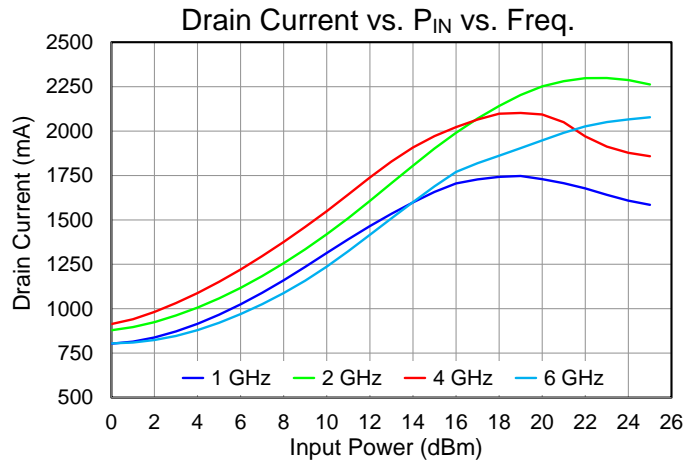
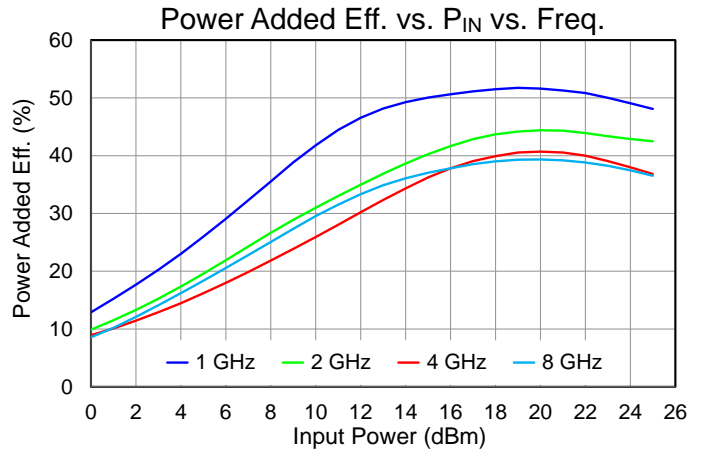
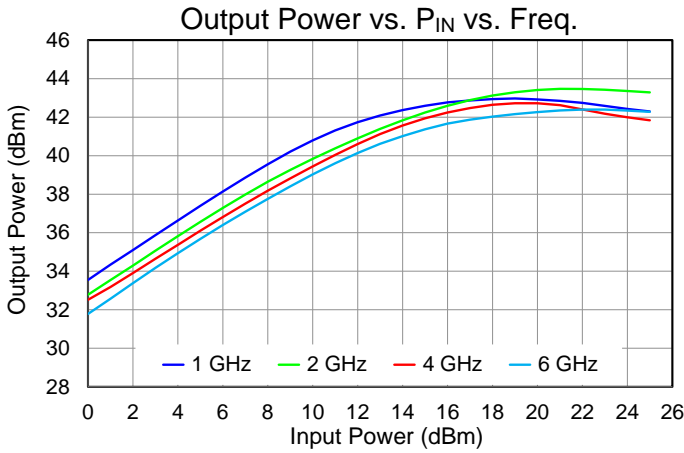
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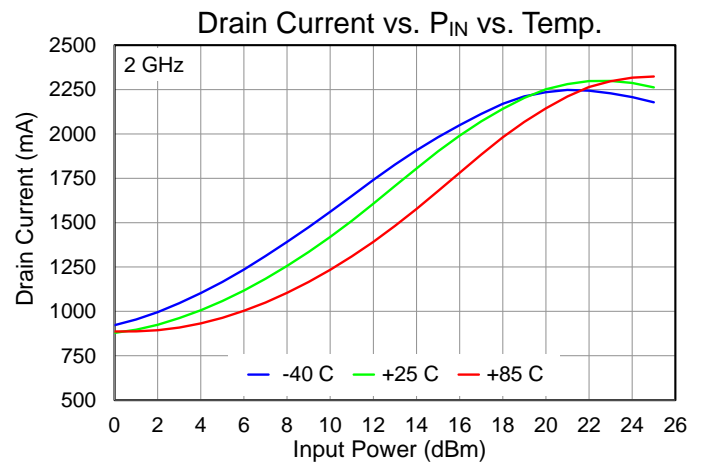
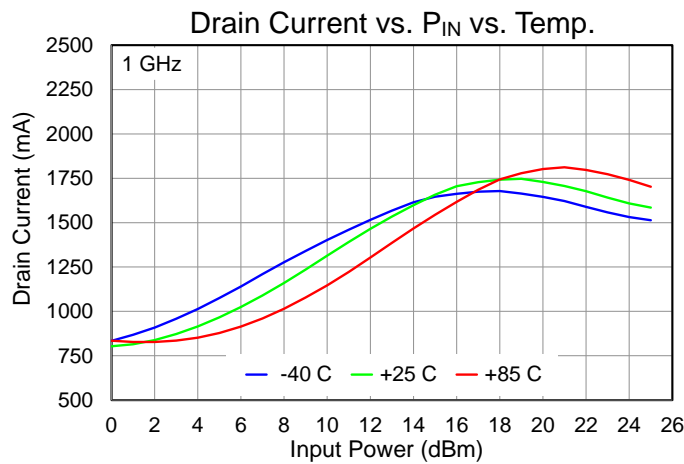
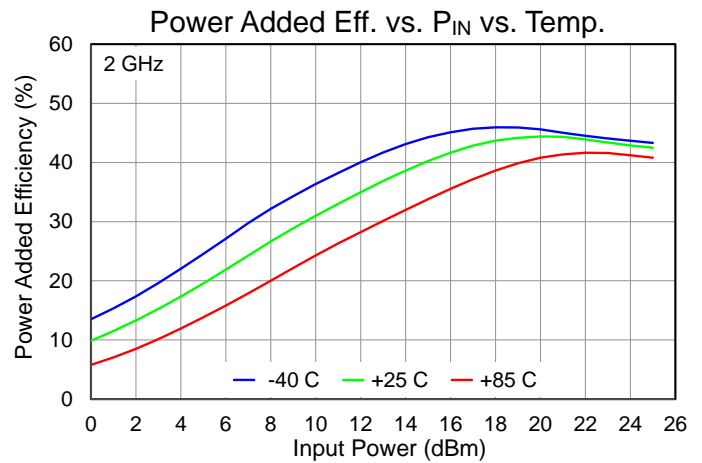
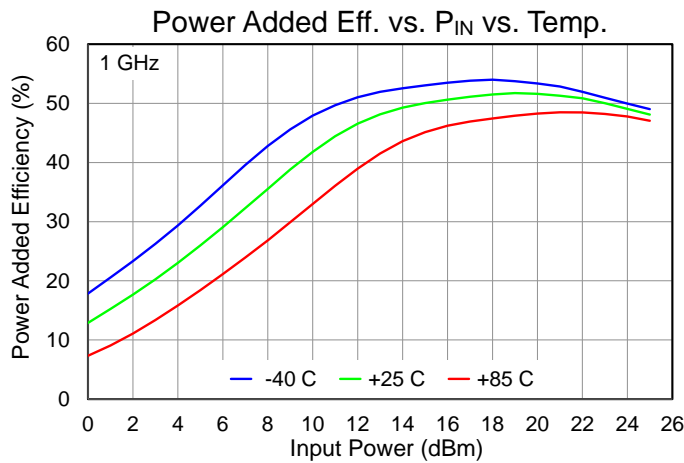
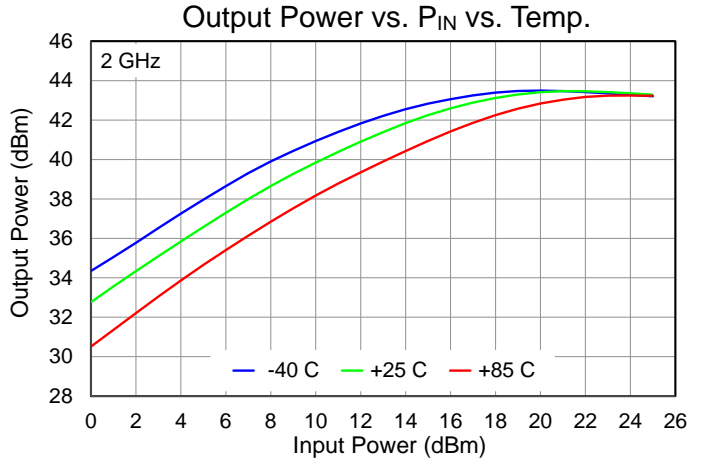
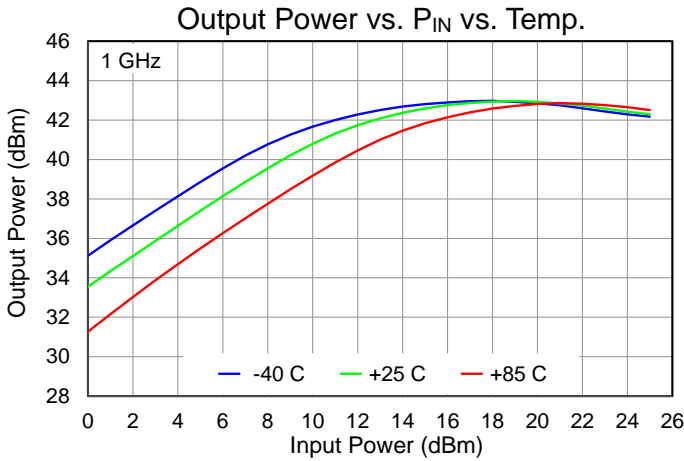
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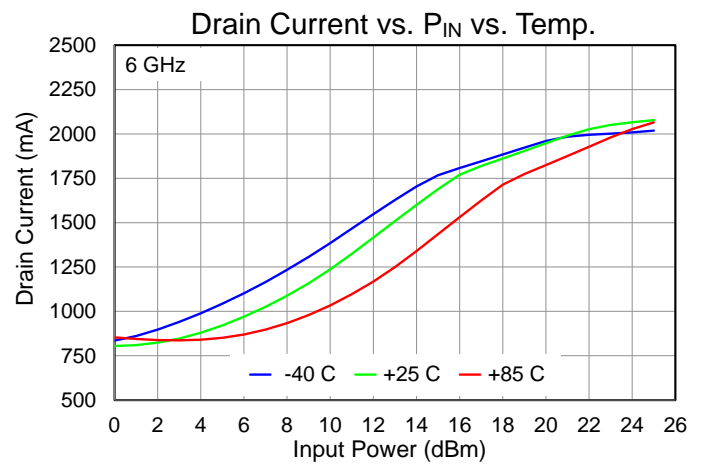
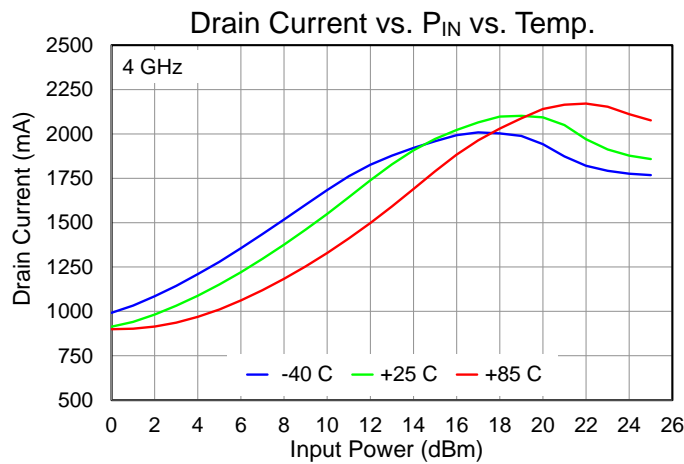
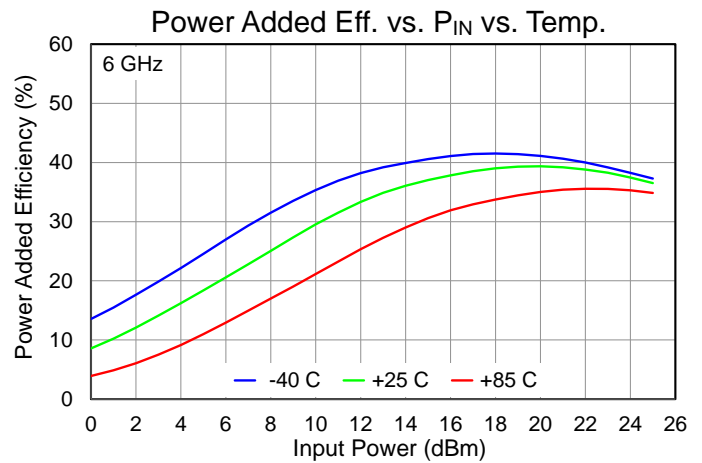
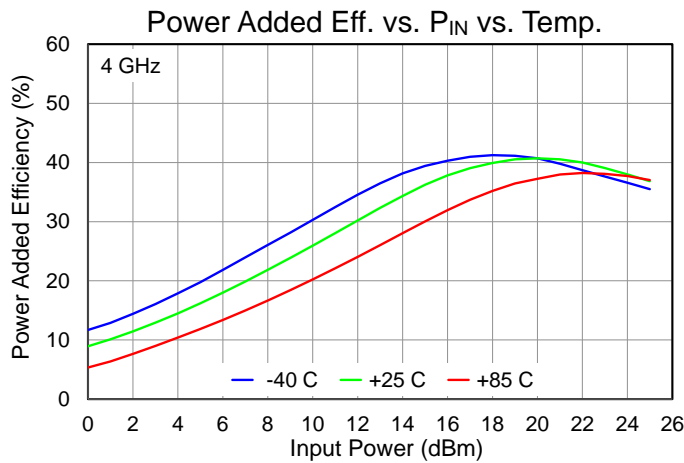
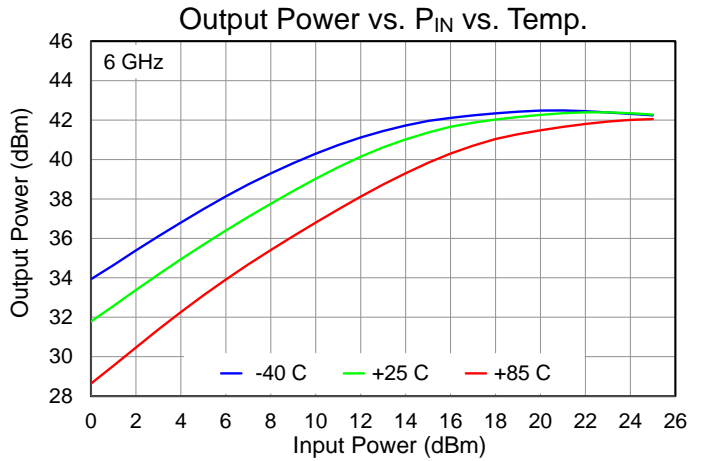
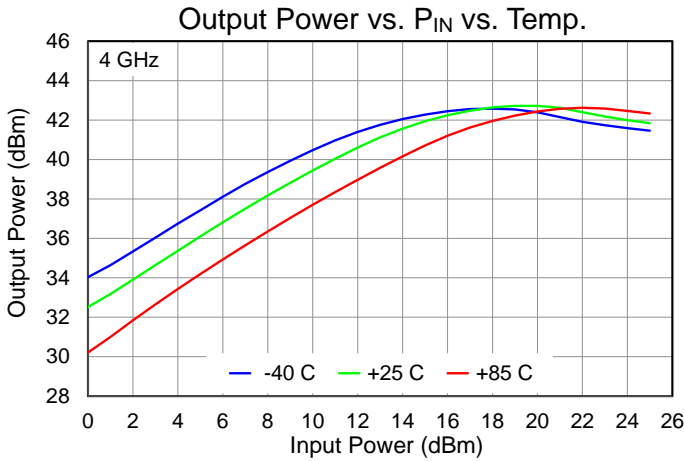
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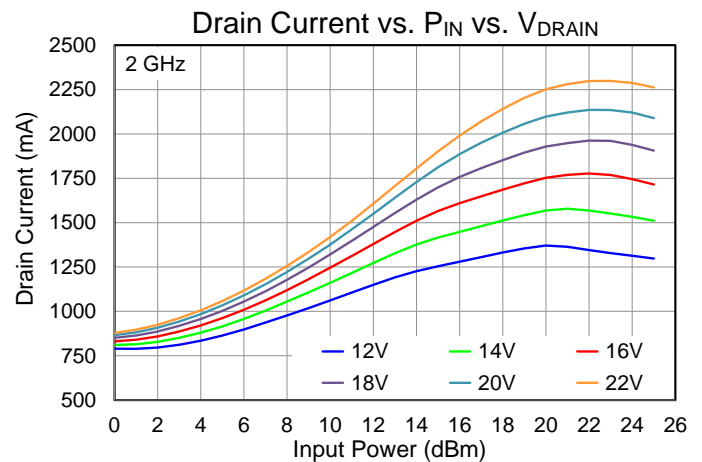
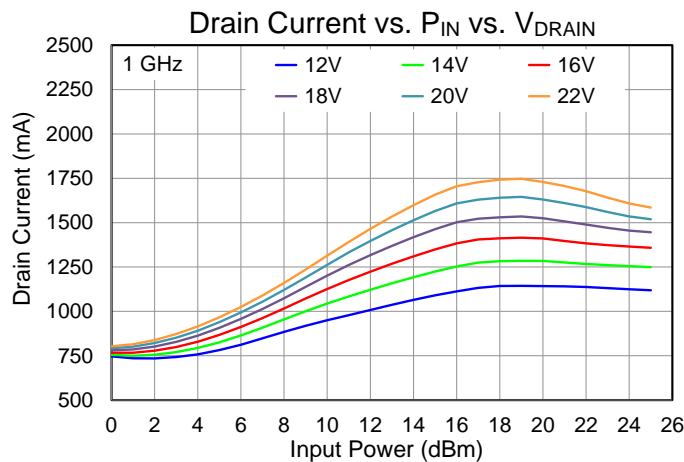
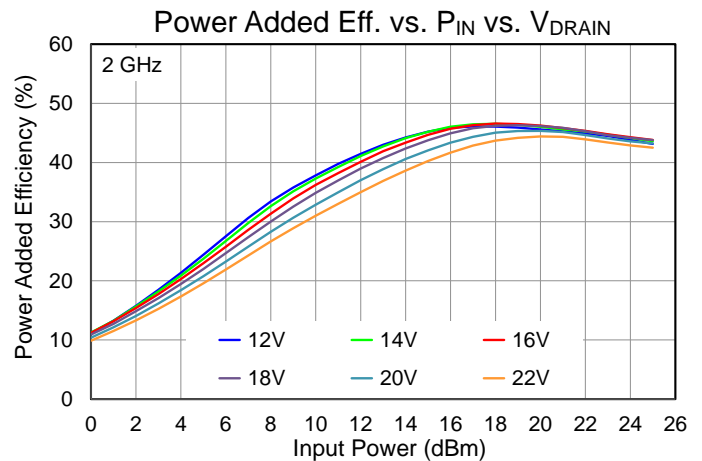
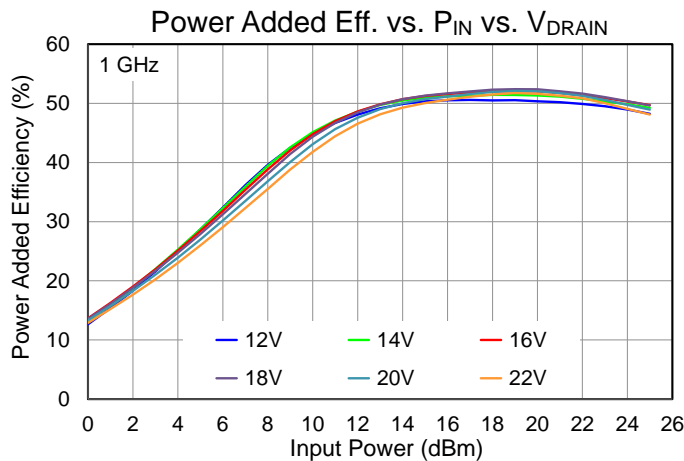
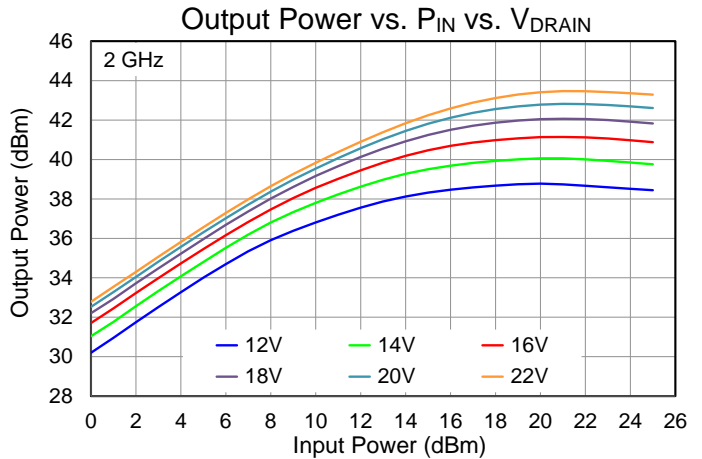
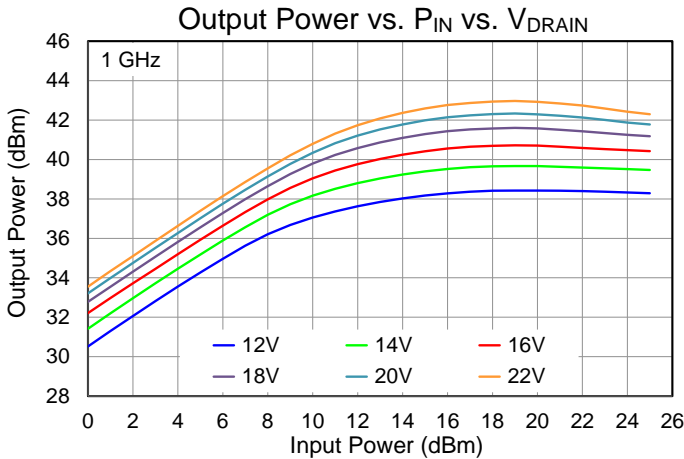
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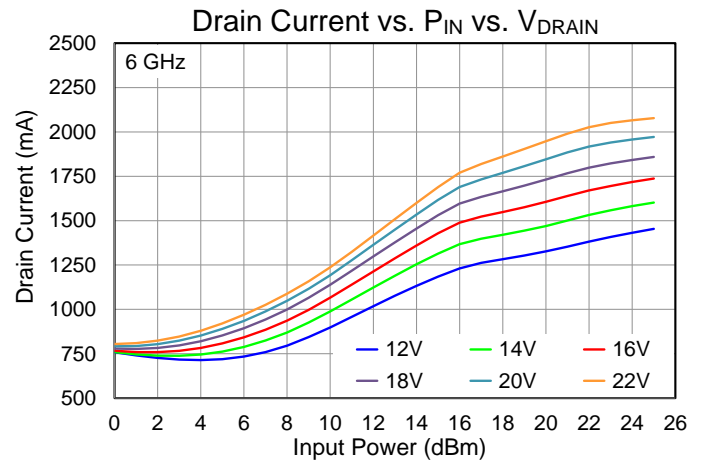
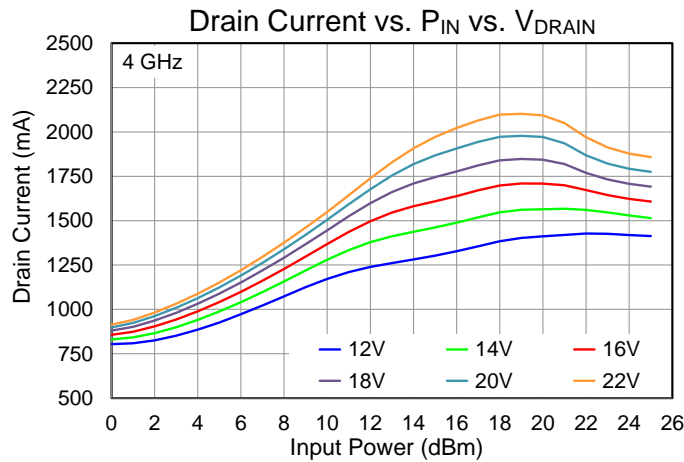
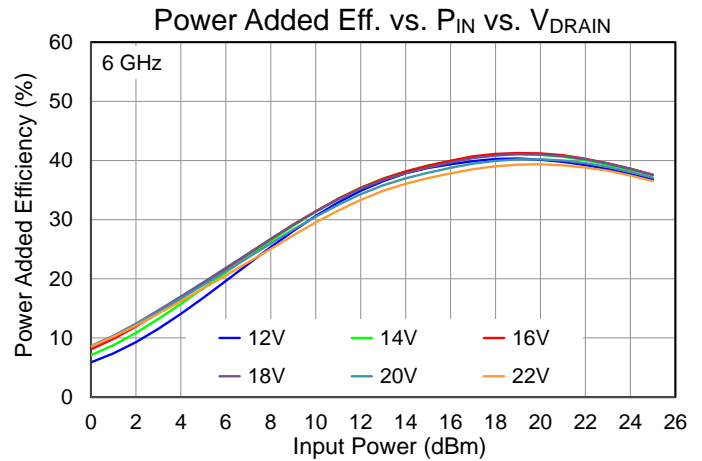
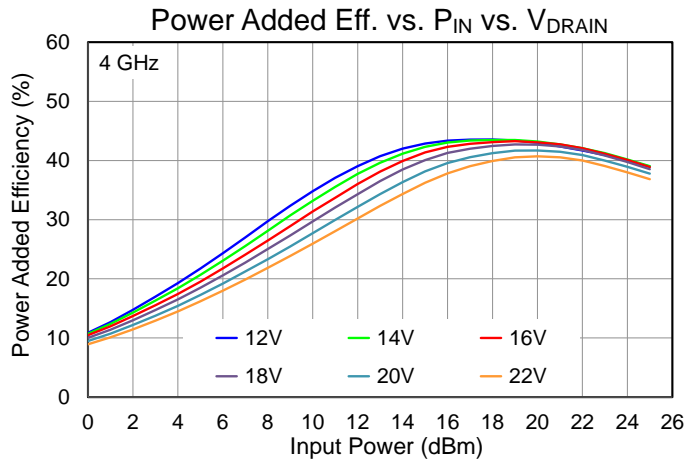
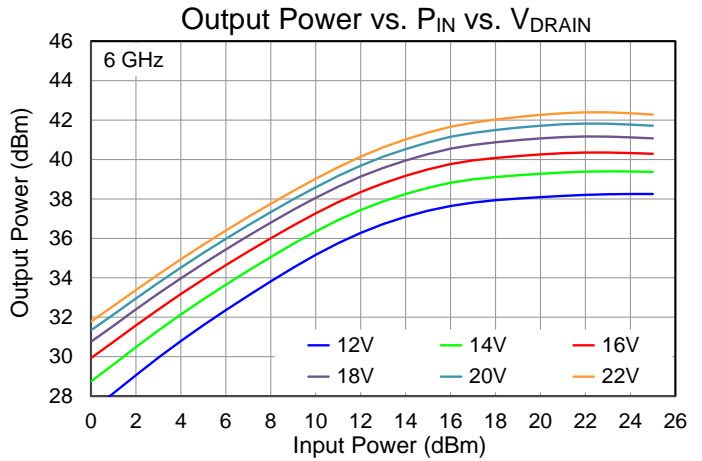
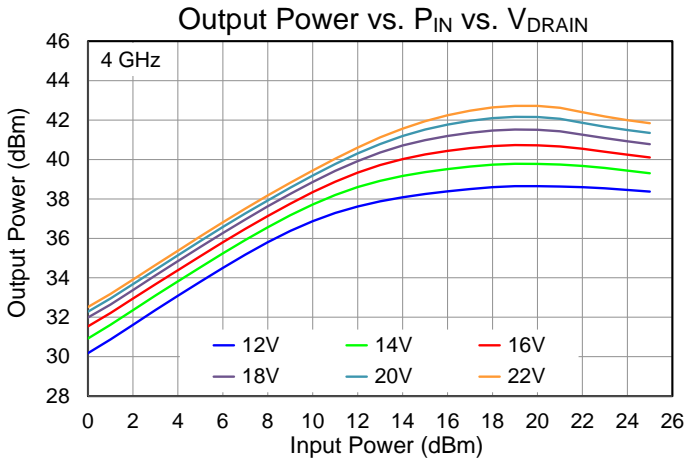
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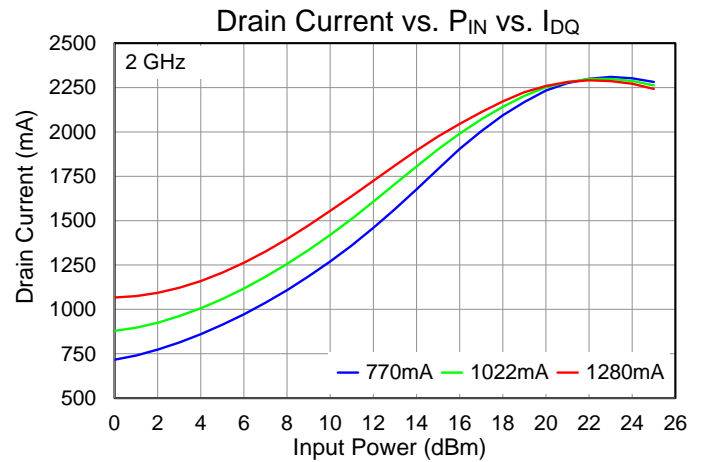
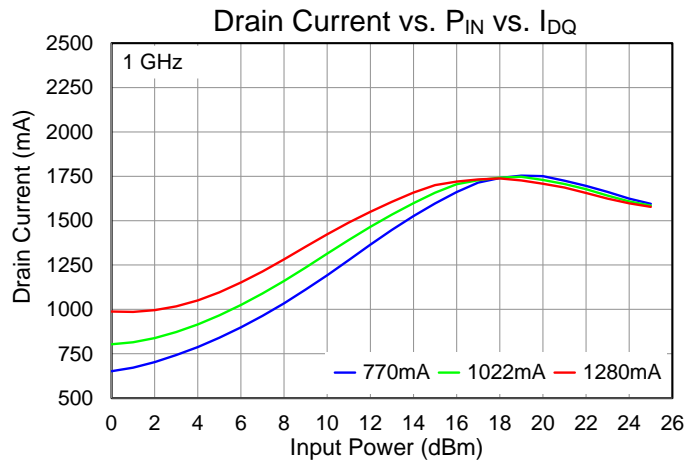
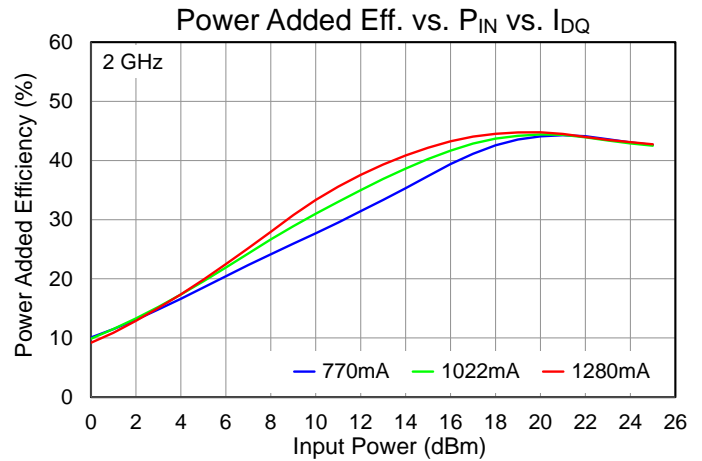
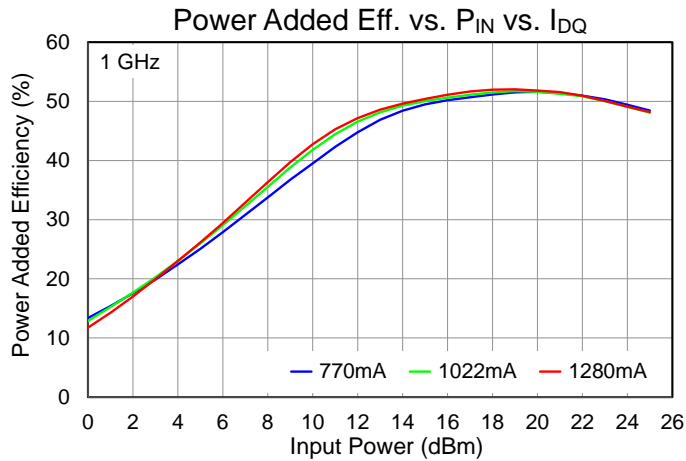
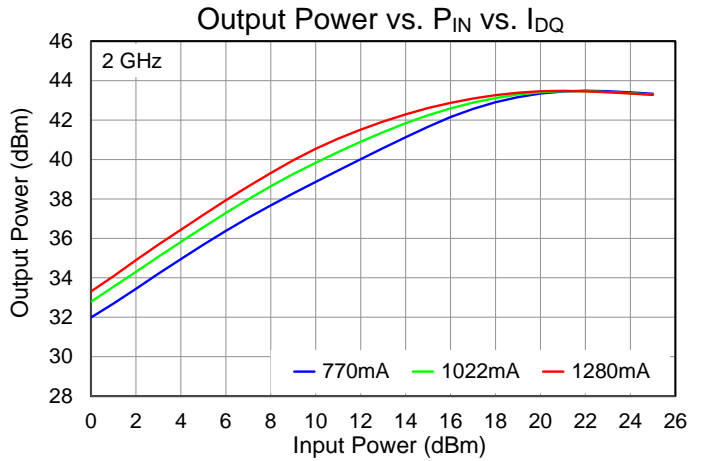
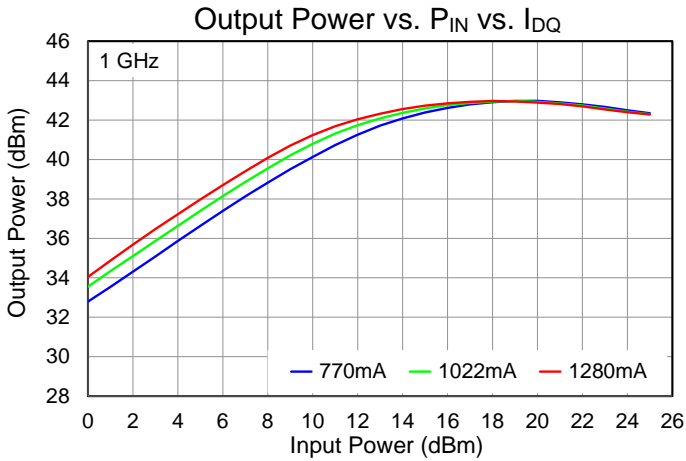
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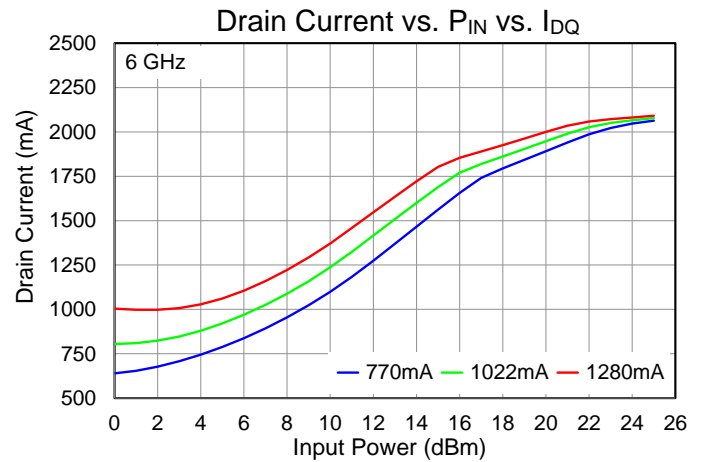
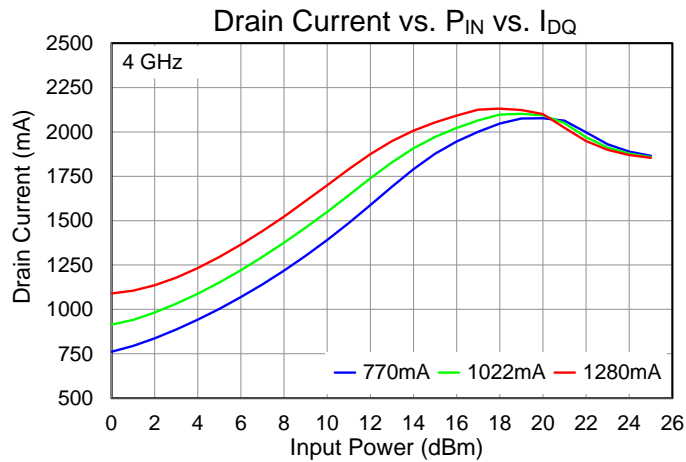
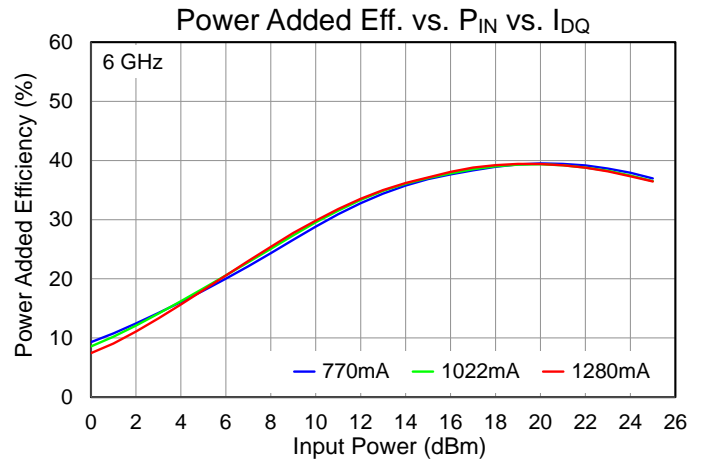
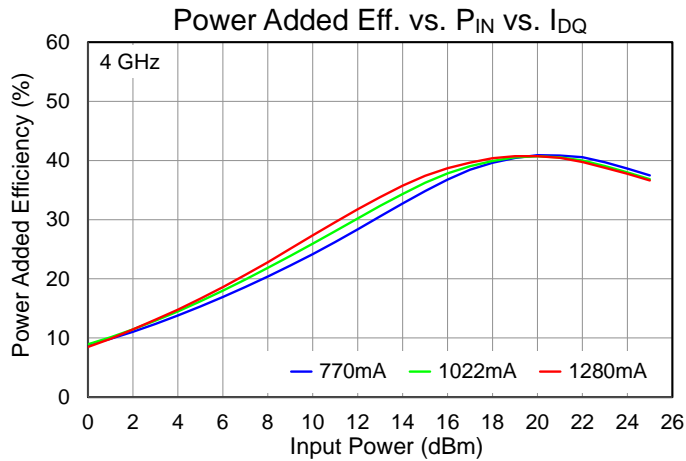
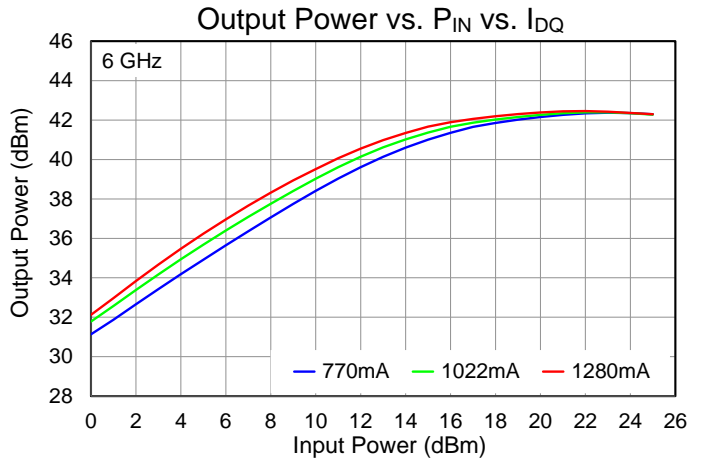
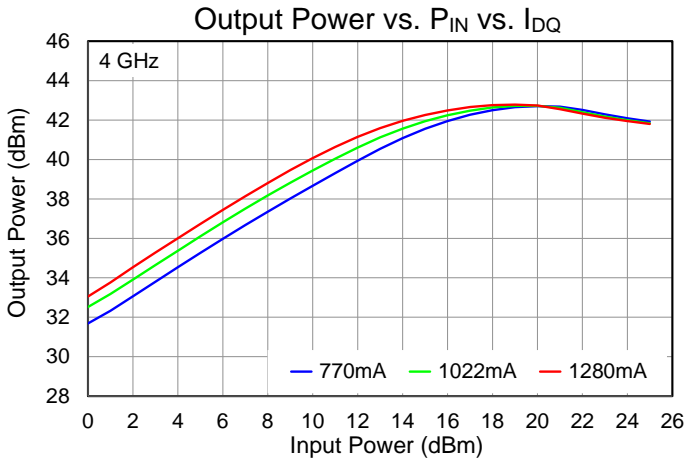
Performance Plots – Large Signal

Test conditions, unless otherwise noted: $V_D = 22\text{ V}$, $T = +25\text{ }^\circ\text{C}$, CW



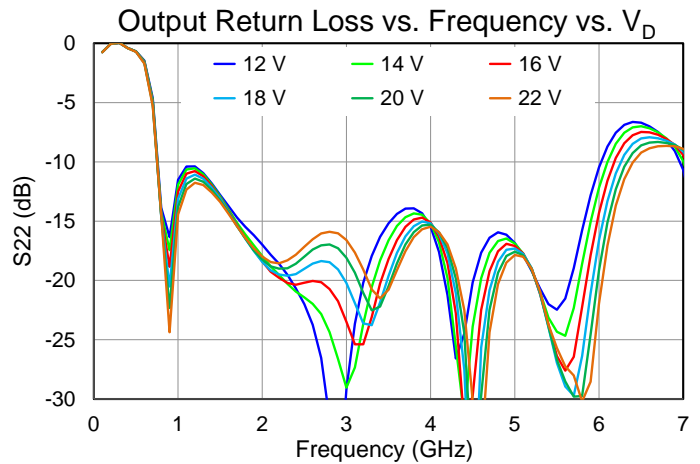
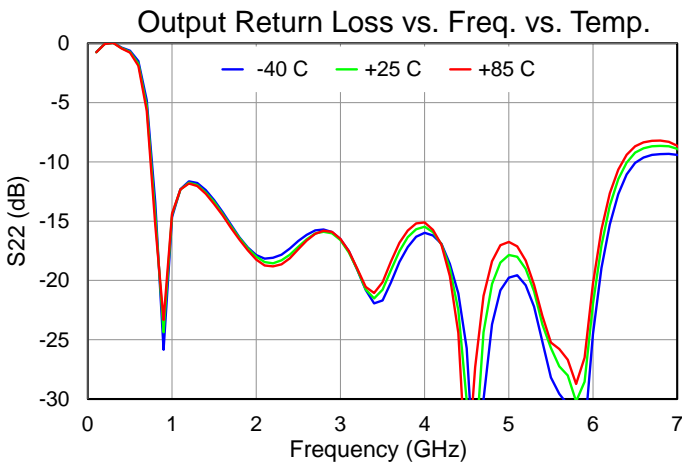
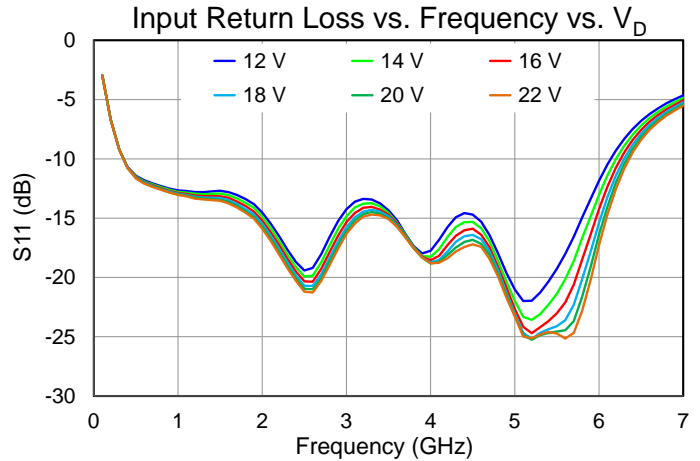
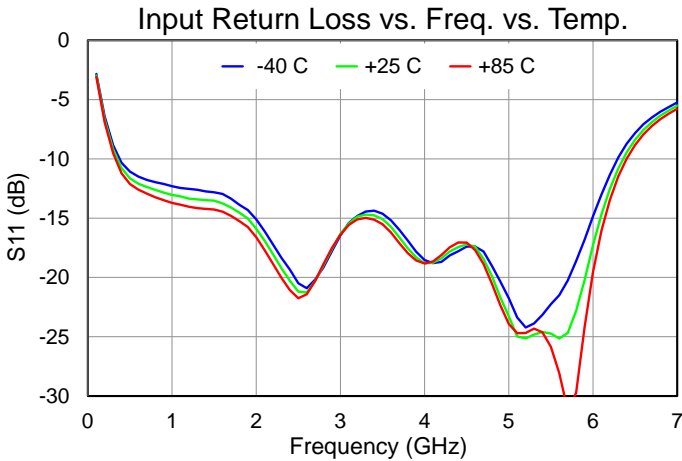
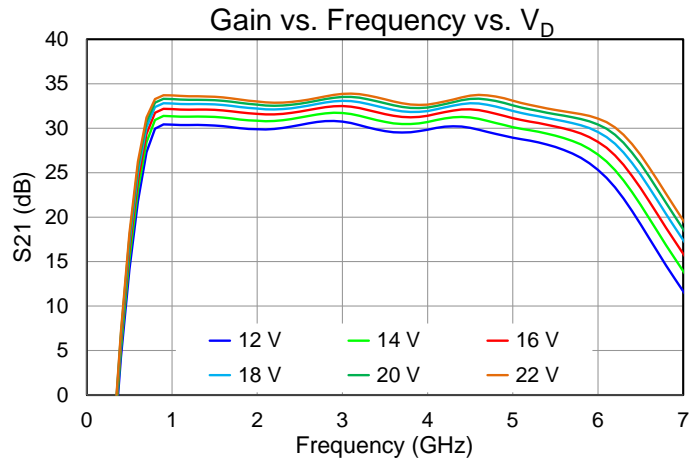
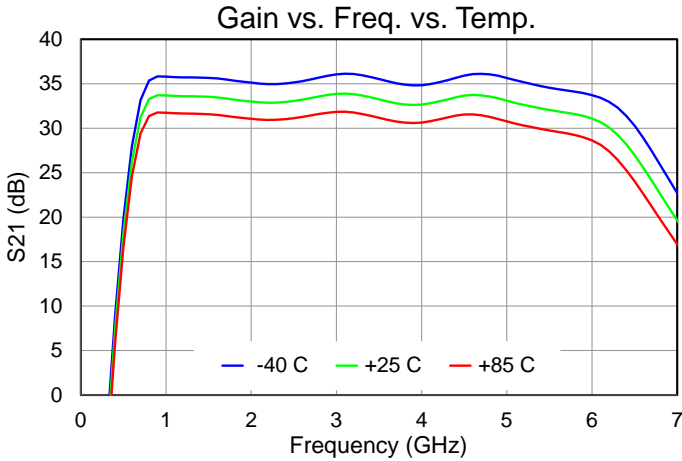
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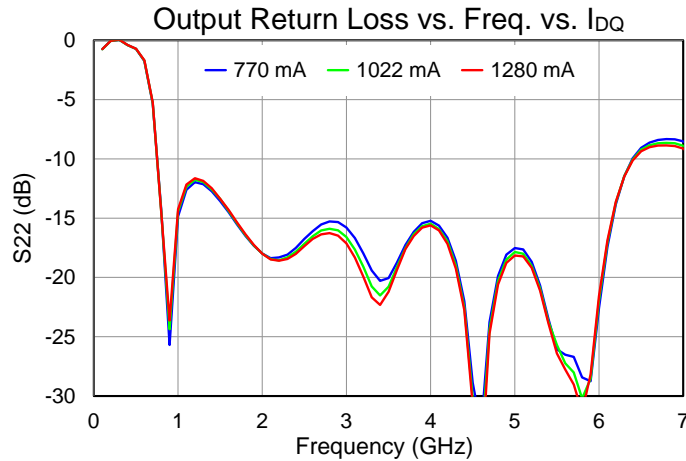
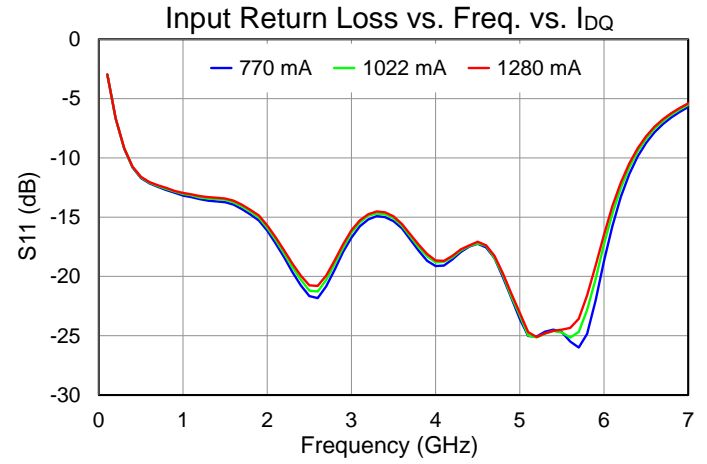
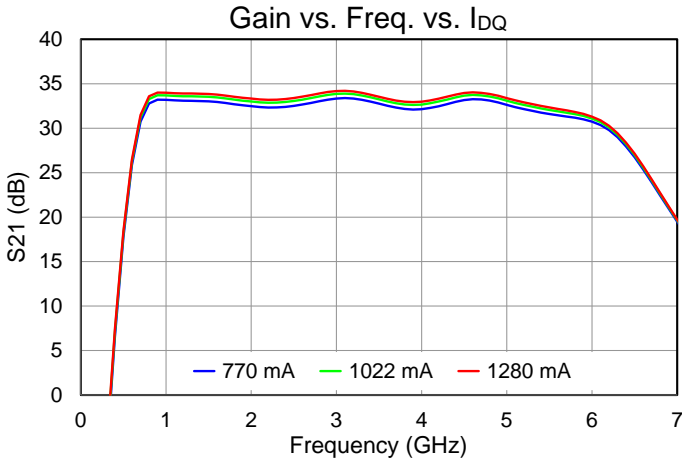
Performance Plots – Small Signal

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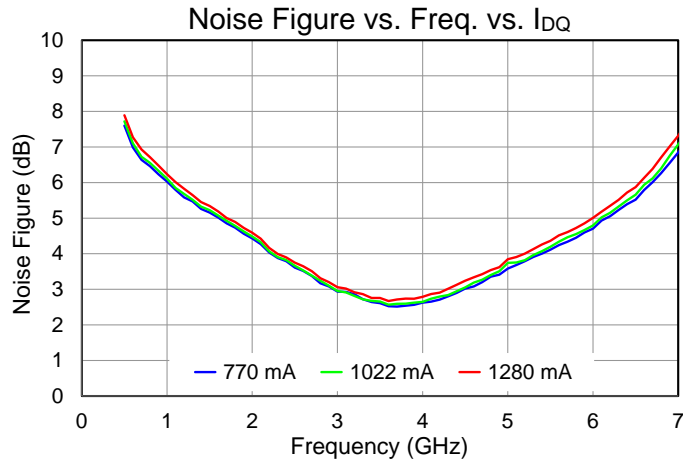
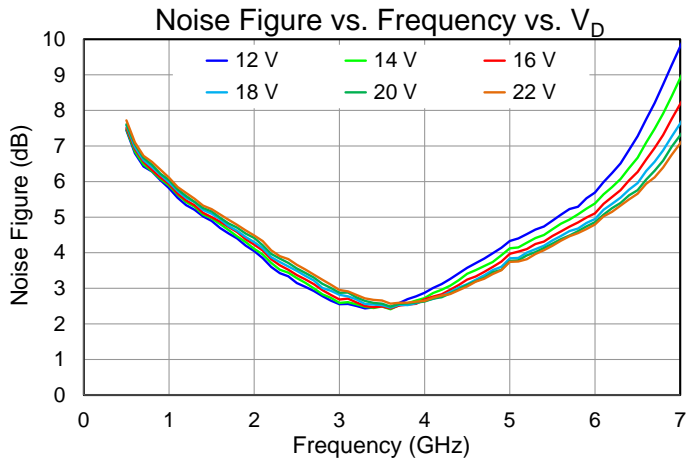
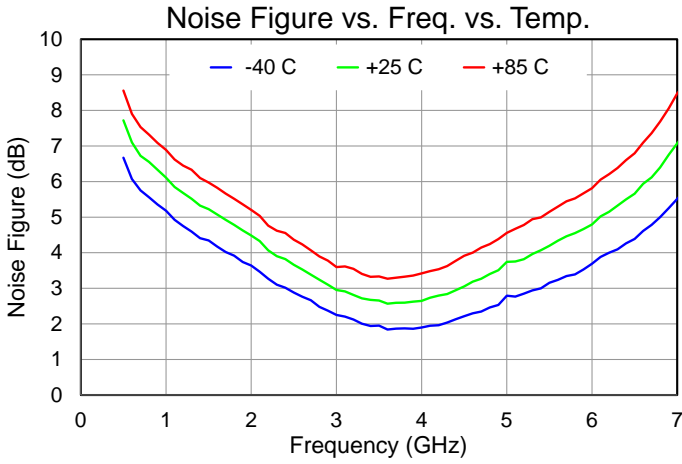
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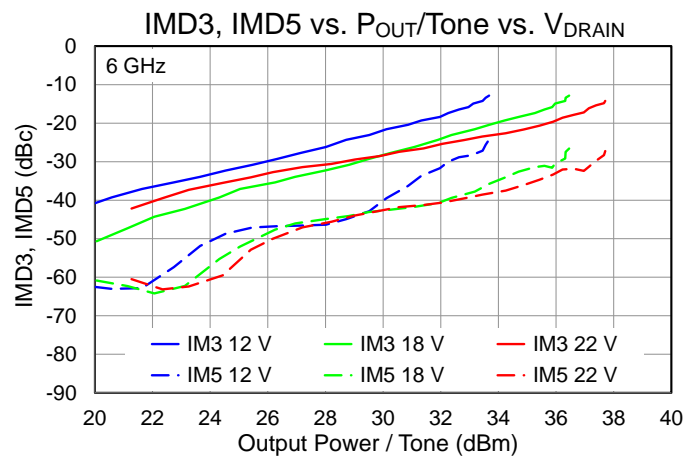
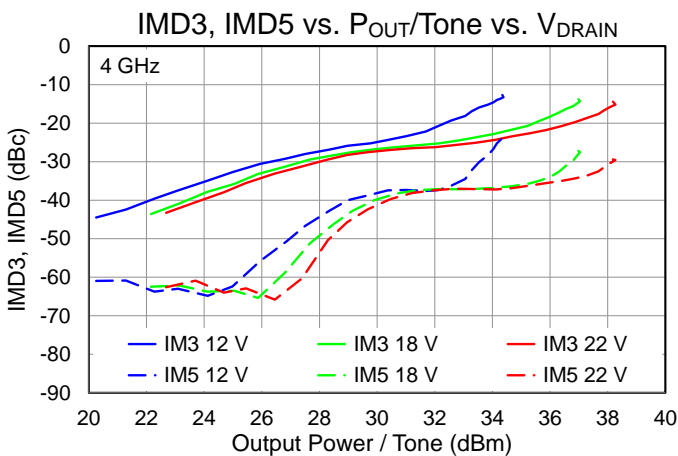
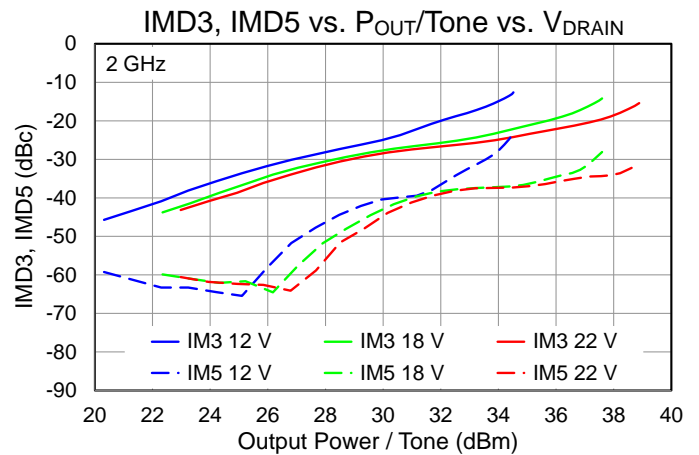
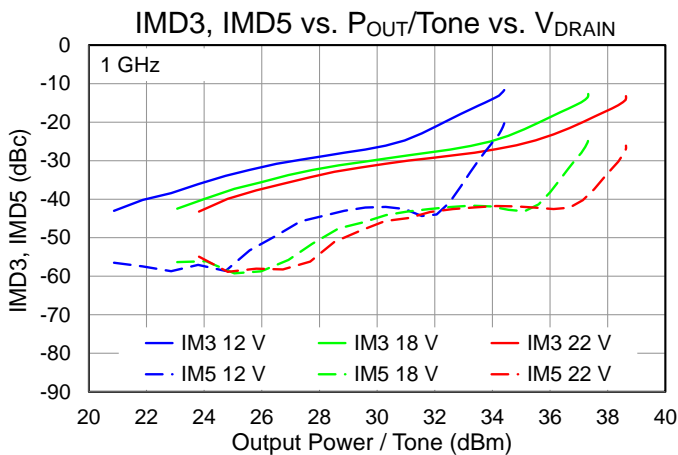
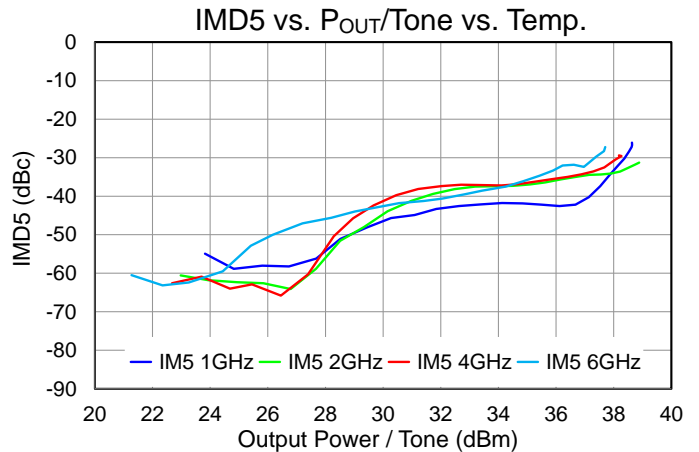
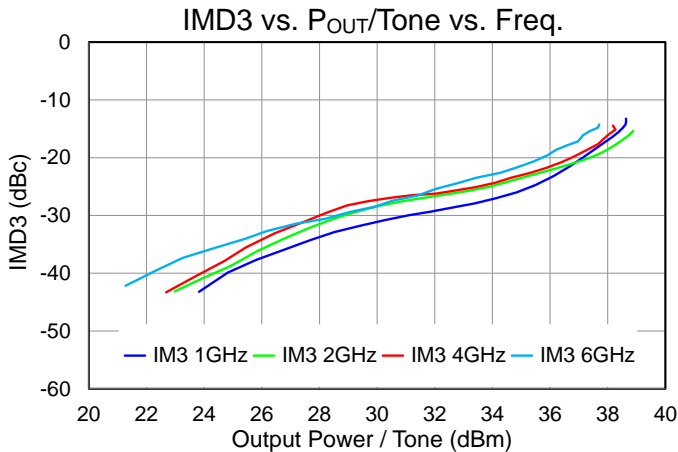
Performance Plots – Noise Figure

Test conditions, unless otherwise noted: $V_D = 22\text{ V}$, $I_{DQ} = 1022\text{ mA}$, $T = +25\text{ }^\circ\text{C}$, CW



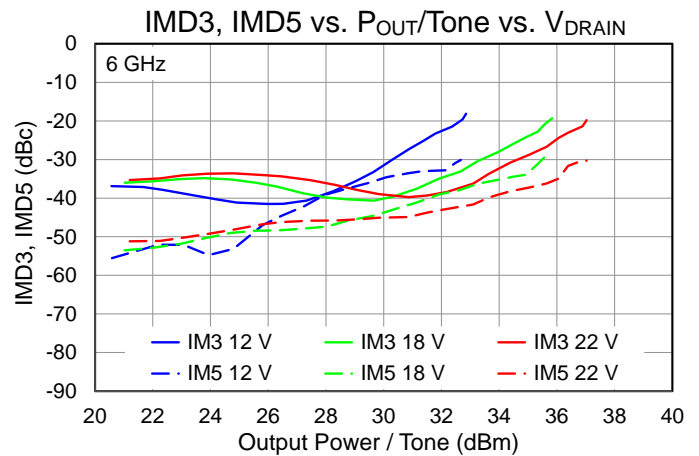
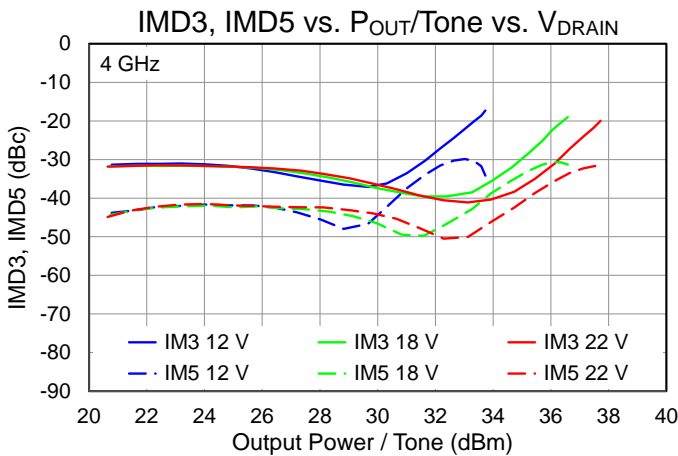
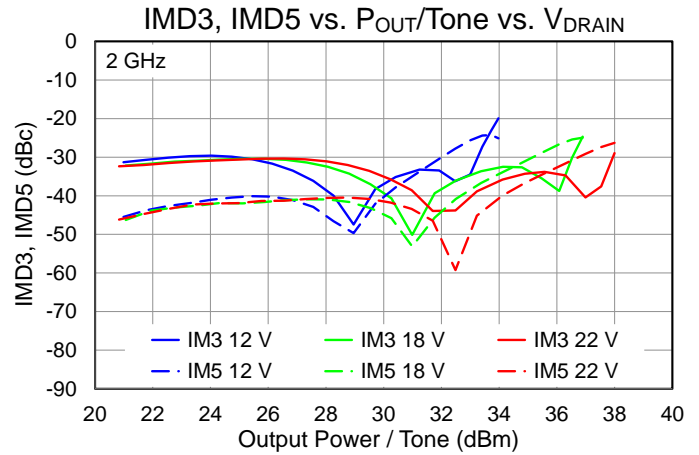
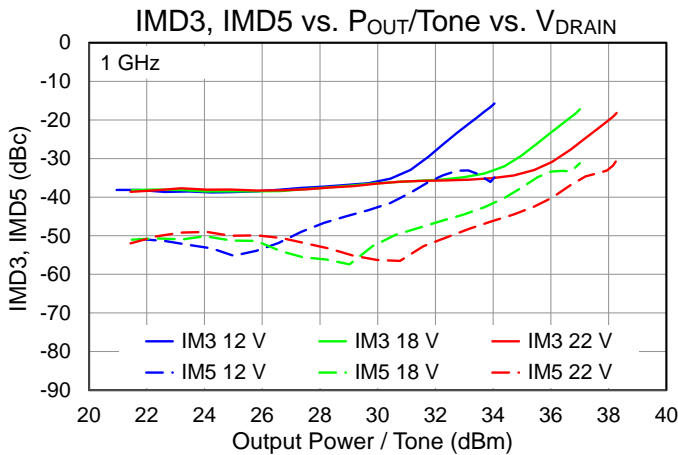
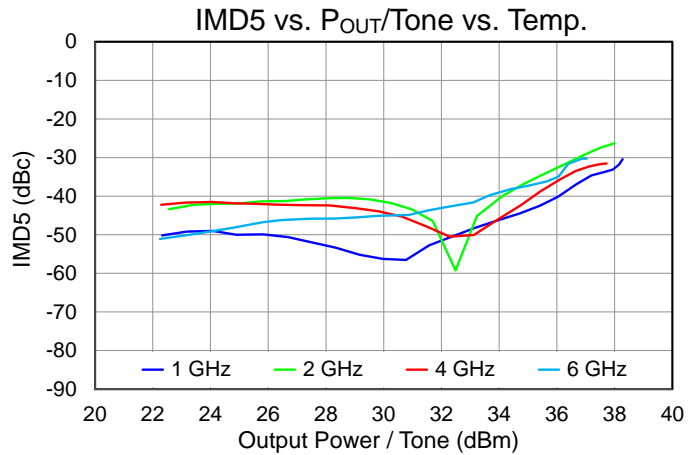
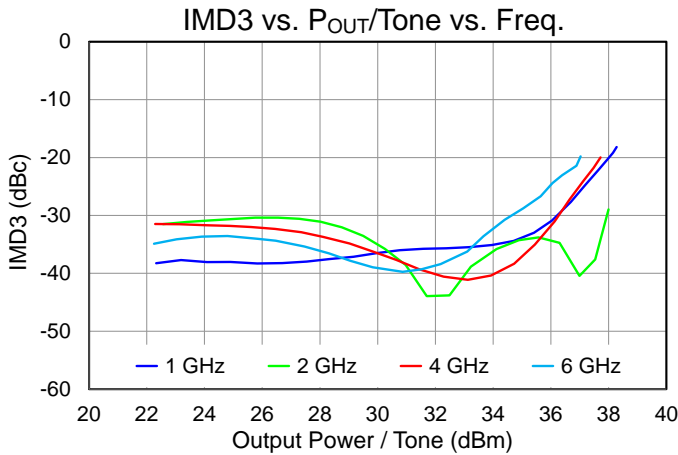
Performance Plots – Linearity

Test conditions, unless otherwise noted: $V_D = 22\text{ V}$, $I_{DQ} = 1022\text{ mA}$, $T = +25\text{ }^\circ\text{C}$, CW, Tone Spacing = 10 MHz



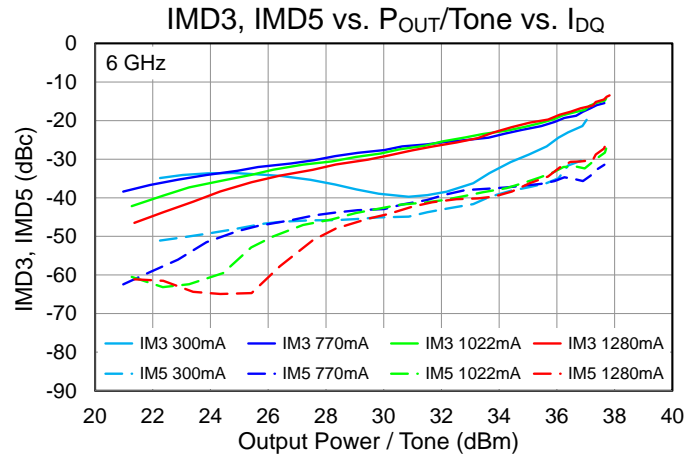
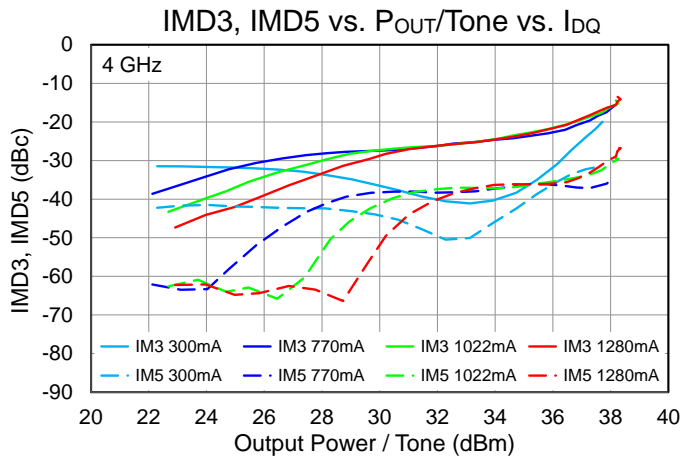
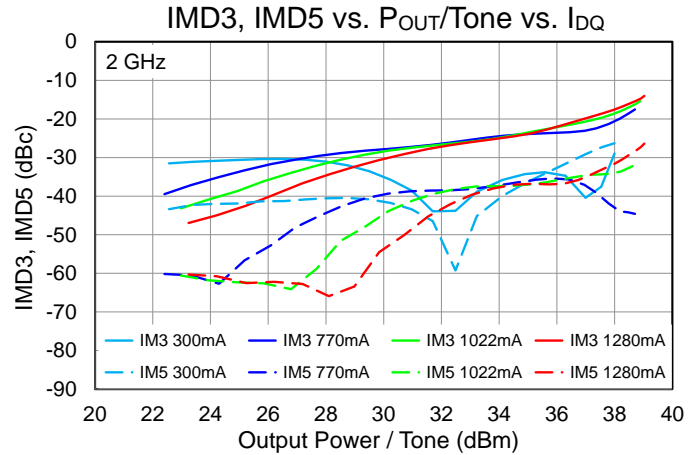
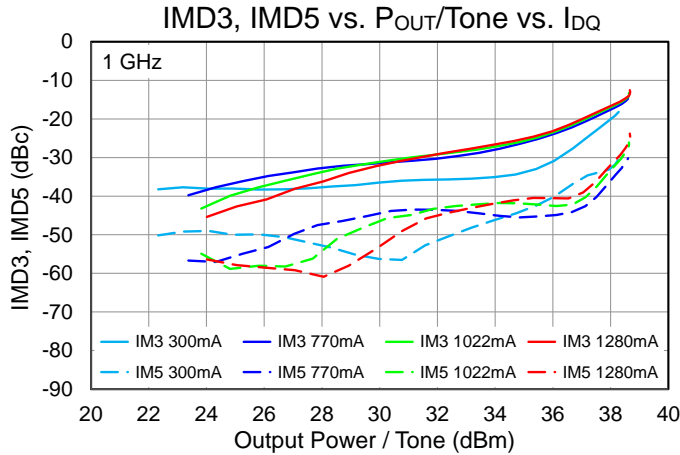
Performance Plots – Linearity

Test conditions, unless otherwise noted: $V_D = 22\text{ V}$, $I_{DQ} = 300\text{ mA}$, $T = +25^\circ\text{C}$, CW, Tone Spacing = 10 MHz



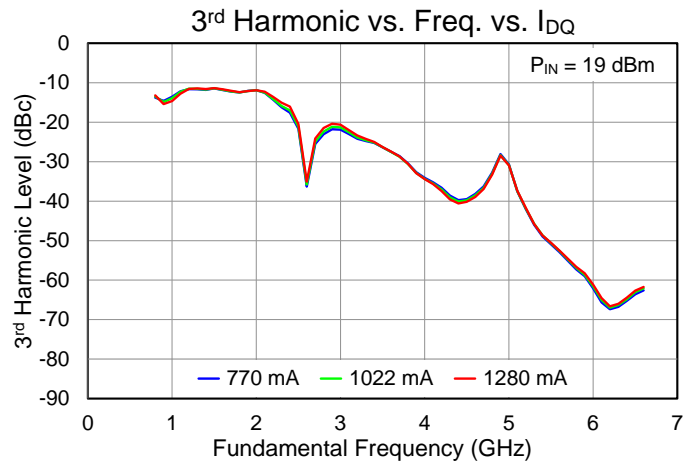
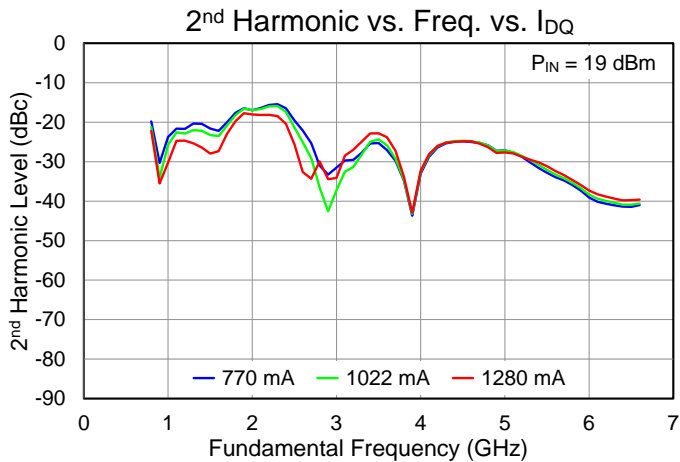
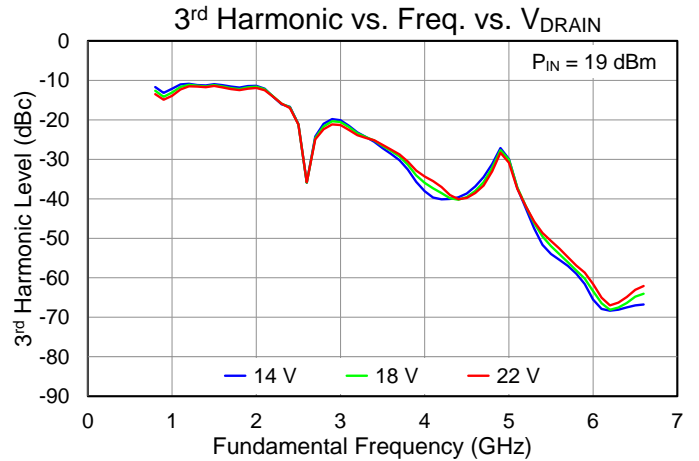
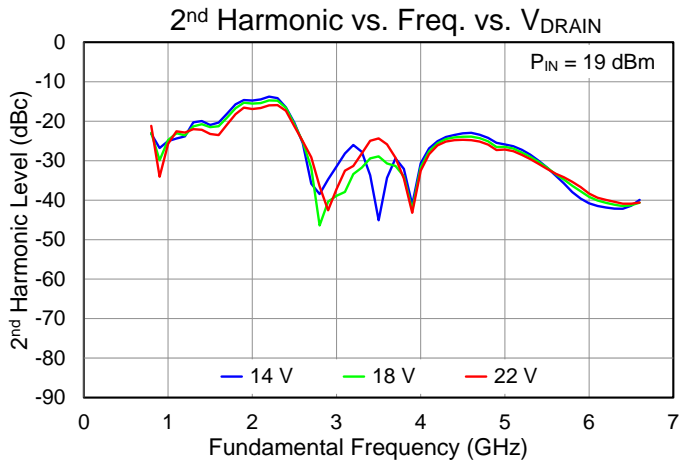
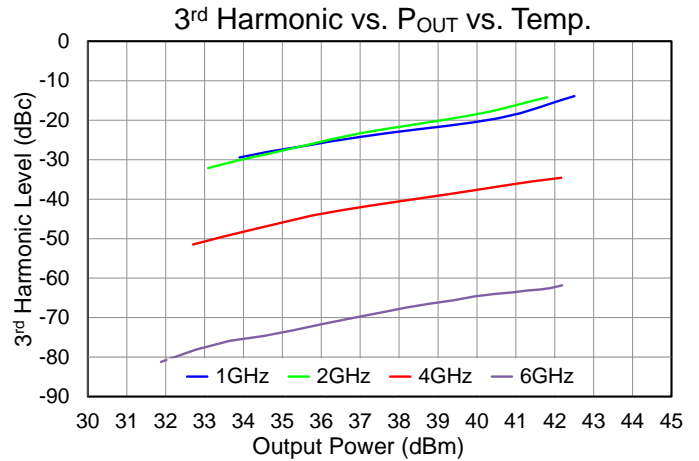
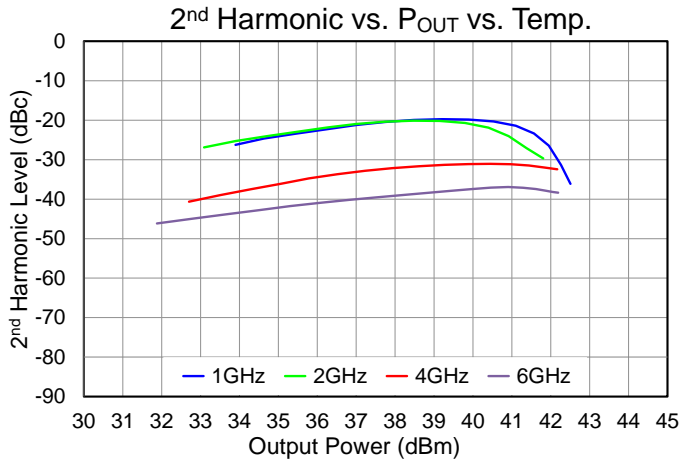
Performance Plots – Linearity

Test conditions, unless otherwise noted: $V_D = 22\text{ V}$, $I_{DQ} = 1022\text{ mA}$, $T = +25\text{ }^\circ\text{C}$, CW, Tone Spacing = 10 MHz



Performance Plots – Harmonics

Test conditions, unless otherwise noted: $V_D = 22\text{ V}$, $I_{DQ} = 1022\text{ mA}$, $T = +25\text{ }^\circ\text{C}$, CW



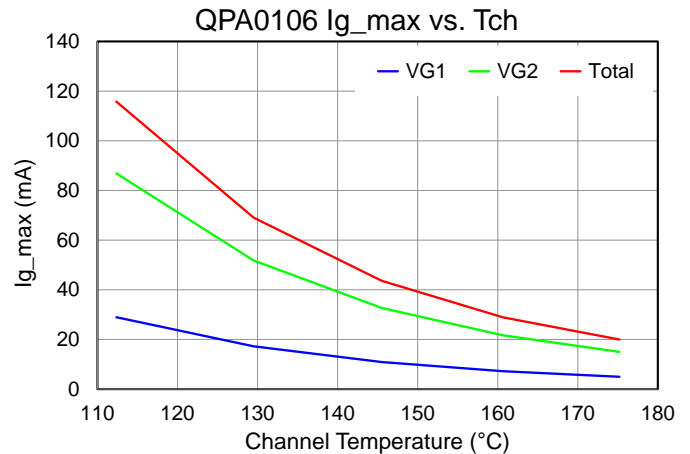
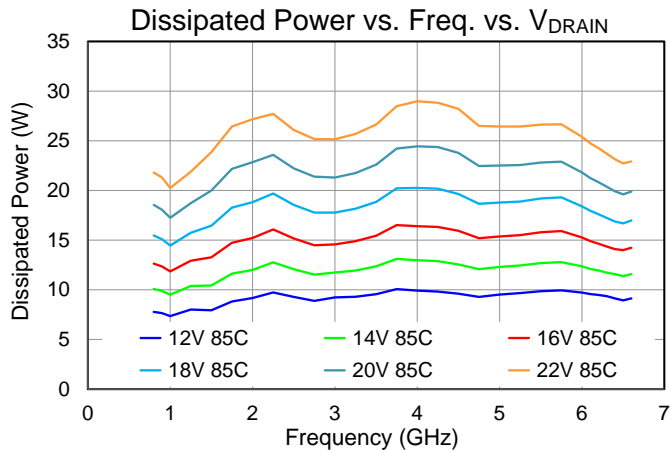
Thermal and Reliability Information

Parameter	Test Conditions	Value	Units
Thermal Resistance (θ_{JC}) ⁽¹⁾	$T_{base} = 85\text{ }^{\circ}\text{C}$, $V_D = 22\text{ V}$, $I_{DQ} = 1022\text{ mA}$, $P_{DISS} = 22.5\text{ W}$ (Quiescent; no RF drive)	1.569	$^{\circ}\text{C}/\text{W}$
Channel Temperature, T_{CH} (Quiescent) ⁽²⁾		120.3	$^{\circ}\text{C}$
Thermal Resistance (θ_{JC}) ⁽¹⁾	$T_{base} = 85\text{ }^{\circ}\text{C}$, $V_D = 22\text{ V}$, $I_{DQ} = 1022\text{ mA}$, Freq = 4.0 GHz, $I_{D_Drive} = 2088\text{ mA}$, $P_{IN} = 19\text{ dBm}$, $P_{OUT} = 42.2\text{ dBm}$, $P_{DISS} = 29.0\text{ W}$	1.770	$^{\circ}\text{C}/\text{W}$
Channel Temperature, T_{CH} (w/ RF drive) ⁽²⁾		136.3	$^{\circ}\text{C}$

Notes:

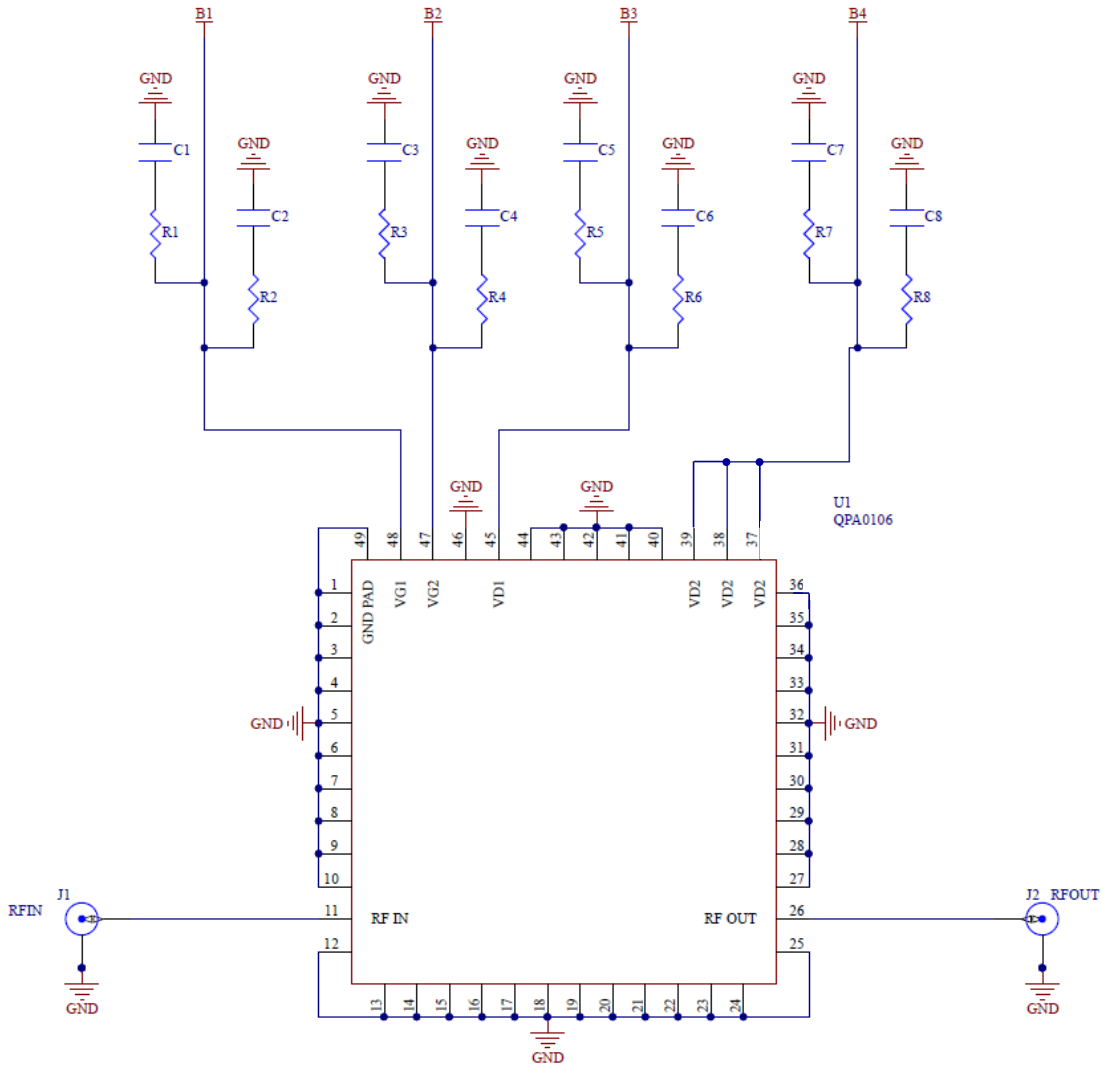
1. Thermal resistance determined to the back of package, T_{base} (85 $^{\circ}\text{C}$)
2. T_{CH} values are IR Scan equivalent temperatures. Refer to the following document: [GaN Device Channel Temperature, Thermal Resistance, and Reliability Estimates](#)

Dissipated Power and Maximum Gate Current



Test conditions, unless otherwise noted: $V_D = 22\text{ V}$, $I_{DQ} = 1022\text{ mA}$, $T = +25\text{ }^{\circ}\text{C}$, $P_{IN} = 19\text{ dBm}$

Applications Information



VG1 and VG2 should be tied together.
VD1 and VD2 should be tied together.

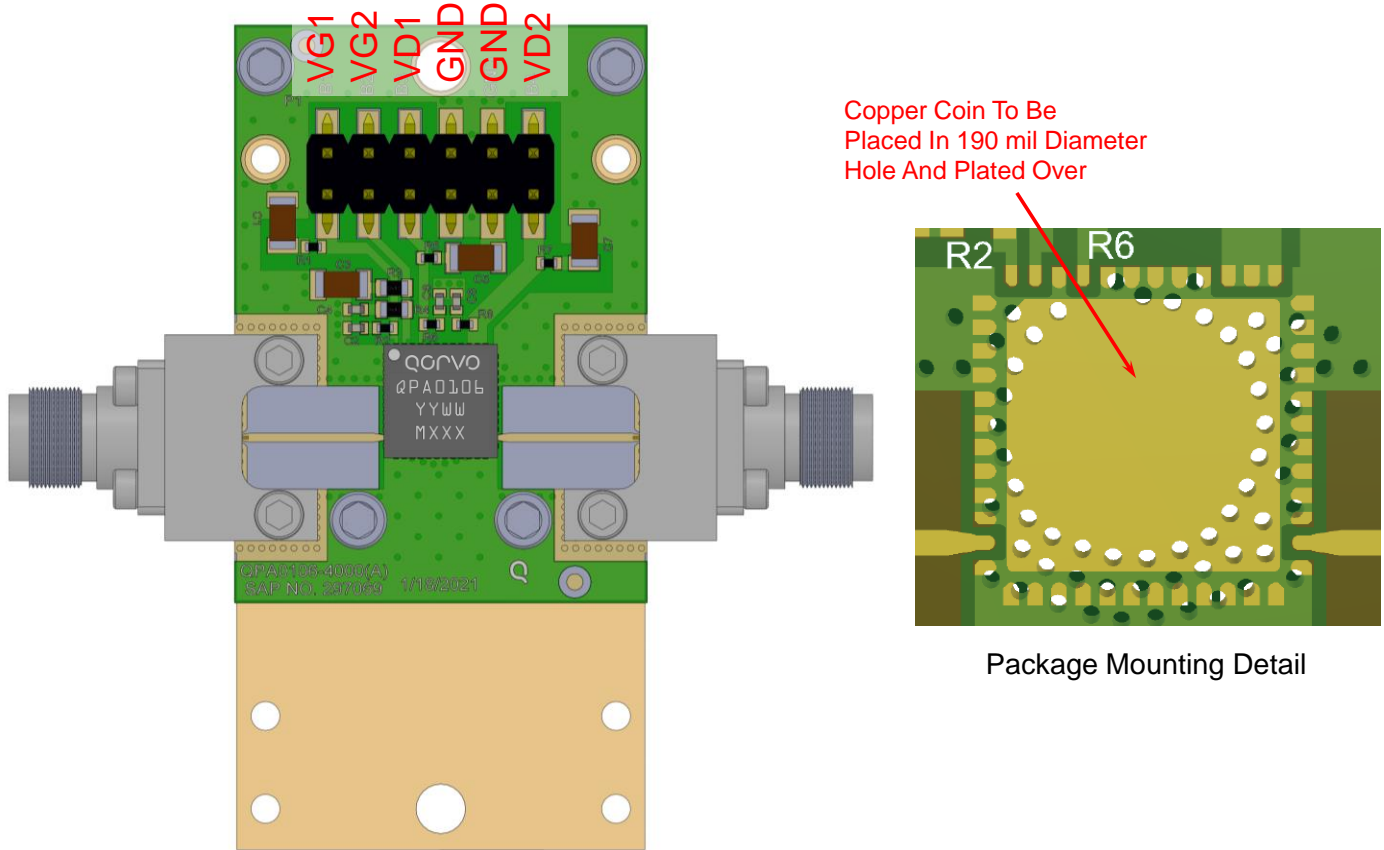
Bias-Up Procedure

- Turn on V_G supply and set $V_G = -4V$, I_G limit to 30 mA
- Turn on V_D supply and set $V_D = 0V$, I_D limit to 3.5 A
- Adjust V_D to 22 V
- Adjust V_G to obtain desired I_{DQ} (1022 mA)

Bias-Down Procedure

- Set $V_G = -4 V$
- Set $V_D = 0 V$
- Turn off V_D Supply
- Turn off V_G Supply

Evaluation Board (EVB) Layout Assembly

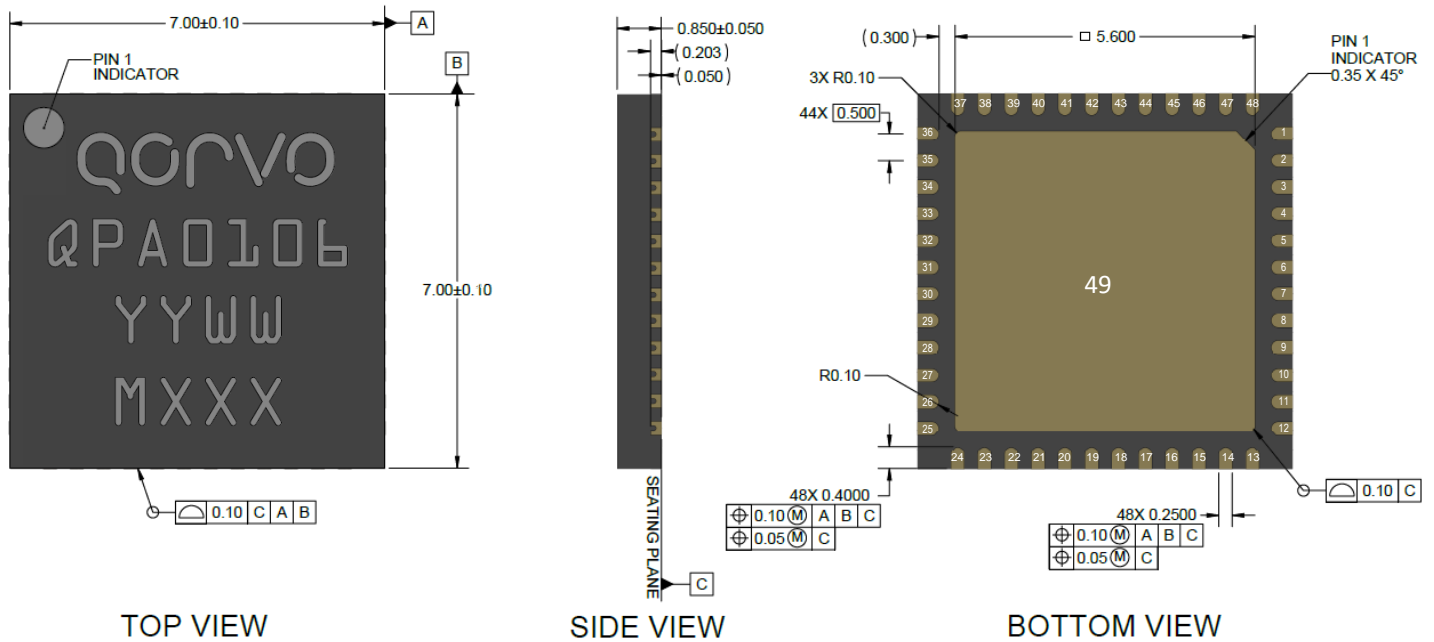


Multilayer PCB with copper coin for improved thermal management. Copper layers are 0.5 oz. both sides.
VG1 and VG2 should be tied together.
VD1 and VD2 should be tied together.

Bill of Materials

Reference Des.	Value	Description	Manuf.	Part No.
C2,C4,C6,C8	0.01 uF	CAP, 0.01uF, 10%, 50V, X7R, 0402	Various	
C1,C3,C5,C7	10 uF	CAP, 10uF, 20%, 50V, 20%, X5R, 1206	Various	
R4	0 ohm	RES, 0 ohm, 1/10W, 0603	Various	
R1	5.1 ohm	RES, 5.1 OHM, 5%, 50V, 0402	Various	
R3	5.1 ohm	RES, 5.1 OHM, 1%, 1/10W, 0603	Various	
R2,R6,R5,R7,R8	0 ohm	RES, 0 OHM, 5%, 1/10W, 0402	Various	
J1, J2	2.92 mm	CONN, 2.92, END, F, PIN .007, DIEL .048	Southwest Microwave	1092-02A-5

Mechanical Information



Notes:

Material:

1. Package Lid: plastic overmold compound
2. All package leads are gold plated
3. The part is mold encapsulated

Tolerances:

- .XX = $\pm .25$
- .XXX = $\pm .100$
- .XXXX = $\pm .0245$

Unless otherwise specified, dimensions are in mm

Bond Pad Description

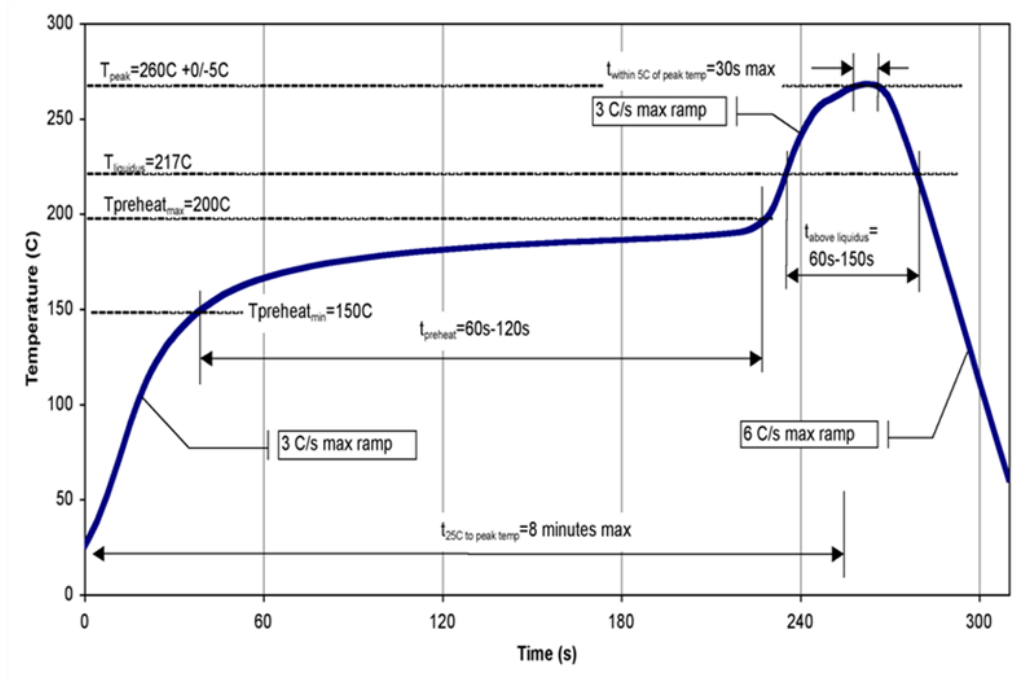
Pad No.	Symbol	Description
1-10, 12-25, 27-36, 40-44, 46	NC/GND	No internal connection. May be connected to PCB ground
11	RF IN	RF input. 50 Ohms. DC blocked
26	RF OUT	RF output. 50 Ohms. DC blocked
37-39	VD2	Second stage drain voltage
45	VD1	First stage drain voltage
47	VG2	Second stage gate voltage
48	VG1	First stage gate voltage
49	GND	Center pad (49) must be tied to PCB ground for electrical and thermal performance

Assembly Notes

Compatible with lead-free soldering processes with 260°C peak reflow temperature.

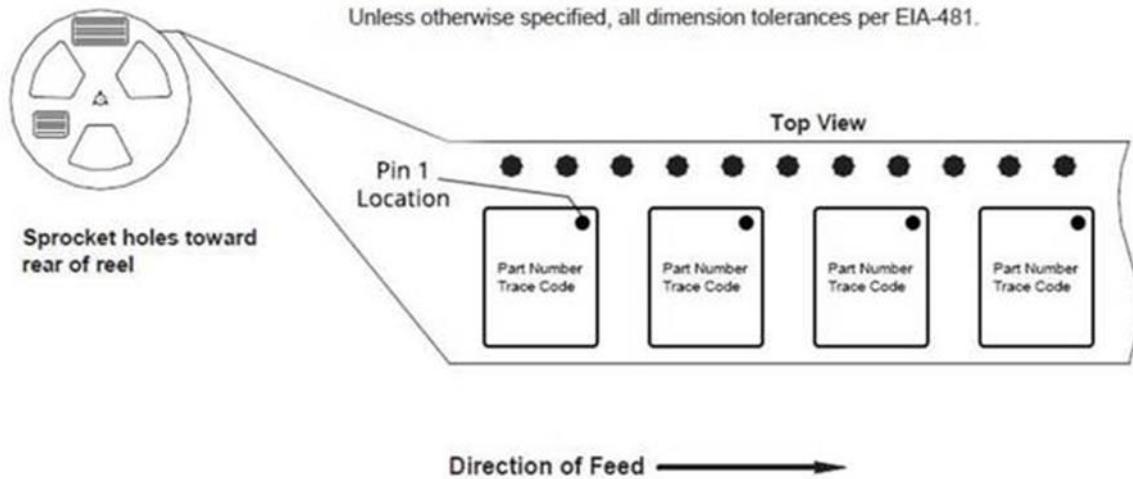
All package leads are gold plated

Solder rework is not recommended



Tape and Reel Information – Carrier and Cover Tape Dimensions

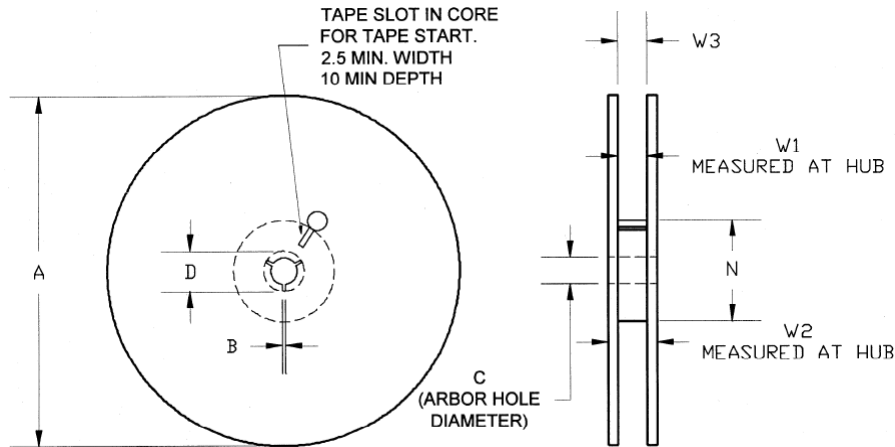
Tape and reel specifications for this part are also available on the Qorvo website.
 Standard T/R size = 250 pieces on a 7" reel.



Feature	Measure	Symbol	Size (in)	Size (mm)
Cavity	Length	A0	0.285	7.25
	Width	B0	0.285	7.25
	Depth	K0	0.043	1.1
	Pitch	P1	0.472	12
Centerline Distance	Cavity to Perforation - Length Direction	P2	0.079	2.0
	Cavity to Perforation - Width Direction	F	0.295	7.5
Cover Tape	Width	C	0.524	13.3
Carrier Tape	Width	W	0.630	16.0

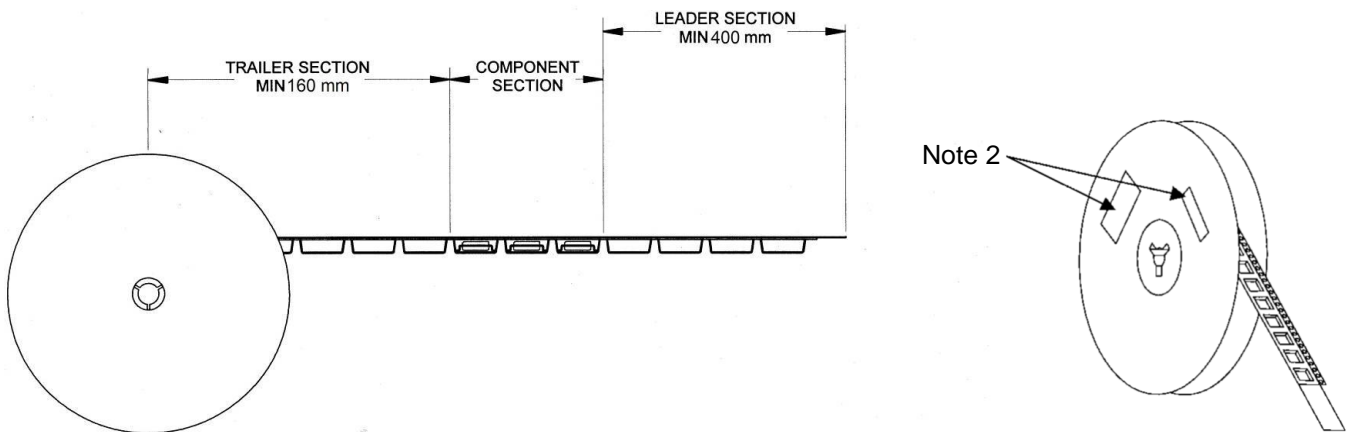
Tape and Reel Information – Reel Dimensions

Packaging reels are used to prevent damage to devices during shipping and storage, loaded carrier tape is typically wound onto a plastic take-up reel. The reel size is 7" diameter. The reels are made from high-impact injection-molded polystyrene (HIPS), which offers mechanical and ESD protection to packaged devices.



Feature	Measure	Symbol	Size (in)	Size (mm)
Flange	Diameter	A	6.969	177.0
	Thickness	W2	0.881	22.4
	Space Between Flange	W1	0.646	16.4
Hub	Outer Diameter	N	2.283	58.0
	Arbor Hole Diameter	C	0.512	13.0
	Key Slit Width	B	0.079	2.0
	Key Slit Diameter	D	0.795	20.2

Tape and Reel Information – Tape Length and Label Placement



Notes:

1. Empty part cavities at the trailing and leading ends are sealed with cover tape. See EIA 481.
2. Labels are placed on the flange opposite the sprockets in the carrier tape.

Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	1A	ESDA / JEDEC JS-001-2017
ESD – Charge Device Model (CDM)	C3	ESDA / JEDEC JS-002-2018
MSL – Moisture Sensitivity Level	MSL3	JEDEC standard IPC/JEDEC J-STD-020



Caution!
ESD-Sensitive Device

RoHS Compliance

This product is compliant with the 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment), as amended by Directive 2015/863/EU. This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C15H12Br4O2) Free
- PFOS Free
- SVHC Free

Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

Tel: 1-844-890-8163

Web: www.qorvo.com

Email: customer.support@qorvo.com

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