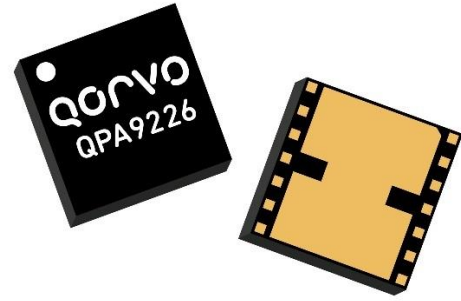


### General Description

The QPA9226 is a high-linearity three-stage power amplifier in a low-cost surface-mount package with on-chip bias control and temperature control circuits, suitable for small cell or enterprise Femto cell base station applications.

The QPA9226 provides 34 dB high gain and -50 dBc ACLR at +24 dBm linear power using a 20 MHz LTE signal over the 2.5–2.7 GHz frequency range covering 3GPP Bands 7, 38, 41.

The QPA9226 integrates three high performance amplifier stages to allow for a compact system design and requires very few external components for operation. The amplifier is bias adjustable allowing the amplifier's power consumption to be optimized for specific performance requirements. The QPA9226 is available in a RoHS-compliant 7 x 7 mm surface mount package.

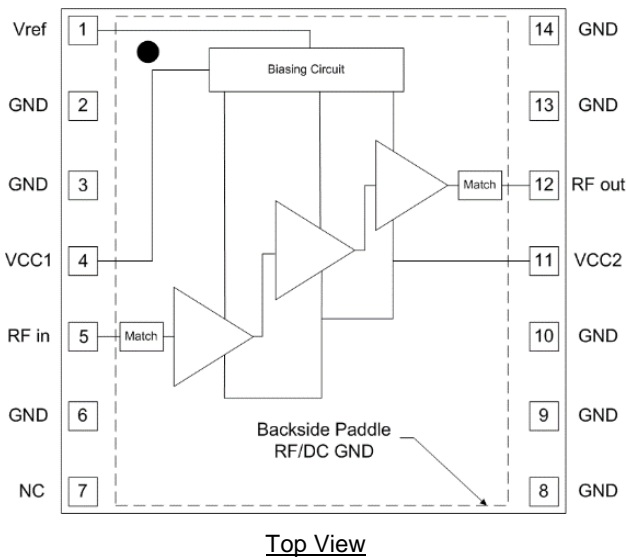


14 Pin 7 x 7 mm Leadless SMT Package

### Product Features

- 2.5–2.7 GHz Frequency Range
- Fully integrated, 3-Stage Power Amplifier
- Internally Matched 50 Ω Input/Output
- -50 dBc ACLR at  $P_{avg} = +24$  dBm
- 34 dB Gain
- 14% PAE at +24 dBm
- 198 mA Quiescent Current
- On-chip Control Bias and Temp. Comp Circuit
- RoHS compliant
- Covers Bands 7, 38, 41

### Functional Block Diagram



### Applications

- Small Cell / Picocell
- Enterprise Femtocell
- Customer Premises Equipment (CPE)
- Data Cards and Terminals
- Distributed Antenna Systems (DAS)
- Booster Amps, Repeaters

### Ordering Information

Part No.	Description
QPA9226TR13	2,500 pieces on a 13" reel (standard)
QPA9226SR	100 pieces on a 7" reel
QPA9226PCB401	QPA9226 Evaluation Board

### Absolute Maximum Ratings

Parameter	Rating
Storage Temperature	-55 to +150 °C
RF Input Power, CW, 50Ω, T=+25 °C	+9 dBm
Supply Voltage (V <sub>CC</sub> )	6 V
V <sub>REF</sub>	+3.5 V

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability.

### Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
V <sub>CC1</sub> , V <sub>CC2</sub>	+3.6	+4.5	+5.25	V
V <sub>ref</sub>	+2.75	+2.85	+2.95	V
T <sub>CASE</sub>	-40		+85	°C
T <sub>j</sub> at T <sub>CASE</sub> max			+218	°C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

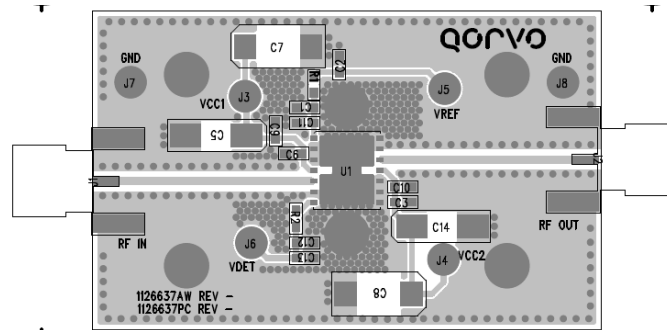
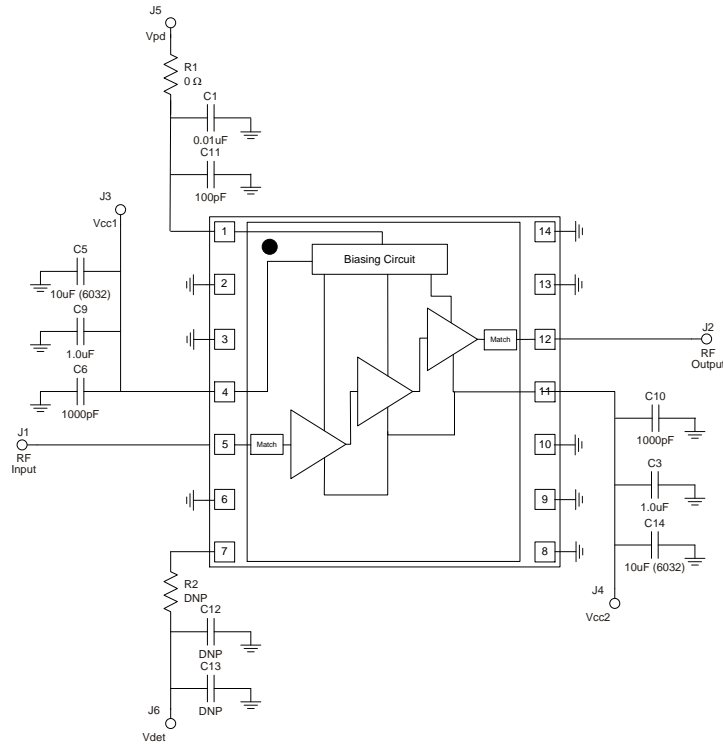
### Electrical Specifications

Parameter	Conditions <sup>(1)</sup>	Min	Typ	Max	Units
Frequency Range		2500		2700	MHz
Test Frequency			2600		MHz
Gain		31	34		dB
Input Return Loss			12		dB
Output Return Loss			7.0		dB
Output P1dB			33.5		dBm
ACLR	P <sub>OUT</sub> = +24 dBm, 20 MHz LTE E-TM1.1, 9.5 dB PAR		-50	-47	dBc
Power Added Efficiency	P <sub>OUT</sub> = +24 dBm, 20 MHz LTE E-TM1.1, 9.5 dB PAR	12.5	14		%
Spurious Output Level	P <sub>OUT</sub> = +24 dBm, 10:1 VSWR		58		dBc
VSWR survivability	No permanent degradation or failure	10:1			-
Quiescent Current, I <sub>CQ</sub>	V <sub>CC1</sub> + V <sub>CC2</sub>	180	198	250	mA
Reference Current, I <sub>ref</sub>	Temp = -40°C to +85°C, V <sub>REF</sub> = +2.85V		6	8	mA
Leakage Current	V <sub>CC</sub> = +4.5 V, V <sub>REF</sub> = 0 V		1.5	5	μA
Operational Current, I <sub>CC</sub>	P <sub>OUT</sub> = +24 dBm		398	450	mA
Switching Speed	0% V <sub>ref</sub> to 90% RF Rise time		1.7	2.5	μs
	100% V <sub>ref</sub> to 10% RF Fall time		0.87	1	μs
Harmonics	2F <sub>0</sub> at +24dBm, CW signal		-32	-28	dBc
	3F <sub>0</sub> at +24dBm, CW signal		-38	-33	dBc
	4F <sub>0</sub> at +24dBm, CW signal		-66	-61	dBc
Thermal Resistance, θ <sub>jc</sub>	Module (junction to case)			34.8	°C/W

Notes:

1. Test conditions unless otherwise noted: V<sub>CC1</sub> = V<sub>CC2</sub> = +4.5 V, V<sub>REF</sub> = +2.85V, Temp = +25 °C, 50 Ω system.

### Evaluation Board

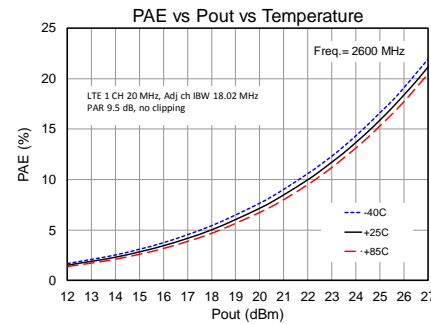
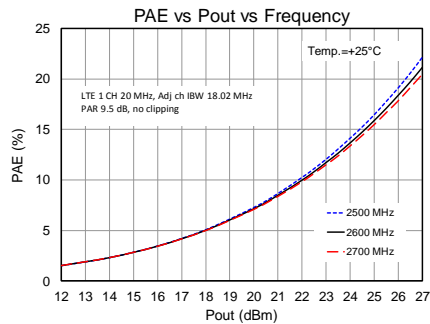
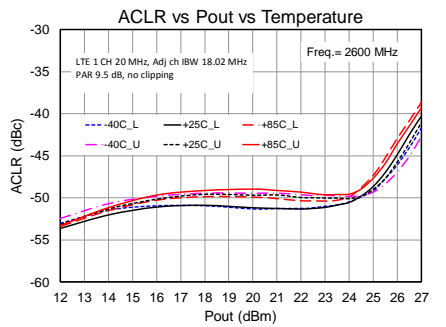
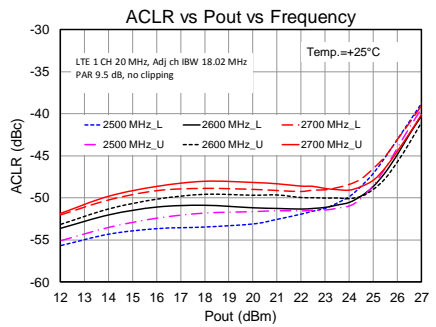
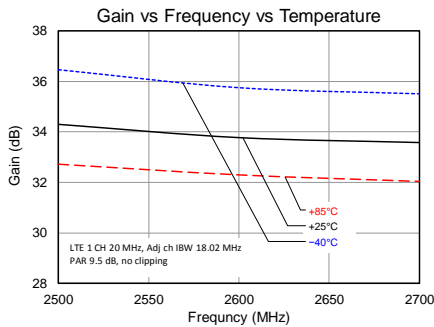
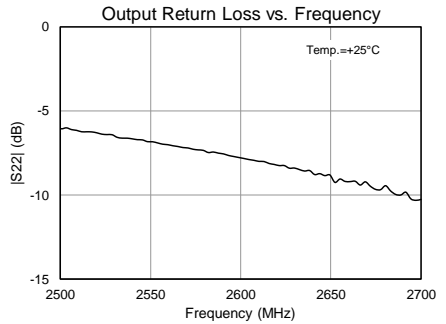
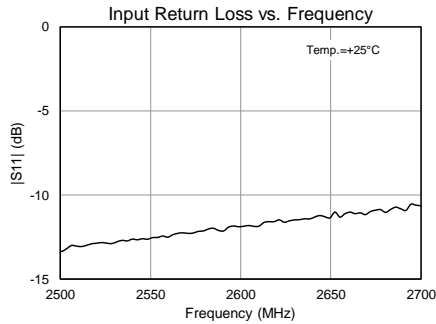
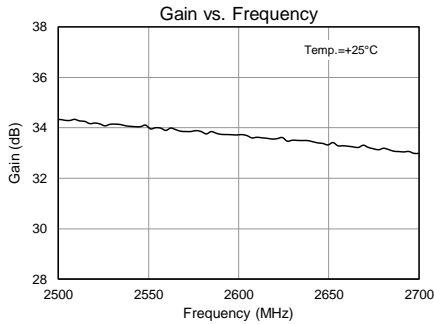


### Bill of Material

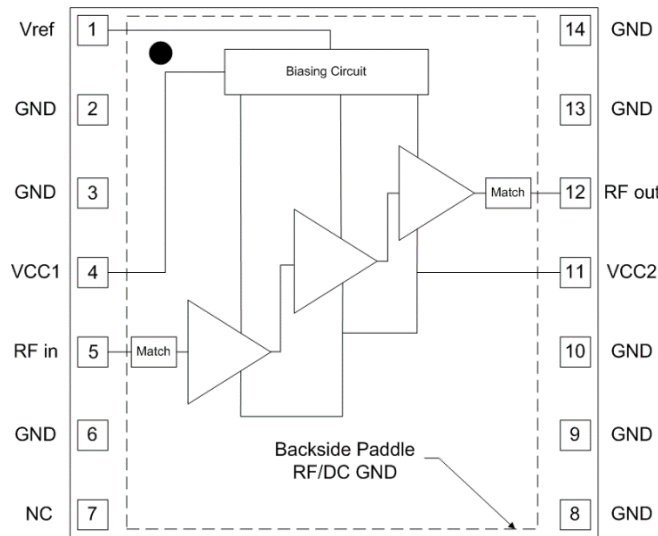
Reference Des.	Value	Description	Manuf.	Part Number
n/a	n/a	Printed Circuit Board		
U1	n/a	High Linearity 0.25 W Power Amplifier	Qorvo	QPA9226
R1	0 Ω	Resistor, Chip, 0603, 5%	various	
C1	0.01 uF	Capacitor, Chip, 0603, 5%	various	
C11	100 pF	Capacitor, Chip, 0603, 5%	various	
C3, C9	0.1 uF	Capacitor, Chip, 0603, 5%	various	
C5, C14	10 uF	Capacitor, Chip, 6032, 10%, Tantalum	various	
C6, C10	1000 pF	Capacitor, Chip, 0603, NPO/COG, 5%	various	

### Performance Plots

Test conditions unless otherwise noted:  $V_{CC1} = V_{CC2} = +4.5V$ ,  $V_{REF} = +2.85V$ ,  $I_{CQ} = 198mA$ ,  $Temp. = +25^\circ C$



### Pin Configuration and Description

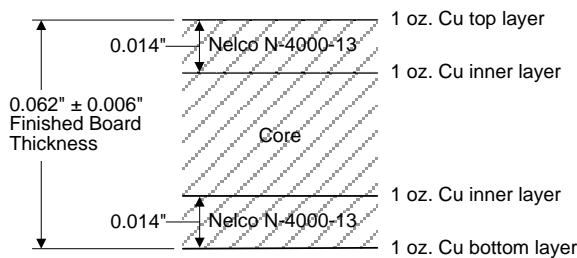


Top View

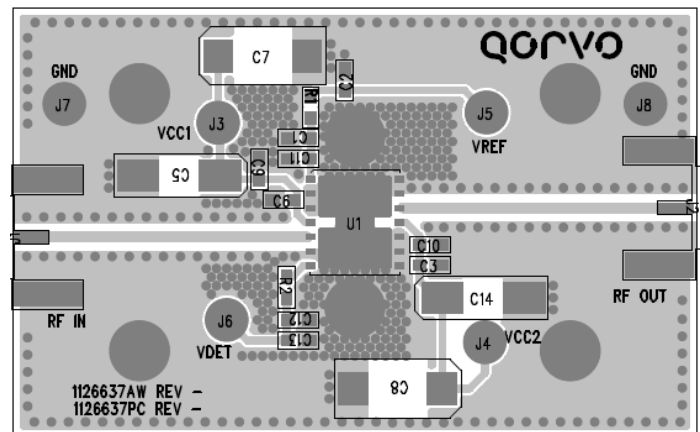
Pad No.	Label	Description
1	V <sub>REF</sub>	Sets the bias current for the amplifiers. It can also be used to power down the device.
2, 3, 6, 8, 9, 10, 13, 14	GND	RF and DC ground.
4	V <sub>CC1</sub>	Voltage supply for the active bias circuitry.
5	RFin	RF input pin. The DC is internally blocked at this pin.
7	NC	No internal connection.
11	V <sub>CC2</sub>	DC voltage supply connection for AMP1, 2, 3.
12	RFout	RF output pin. The DC is internally blocked at this pin.
Backside Paddle	RF/DC GND	RF/DC ground. See PCB Mounting Pattern for suggested footprint.

### Evaluation Board PCB Information

Qorvo PCB 1126637 Material and Stack-up

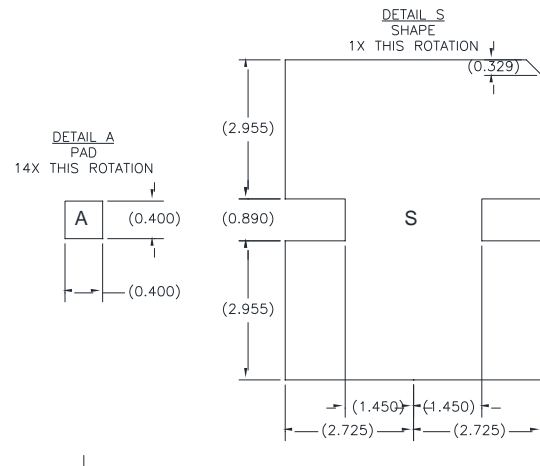
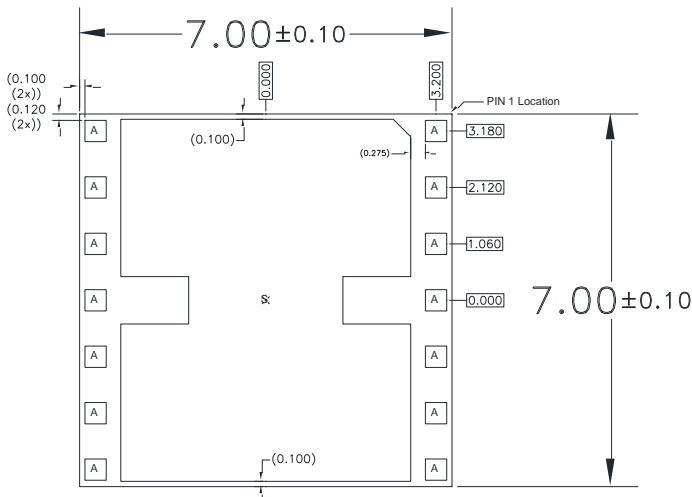
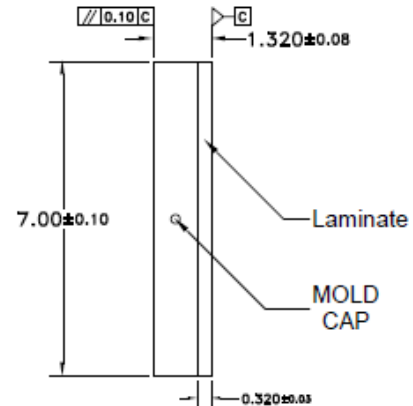
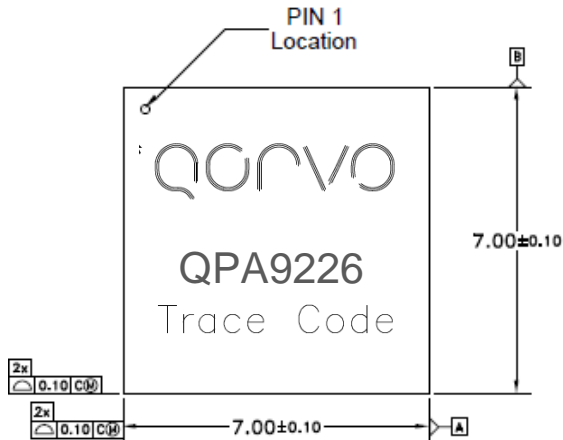


50 ohm line dimensions: width = .028"  
spacing = .028".



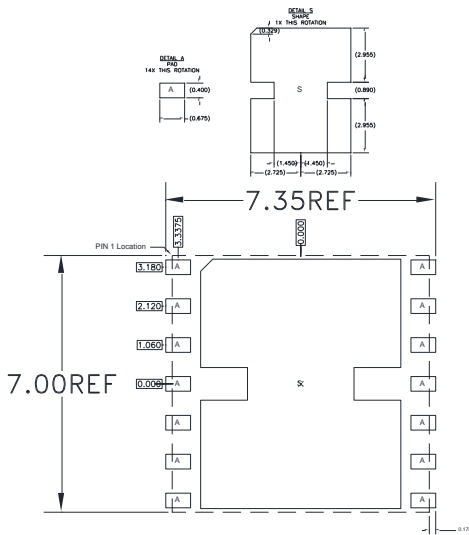
### Package Marking and Dimensions

Marking: Part Number – QPA9226  
Trace Code

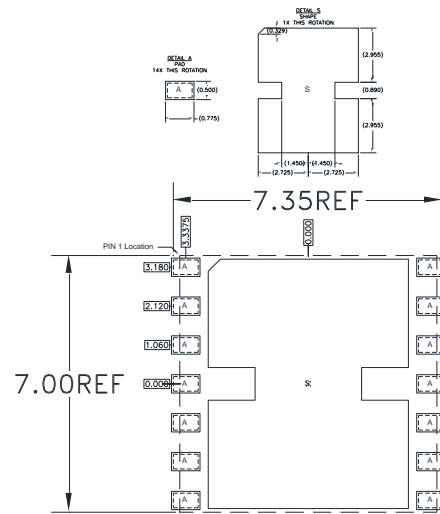


- Notes:
1. All dimensions are in millimeters. Angles are in degrees.
  2. Dimension and tolerance formats conform to ASME Y14.4M-1994.
  3. The terminal #1 identifier and terminal numbering conform to JESD 95-1 SPP-012.

### PCB Mounting Pattern



RECOMMENDED  
LAND PATTERN



RECOMMENDED  
LAND PATTERN MASK

Notes:

1. All dimensions are in millimeters. Angles are in degrees.
2. Use 1 oz. copper minimum for top and bottom layer metal.
3. Vias are required under the backside paddle of this device for proper RF/DC grounding and thermal dissipation. We recommend a 0.35mm (#80/.0135") diameter bit for drilling via holes and a final plated thru diameter of 0.25 mm (0.10").
4. Ensure good package backside paddle solder attach for reliable operation and best electrical performance.

## Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 2	ESDA / JEDEC JS-001-2012
ESD – Charged Device Model (CDM)	Class C3	JEDEC JESD22-C101F
MSL – Moisture Sensitivity Level	Level 3	IPC/JEDEC J-STD-020



Caution!  
ESD-Sensitive Device

## Solderability

Compatible with both lead-free (260°C max. reflow temp.) and tin/lead (245°C max. reflow temp.) soldering processes.

Solder profiles available upon request.

Contact plating: Electrolytic plated Au over Ni

## RoHS Compliance

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment). This product also has the following attributes:

- Product uses RoHS Exemption 7c-I to meet RoHS Compliance requirements.
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C<sub>15</sub>H<sub>12</sub>Br<sub>4</sub>O<sub>2</sub>) Free
- PFOS Free
- SVHC Free

## Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

**Tel: 1-844-890-8163**

**Web: [www.qorvo.com](http://www.qorvo.com)**

**Email: [customer.support@qorvo.com](mailto:customer.support@qorvo.com)**

For technical questions and application information: **Email: [sicapplications.engineering@qorvo.com](mailto:sicapplications.engineering@qorvo.com)**

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