Spatium™ QPB0218N

2 – 18 GHz GaN Power Amplifier

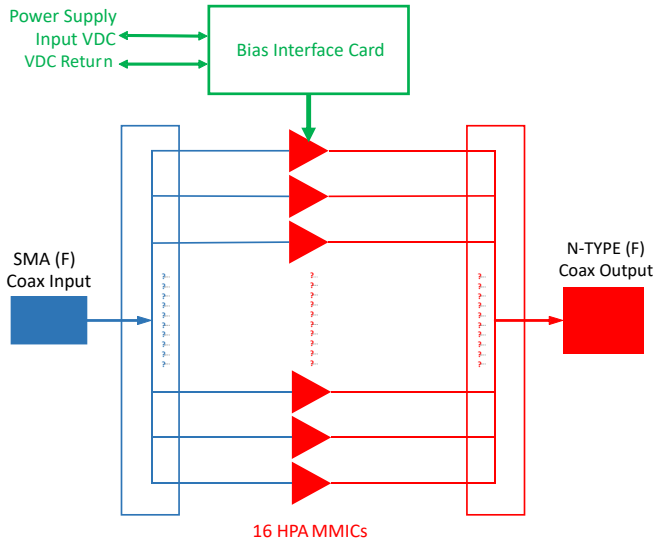
Product Description

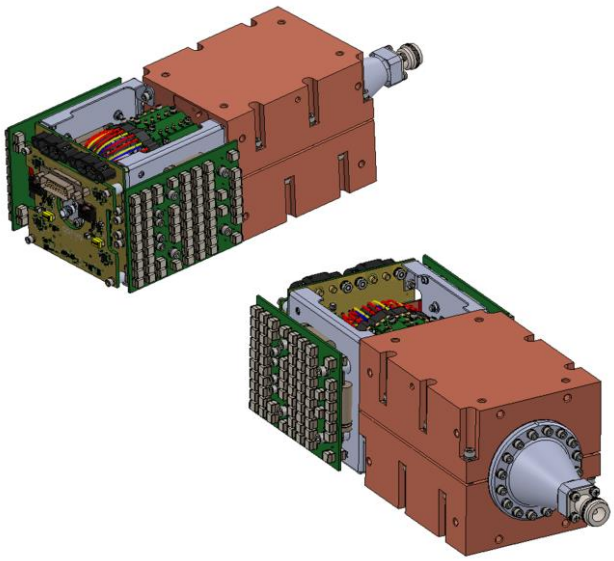
An excellent alternative to traveling wave tube amplifiers, Qorvo's Spatium™ QPB0218N is a solid state, spatial combining amplifier with an operating range of 2–18 GHz. With its maximum performance in output power, gain, power added efficiency, and frequency range, this Spatium is the ideal building block for microwave subsystems with wide-ranging applications.

Qorvo's patented and field-proven Spatium combining technology provides unprecedented Solid-State Power Amplifier (SSPA) performance in a rugged, compact size and weight which reduces total cost of ownership compared to alternative technologies. This product offering combines Qorvo's market leadership in GaN technology and MMIC design along with our high-count combining techniques for a best in class solution to power amplification.

The QPB0218N is equipped with an integrated bias card, which allows for convenience of operation, reducing electrical losses in the bias networks, and weight reduction over using a separate bias card. It provides individualized bias settings for each amplifier blade in the Spatium SSPA as well as drain pulsing up to 1 MHz PRF for superior power savings and noise performance.

Functional Block Diagram





Input (T) and Output (B)

Product Features

- Frequency Range: 2 – 18 GHz
- Saturated Output Power: 51.8 dBm (P_{IN} = 39 dBm)
- Large Signal Gain: 12.8 dB (P_{IN} = 39 dBm)
- Solid State MMIC Reliability
- Multi-Element Redundancy
- Instant On (no warm-up)
- Integrated Bias Card

Performance is typical across frequency. Please reference electrical specification table and data plots for more details.

Applications

- TWTA Replacement

Ordering Information

Part No.	Description
QPB0218N	2 – 18 GHz Spatium™ Amplifier

Absolute Maximum Ratings

Parameter	Value / Unit
Prime Power (V_{DC})*	24 V
Drain Current (I_{D_DRIVE})	40 A
RF Input Power	45 dBm
Operating Temperature*	–40 to +85 °C

Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.

* Rating for thermal reliability

Recommended Operating Conditions

Parameter	Value / Unit
Drain Voltage (V_D)	20 V
Quiescent Drain Current (I_{DQ})	27 A
Operating Drain Current (I_D)	32 A
Operating Temperature **	–40 to +71 °C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

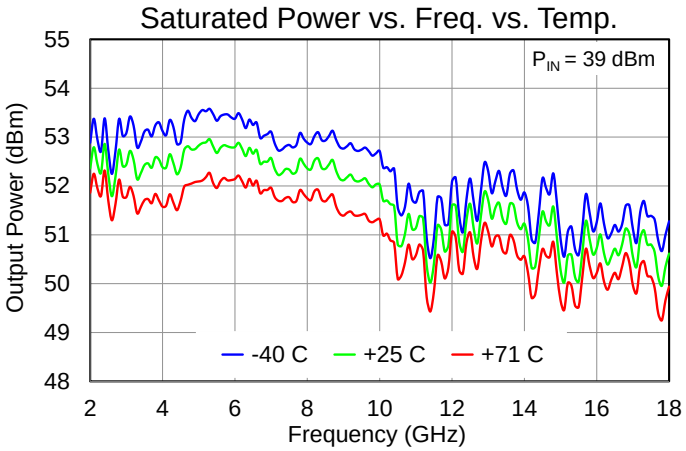
** Refers to outside clamp surface temperature

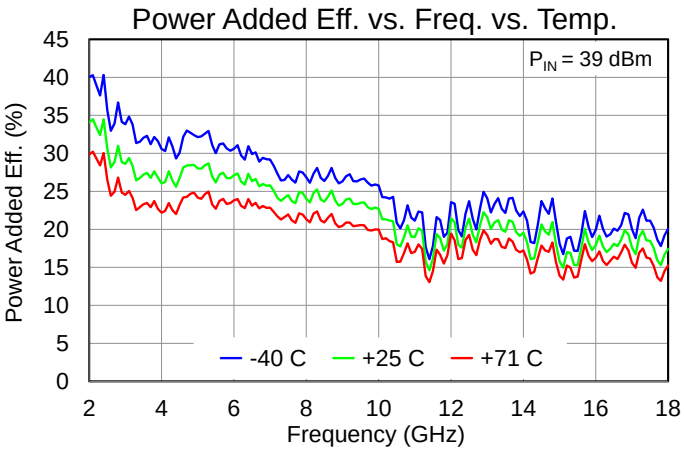
Electrical Specifications

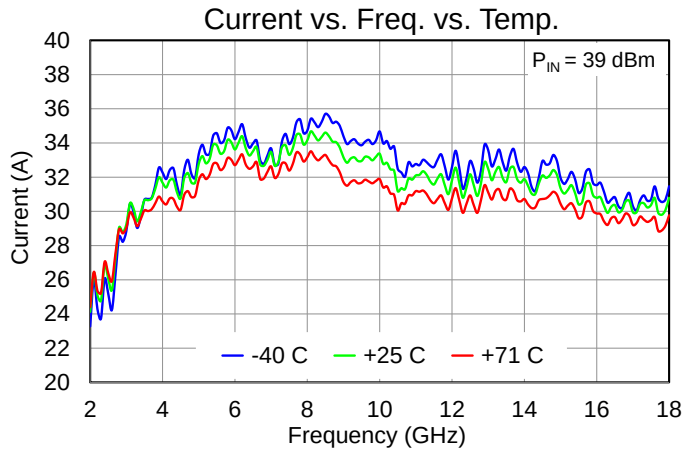
Parameter	Min	Typ	Max	Units
Frequency	2		18	GHz
Saturated P_{OUT}, CW (P_{IN} = 39 dBm)		51.8		dBm
Power-Added Efficiency, CW (P_{IN} = 39 dBm)		22.6		%
Power Gain (CW, P_{IN} = 39 dBm)		12.8		dB
Small Signal Gain (S21)		≥ 17		dB
Input Return Loss		≥ 8		dB
Switching Time (PW=500 ns, F= 10 GHz, P_{IN}=39 dBm)				
ENABLE to RF ON		200		ns
ENABLE to RF OFF		200		ns
Second Harmonic, CW (In band, P_{IN} = 39 dBm)		≤ –17		dBc
Third Harmonic, CW (In band, P_{IN} = 39 dBm)		≤ –11		dBc
Input RF Interface		SMA (F)		
Output RF Interface		Type N (F)		
Weight: Amp + Bias Card		16.5 (7.48)		lbs. (kg)
Amp + Bias Card + One Capacitor Bank		17.0 (7.71)		lbs. (kg)
Amp + Bias Card + Two Capacitor Banks		17.5 (7.94)		lbs. (kg)
Dimensions: Amp + Bias Card (L) x (W) x (H)		11.33 x 3.4 x 3.4 (287.8 x 86.4 x 86.4)		inch (mm)
Amp + Bias Card + One Capacitor Bank		11.33 x 4.1 x 3.4 (287.8 x 104.1 x 86.4)		inch (mm)
Amp + Bias Card + Two Capacitor Banks		11.33 x 4.8 x 3.4 (287.8 x 121.9 x 86.4)		inch (mm)
Test conditions unless otherwise noted: V_{DC} = 20 V, I_{DQ} = 54 A, CW, T = 25 °C				

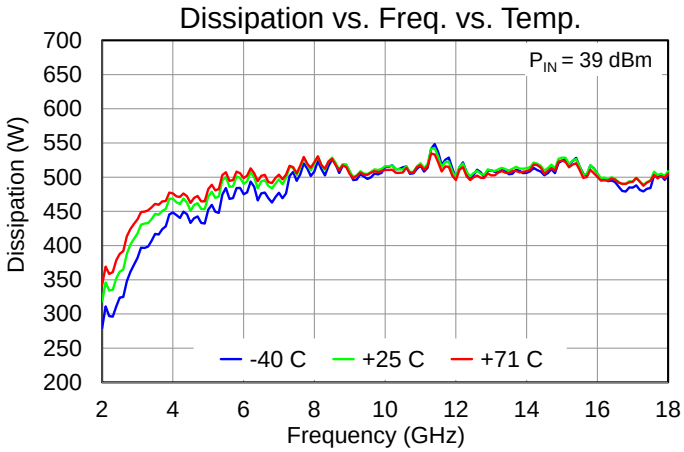
Typical Performance – Large Signal (CW)

Test conditions unless otherwise noted: V_D = 20 V, I_{DQ} = 27 A, T_{CLAMP} = 25 °C, CW Operation. P_{IN} = 39 dBm





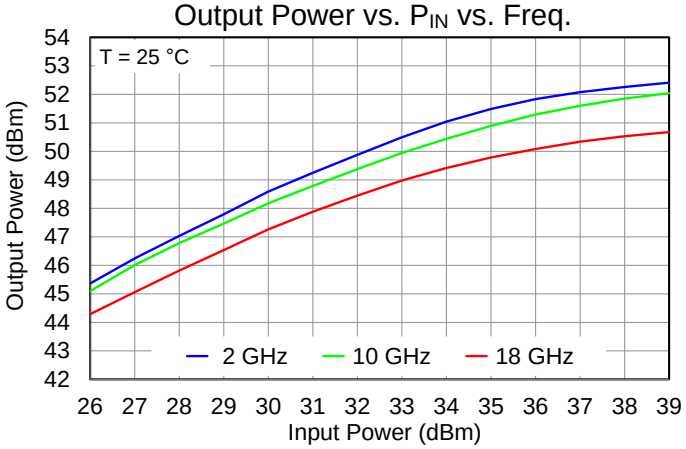




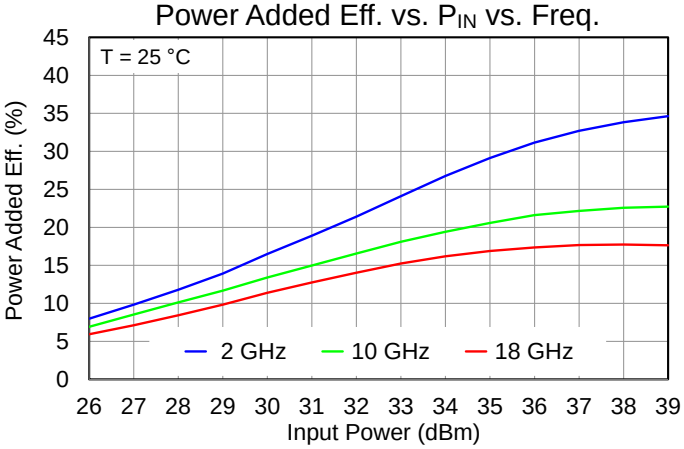
Typical Performance – Large Signal (CW)

Test conditions unless otherwise noted: V_D = 20 V, I_{DQ} = 27 A, T_{CLAMP} = 25 °C, CW Operation

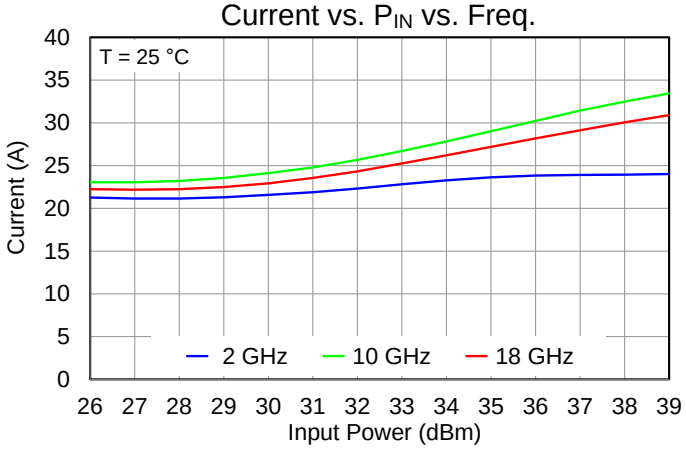
Output Power vs. P_{IN} vs. Freq.



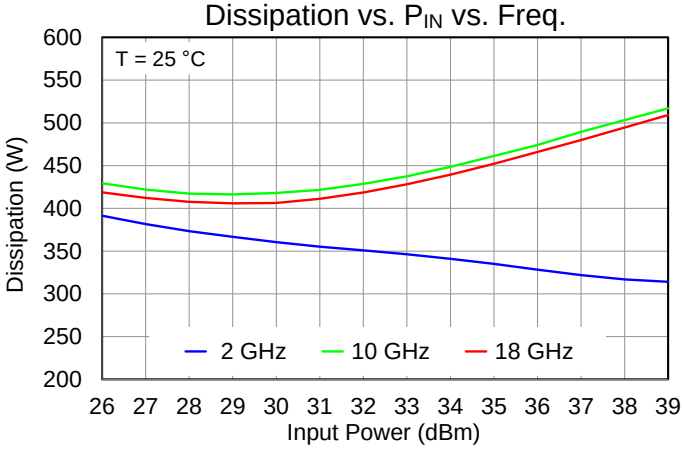
Power Added Eff. vs. P_{IN} vs. Freq.



Current vs. P_{IN} vs. Freq.



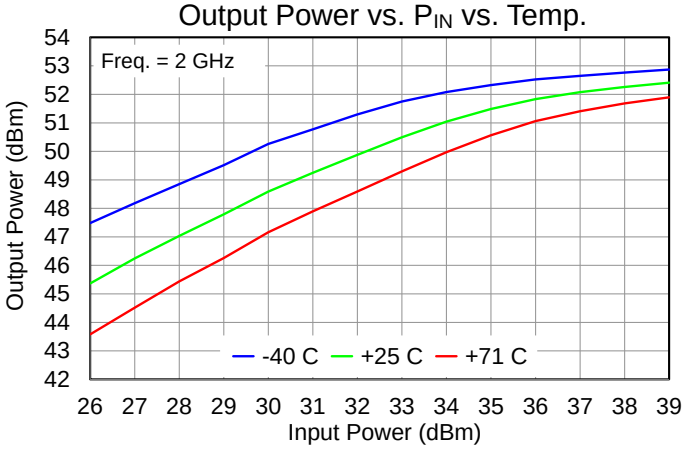
Dissipation vs. P_{IN} vs. Freq.



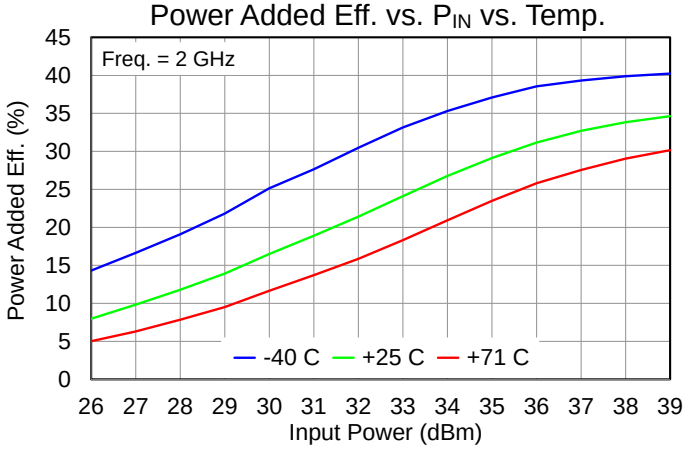
Typical Performance – Large Signal (CW)

Test conditions unless otherwise noted: V_D = 20 V, I_{DQ} = 27 A, T_{CLAMP} = as shown, CW Operation

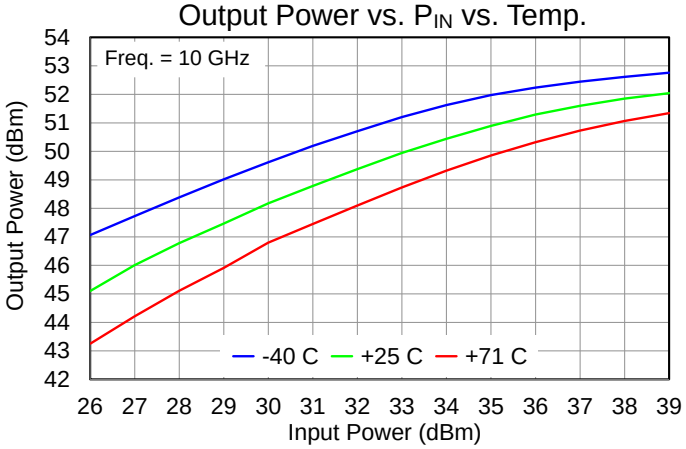
Output Power vs. P_{IN} vs. Temp.



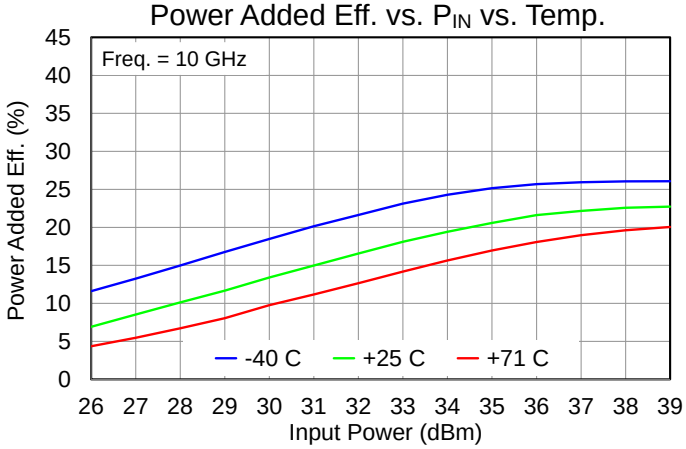
Power Added Eff. vs. P_{IN} vs. Temp.



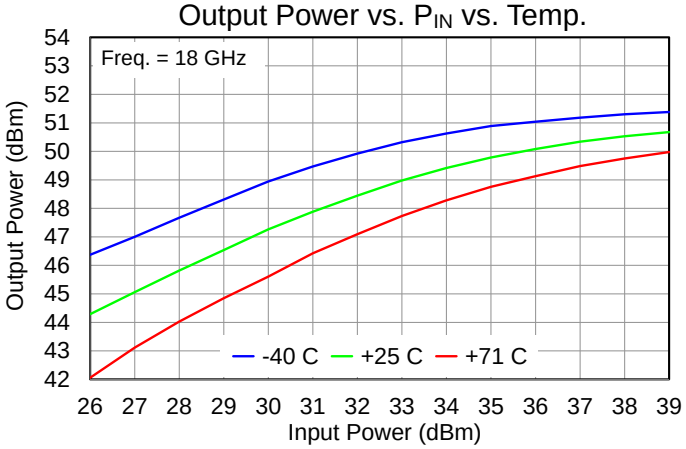
Output Power vs. P_{IN} vs. Temp.



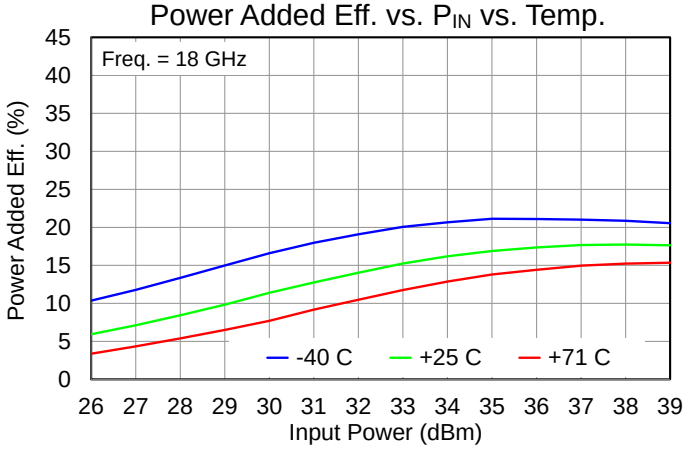
Power Added Eff. vs. P_{IN} vs. Temp.



Output Power vs. P_{IN} vs. Temp.

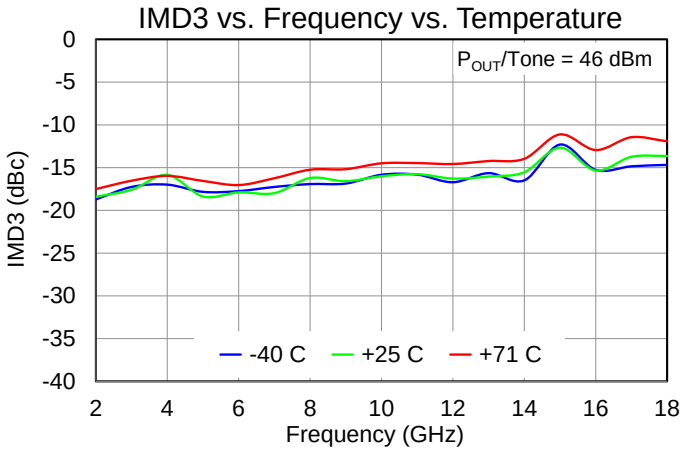


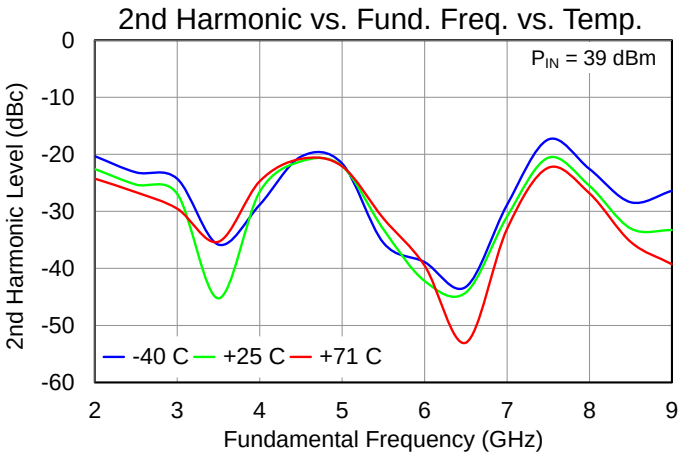
Power Added Eff. vs. P_{IN} vs. Temp.

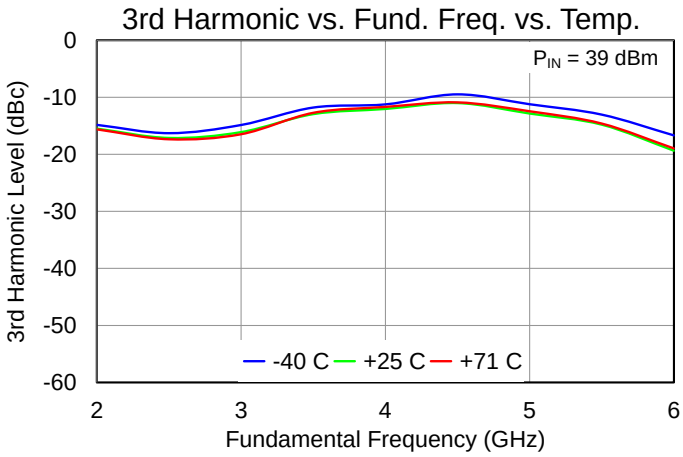


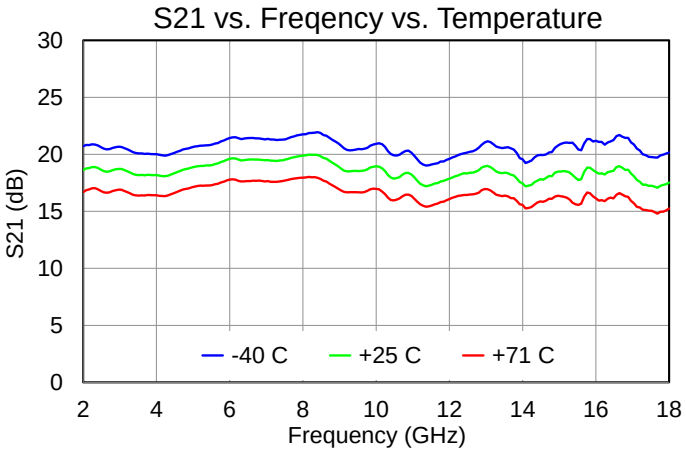
Typical Performance – Linearity, Harmonics, S-Parameters

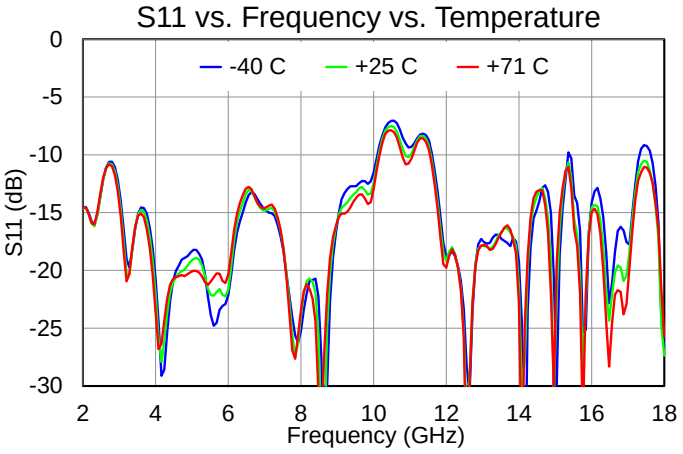
Test conditions unless otherwise noted: V_D = 20 V, I_{DQ} = 27 A, T_{CLAMP} = as shown, CW Operation, Tone Separation = 100 MHz



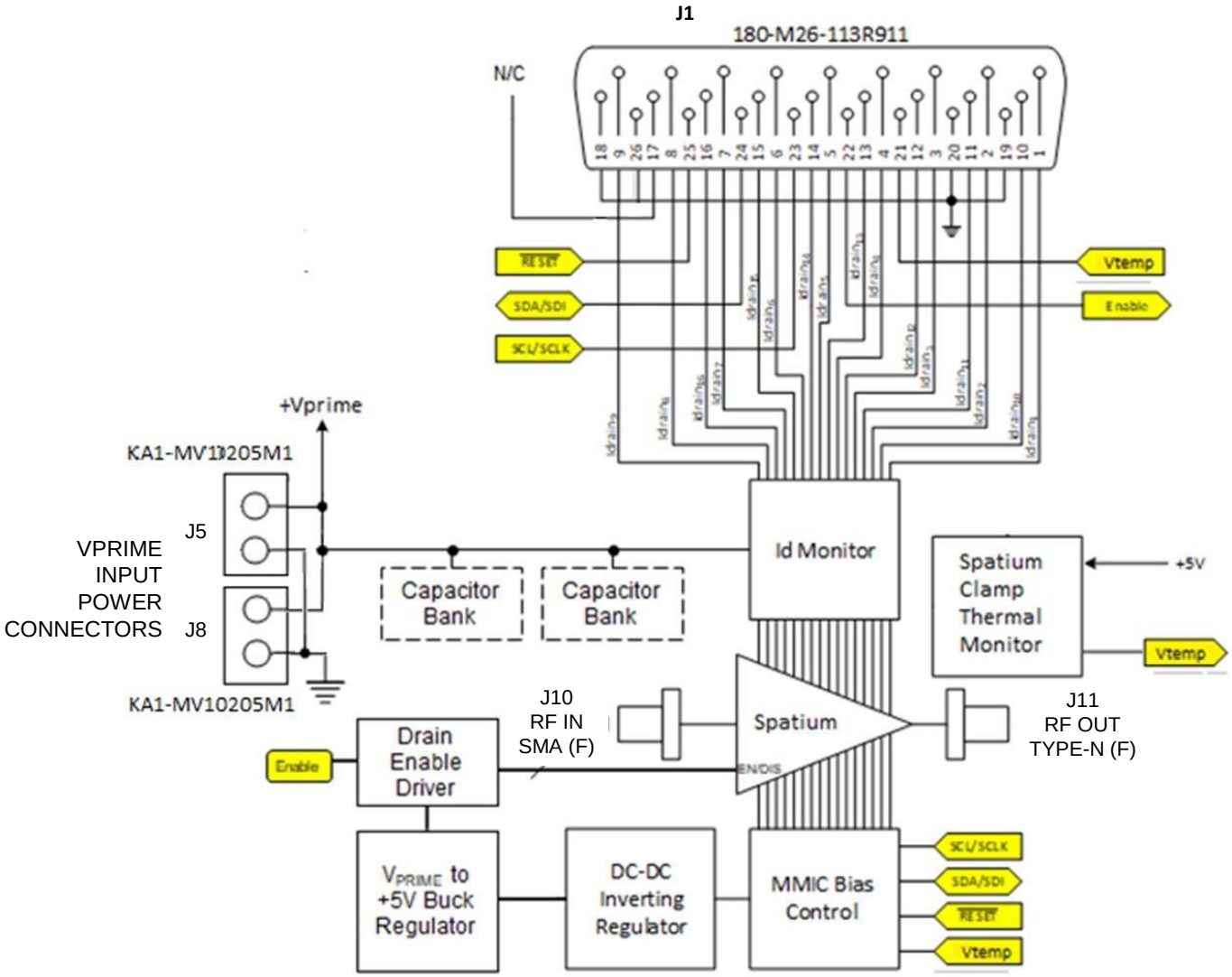






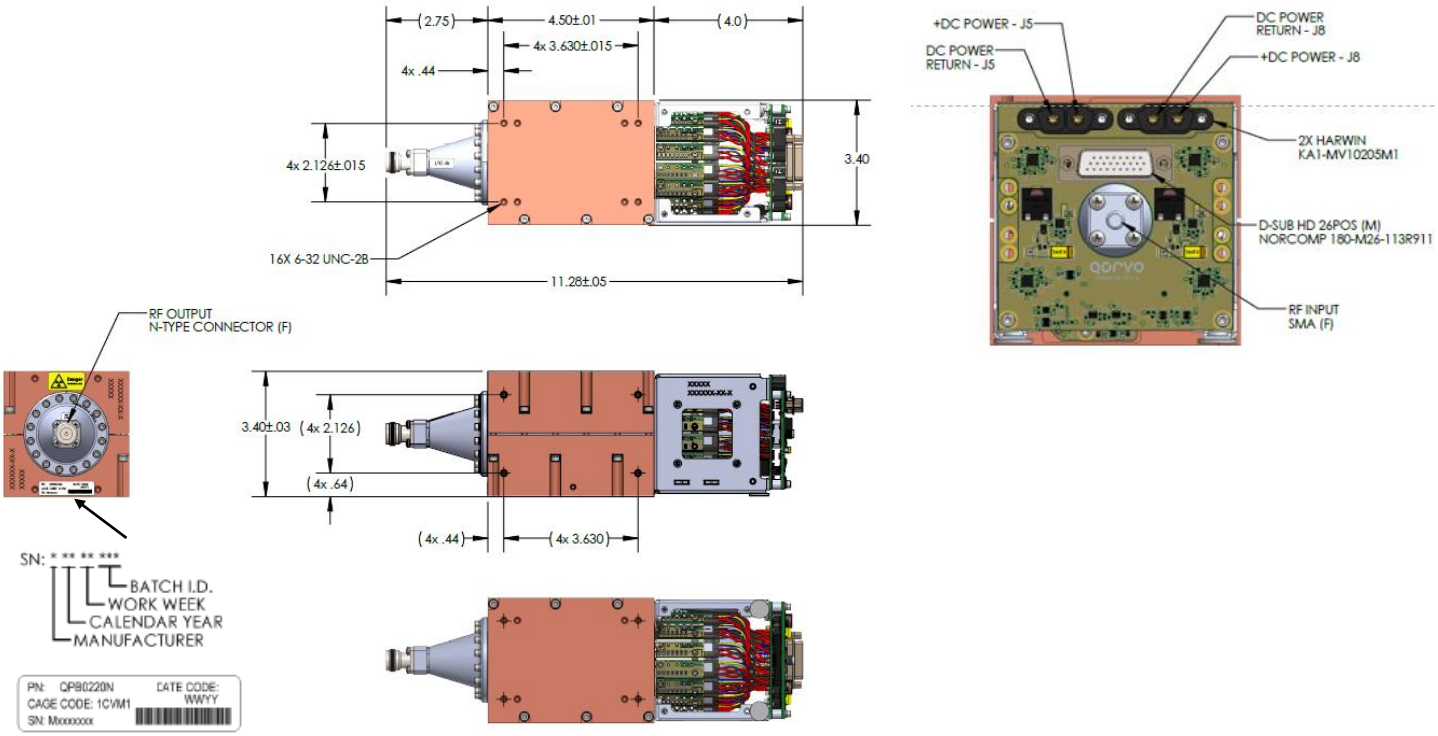


Block Diagram and Description



Pin No.	Label	Description
RF In	J10	SMA (F)
RF Out	J11	TYPE-N (F)
Auxiliary	J1	D-SUB HD 26POS (M), NORCOMP 180-M26-113R911
Power	J5, J8	HARWIN, KA1-MV10205M1

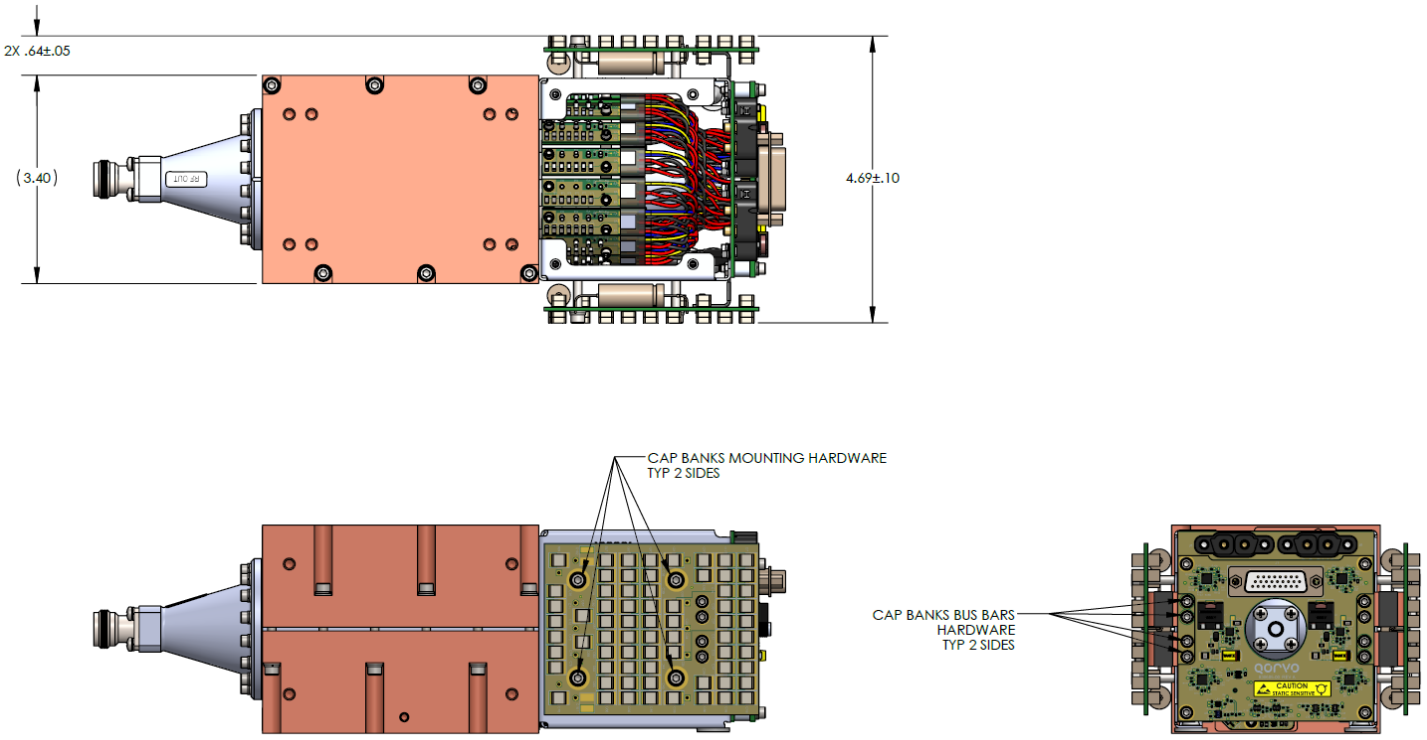
Mechanical Information – Outline Drawing (Amp + Bias Card)



AS CONFIGURED W/O CAPACITOR BANKS

Dimensions are in INCHES

Mechanical Information – Outline Drawing (Amp + Bias Card + 2 Cap. Banks)



AS CONFIGURED WITH TWO CAPACITOR BANKS

Dimensions are in INCHES

Mechanical Information – Bias Card Connector Pins

J1 CONNECTOR PIN FUNCTION AND DEFINITION		
PIN NO.	**FUNCTION**	**DESCRIPTION**
J1-1	DRAIN 1	Voltage output on this pin follows 0.5V/A times the current flowing through channel 1 of the Spatium
J1-2	DRAIN 2	Voltage output on this pin follows 0.5V/A times the current flowing through channel 2 of the Spatium
J1-3	DRAIN 3	Voltage output on this pin follows 0.5V/A times the current flowing through channel 3 of the Spatium
J1-4	DRAIN 4	Voltage output on this pin follows 0.5V/A times the current flowing through channel 4 of the Spatium
J1-5	DRAIN 5	Voltage output on this pin follows 0.5V/A times the current flowing through channel 5 of the Spatium
J1-6	DRAIN 6	Voltage output on this pin follows 0.5V/A times the current flowing through channel 6 of the Spatium
J1-7	DRAIN 7	Voltage output on this pin follows 0.5V/A times the current flowing through channel 7 of the Spatium
J1-8	DRAIN 8	Voltage output on this pin follows 0.5V/A times the current flowing through channel 8 of the Spatium
J1-9	DRAIN 9	Voltage output on this pin follows 0.5V/A times the current flowing through channel 9 of the Spatium
J1-10	DRAIN 10	Voltage output on this pin follows 0.5V/A times the current flowing through channel 10 of the Spatium
J1-11	DRAIN 11	Voltage output on this pin follows 0.5V/A times the current flowing through channel 11 of the Spatium
J1-12	DRAIN 12	Voltage output on this pin follows 0.5V/A times the current flowing through channel 12 of the Spatium
J1-13	DRAIN 13	Voltage output on this pin follows 0.5V/A times the current flowing through channel 13 of the Spatium
J1-14	DRAIN 14	Voltage output on this pin follows 0.5V/A times the current flowing through channel 14 of the Spatium
J1-15	DRAIN 15	Voltage output on this pin follows 0.5V/A times the current flowing through channel 15 of the Spatium
J1-16	DRAIN 16	Voltage output on this pin follows 0.5V/A times the current flowing through channel 16 of the Spatium
J1-17	5V0	5V internally generated reference voltage
J1-18	5V0	5V internally generated reference voltage
J1-19	GND	Connect to logic ground
J1-20	GND	Connect to logic ground
J1-21	VTEMP	Connects to Texas Instruments LMT87 temperature sensor output
J1-22	ENABLE	5V logic command bit to turn on/off the drain voltage leading to each channel of the Spatium. 0V puts the unit into a low-power state while 5V will allow normal operation. In the absence of an external logic signal (open), the amplifier will power on with the application of the supply voltage.
J1-23	SCL	I2C bus used to program amplifier for operation. Please contact Qorvo applications engineering for further information.
J1-24	SDA	I2C bus used to program amplifier for operation. Please contact Qorvo applications engineering for further information.
J1-25	RESET	I2C bus used to program amplifier for operation. Please contact Qorvo applications engineering for further information.
J1-26	GND	Connect to logic ground.
J1-1 through J1-16 can be used for diagnostics / status of MMIC; otherwise, leave open.		
J1-17 and J1-18 can be used to supply up to 100 mA of current if required. Otherwise, leave open. Do not apply a voltage to these pins.		
J1-21 can be used to monitor the reference temperature of the Spatium. For the relationship between the sensor output voltage and temperature, please see the LMT87 datasheet.		

https://www.ti.com/lit/ds/symlink/lmt87.pdf/

Handling Precautions

Caution!
ESD-Sensitive Device

RF VOLTAGE HAZARD: Contact with RF fields at the output connector can cause burns or electric shock. High levels of RF/Microwave energy may be present when the unit is operating.

HIGH DC CURRENT HAZARD: High levels of DC current are present when the unit is operating.

Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

Web: www.qorvo.com
Tel: 1-844-890-8163
Email: customer.support@qorvo.com

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