

R6046FNZ1

Nch 600V 46A Power MOSFET

V_{DSS}	600V
R _{DS(on)} (Max.)	0.098Ω
I _D	46A
P_D	694W

Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V_{GSS}) guaranteed to be $\pm 30V$.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.
- 6) Pb-free lead plating; RoHS compliant

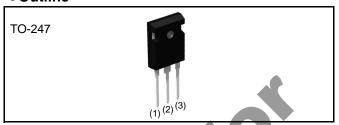
Application

Switching Power Supply

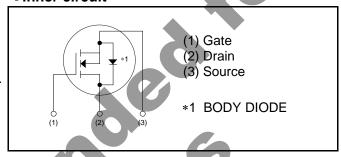
● Absolute maximum ratings(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	600	V
Continuous drain current T _c = 25°C	I _D *1	±46	А
Continuous diam current $T_c = 100^{\circ}C$	I _D *1	±23	А
Pulsed drain current	I _{D,pulse} *2	±115	А
Gate - Source voltage	V_{GSS}	±30	V
Avalanche energy, single pulse	E _{AS} *3	142	mJ
Avalanche energy, repetitive	E _{AR} *4	5.4	mJ
Avalanche current	I _{AR} *3	23	А
Power dissipation $(T_c = 25^{\circ}C)$	P _D	694	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C
Reverse diode dv/dt	dv/dt *5	15	V/ns

Outline



●Inner circuit



or ackaging specifications					
	Packaging	Tube			
	Reel size (mm)	-			
Type	Tape width (mm)	-			
Туре	Basic ordering unit (pcs)	450			
~(2	Taping code	C9			
	Marking	R6046FNZ1			

Absolute maximum ratings

Parameter	Symbol	Conditions	Values	Unit
Drain - Source voltage slope	dv/dt	$V_{DS} = 480V, I_{D} = 46A$ $T_{j} = 125^{\circ}C$	50	V/ns

●Thermal resistance

Parameter	Symbol	Values			Unit
- Farameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	S	0.18	°C/W
Thermal resistance, junction - ambient	R _{thJA}		,	30	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}		-	265	°C

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	600	-	-	V
Drain - Source avalanche breakdown voltage	V _{(BR)DS}	$V_{GS} = 0V, I_D = 46A$	-	700	1	V
		$V_{DS} = 600V, V_{GS} = 0V$				^
Zero gate voltage drain current	I _{DSS}	$T_j = 25^{\circ}C$	-	1	100	μΑ
		T _j = 125°C	-	ı	100	mA
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	ı	±100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V$, $I_D = 1mA$	3	1	5	V
		$V_{GS} = 10V, I_D = 23A$				
Static drain - source on - state resistance	R _{DS(on)} *6	T _j = 25°C	-	0.075	0.098	Ω
2.5.02.02.02.00		T _j = 125°C	-	0.16	-	
Gate input resistance	R _G	f = 1MHz, open drain	-	1.7	-	Ω

●Electrical characteristics(T_a = 25°C)

Darameter	Parameter Symbol Conditions —			Values		Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Transconductance	g _{fs} *6	$V_{DS} = 10V, I_{D} = 23A$	15	31	-	S
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	6230	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	4000		pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	85		
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$	-	175	-	
Effective output capacitance, time related	C _{o(tr)}	$V_{DS} = 0V \text{ to } 480V$	C	596	-	pF
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 300V$, $V_{GS} = 10V$	<u> </u>	73	-	
Rise time	t _r *6	I _D = 23A	-	120	-	20
Turn - off delay time	t _{d(off)} *6	$R_L = 13.0\Omega$		240	480	ns
Fall time	t _f *6	$R_G = 10\Omega$		68	136	

●Gate Charge characteristics(T_a = 25°C)

Parameter	Symbol Conditions -		Values			Unit
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*6}	V _{DD} ≃ 300V	-	150	ı	
Gate - Source charge	Q _{gs} *6	$I_D = 46A$	-	40	1	nC
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	65	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 300V$, $I_D = 46A$	-	7.1	-	V

^{*1} Limited only by maximum temperature allowed.

^{*2} $P_W \le 10\mu s$, Duty cycle $\le 1\%$

^{*3} L $^{\simeq}$ 500 μ H, V_{DD} = 50V, R_{G} = 25 Ω , starting T_{j} = 25°C

^{*4} L $^{\simeq}$ 500 μ H, V_{DD} = 50V, R_G = 25 Ω , starting T_j = 25°C, f = 10kHz

^{*5} Reference measurement circuits Fig.5-1.

^{*6} Pulsed

ullet Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol	Symbol Conditions -		Symbol Conditions Values			Unit
r ai ai ii etei	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Inverse diode continuous, forward current	l _S *1	T _c = 25°C	-	,	46	A	
Inverse diode direct current, pulsed	I _{SM} *2	11 _c = 25 G	'	-	115	А	
Forward voltage	V _{SD} *6	$V_{GS} = 0V, I_{S} = 46A$	-	-	1.5	V	
Reverse recovery time	t _{rr} *6		- (145	ı	ns	
Reverse recovery charge	Q _{rr} *6	I _S = 46A di/dt = 100A/us		0.74	-	μС	
Peak reverse recovery current	I _{rrm} *6		1	9.6	ı	Α	
Peak rate of fall of reverse recovery current	di _{rr} /dt	T _j = 25°C	-	1200	-	A/μs	

●Typical Transient Thermal Characteristics

Symbol	Value	Unit
R _{th1}	0.055	
R _{th2}	0.164	K/W
R _{th3}	0.821	

Symbol	Value	Unit
C _{th1}	0.0236	
C _{th2}	0.134	Ws/K
C_{th3}	1.09	

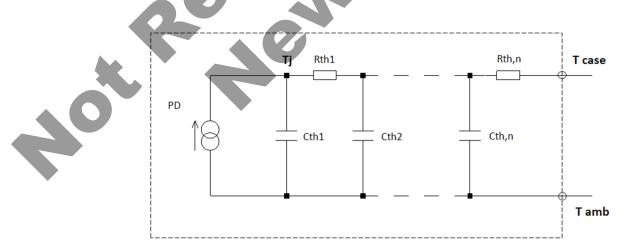


Fig.1 Power Dissipation Derating Curve

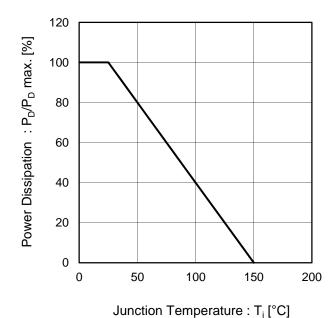
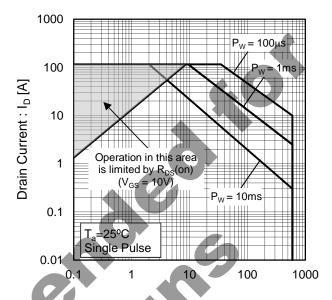
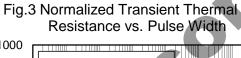
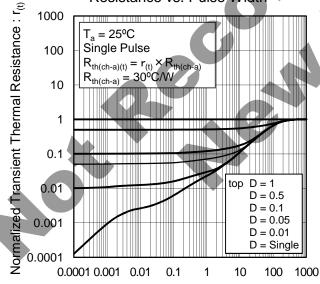


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]





Pulse Width : P_W [s]

Fig.4 Avalanche Current vs Inductive Load

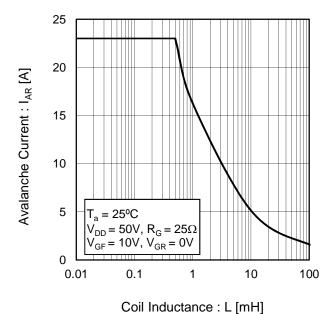
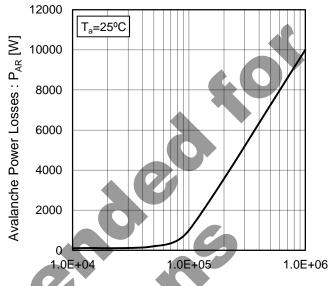


Fig.5 Avalanche Power Losses



Frequency: f [Hz]

Fig.6 Avalanche Energy Derating Curve vs Junction Temperature

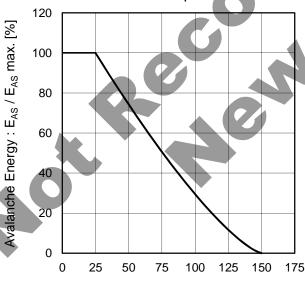
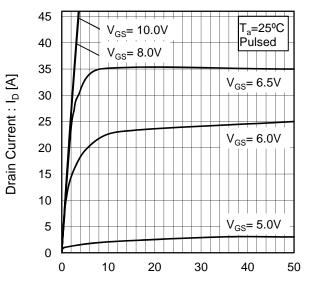
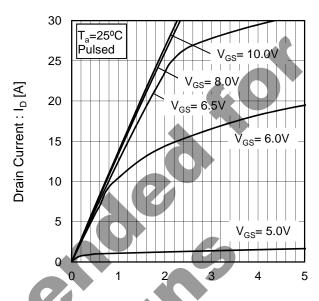


Fig.7 Typical Output Characteristics(I)

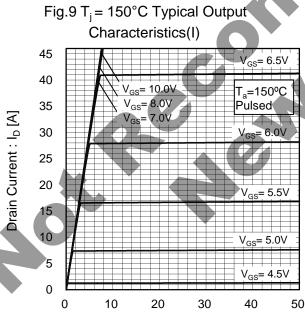


Drain - Source Voltage : V_{DS} [V]

Fig.8 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]



Drain - Source Voltage : V_{DS} [V]

Fig. 10 $T_i = 150^{\circ}$ C Typical Output Characteristics(II) 30 V_{GS}= 10.0V T_a=150°C V_{GS}= 8.0V Pulsed 25 $V_{GS} = 7.0V$ V_{GS}= 6.5V Drain Current : I_D [A] 20 V_{GS}= 6.0V V_{GS}= 5.5V 15 10 $V_{GS} = 5.0V$ 5 V_{GS}= 4.5V 0 2 1 0 3 5

Drain - Source Voltage : V_{DS} [V]

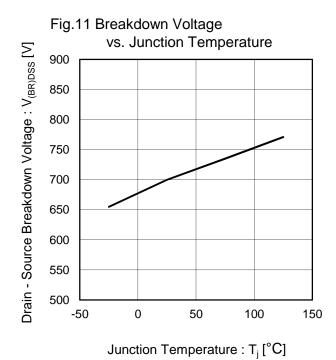
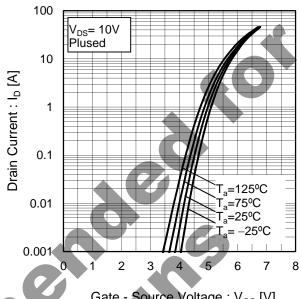


Fig.12 Typical Transfer Characteristics



Gate - Source Voltage : V_{GS} [V]

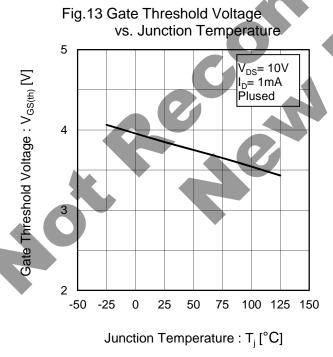


Fig.14 Transconductance vs. Drain Current

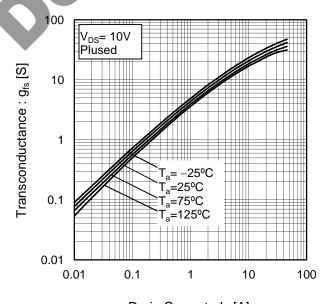
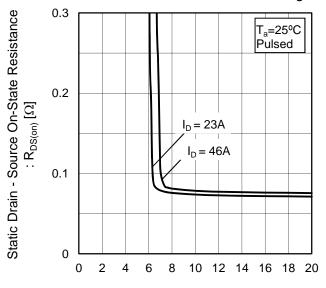
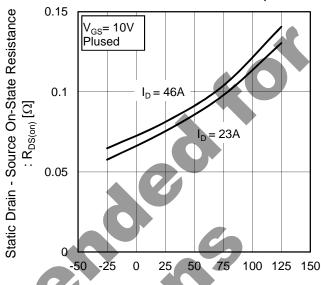


Fig.15 Static Drain - Source On - State Resistance vs. Gate Source Voltage



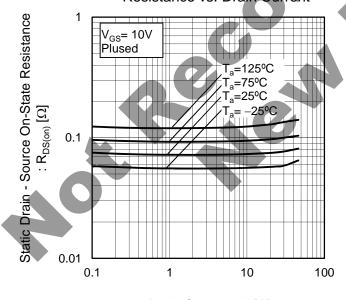
Gate - Source Voltage : V_{GS} [V]

Fig.16 Static Drain - Source On - State
Resistance vs. Junction Temperature



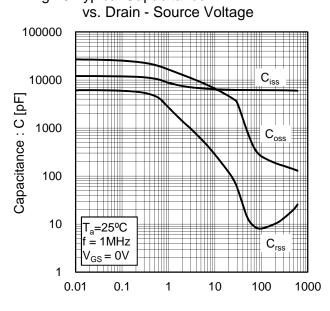
Junction Temperature : T_j [°C]

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current



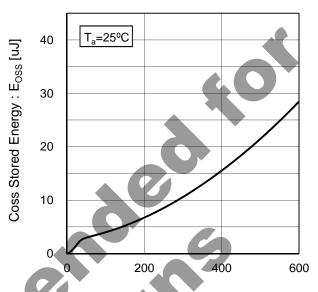
Drain Current : I_D [A]

Fig.18 Typical Capacitance



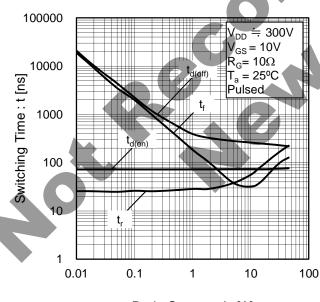
Drain - Source Voltage : V_{DS} [V]

Fig.19 Coss Stored Energy



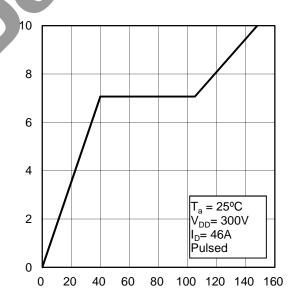
Drain - Source Voltage : V_{DS} [V]

Fig.20 Switching Characteristics



Drain Current : I_D [A]

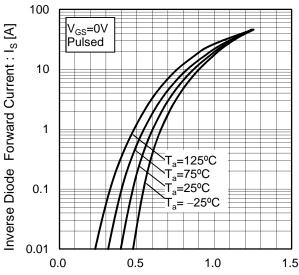
Fig.21 Dynamic Input Characteristics



Total Gate Charge : Q_g [nC]

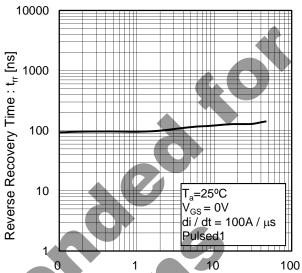
Gate - Source Voltage : V_{GS} [V]

Fig.22 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Fig.23 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

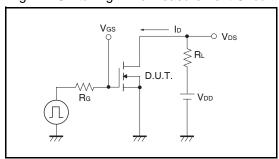


Fig.2-1 Gate Charge Measurement Circuit

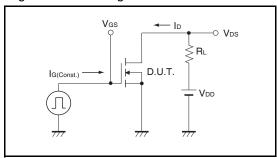


Fig.3-1 Avalanche Measurement Circuit

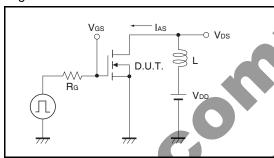


Fig.4-1 dv/dt Measurement Circuit

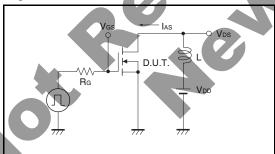


Fig.5-1 di/dt Measurement Circuit

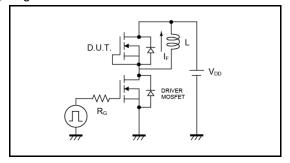


Fig.1-2 Switching Waveforms

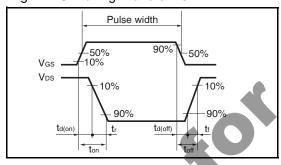


Fig.2-2 Gate Charge Waveform

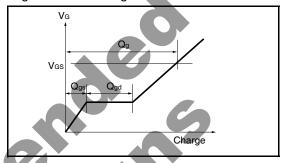


Fig.3-2 Avalanche Waveform

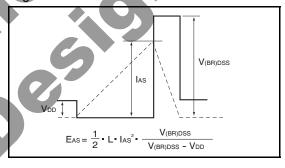


Fig.4-2 dv/dt Waveform

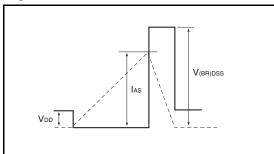
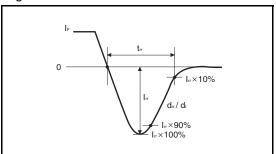
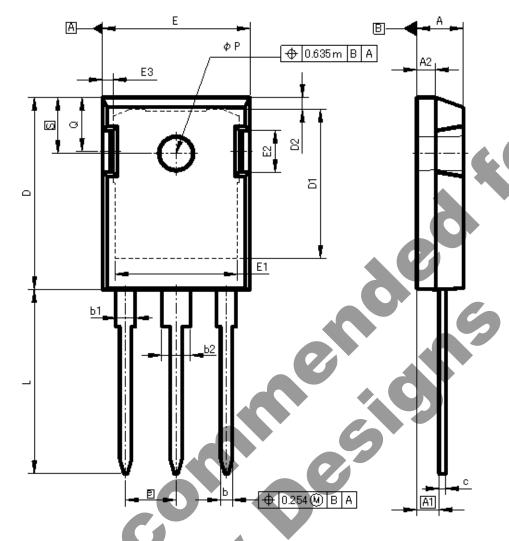


Fig.5-2 di/dt Waveform



●Dimensions (Unit : mm)

TO-247



DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A	4.83	5.21	0.190	0.205
A1	2.29	2.54	0.090	0.100
A2	1.91	2.16	0.075	0.085
b	1.14	1.40	0.045	0.055
b1	1.91	2.20	0.075	0.087
b2	2.92	3.20	0.115	0.126
С	0.61	0.80	0.024	0.031
D	20.80	21.34	0.819	0.840
D1	17.43	17.83	0.686	0.702
E	15.75	16.13	0.620	0.635
е	5.45		0.2	15
N	3.00		3.0	00
L	19.81	20.57	0.780	0.810
L1	3.81	4.32	0.150	0.170
ФР	3.55	3.65	0.140	0.144
Q	5.59	6.20	0.220	0.244
S	6.	15	0.2	40

Dimension in mm / inches

Rev.003

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JAPAN	USA	EU	CHINA
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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power, exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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